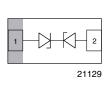
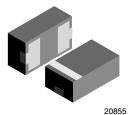


# Bidirectional Symmetrical (BiSy) Single Line ESD-Protection Diode in LLP1006-2L





#### **MARKING** (example only)



Bar = pin 1marking X = date code

Y = type code (see table below)

#### **FEATURES**

- Ultra compact LLP1006-2L package
- Low package profile < 0.4 mm
- 1-line ESD-protection
- Working range ± 7 V
- Low leakage current I<sub>R</sub> < 0.1 μA</li>
- Low load capacitance C<sub>D</sub> = 14 pF
- ESD-protection acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge





COMPLIANT GREEN

- <u>(5-2008)</u>
- Soldering can be checked by standard vision inspection; no X-ray necessary
- Pin plating NiPdAu (e4) no whisker growth
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

ORDERING INFORMATION				
DEVICE NAME ORDERING CODE		TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY	
VCUT07B1-HD1	VCUT07B1-HD1-G4-08	8000	8000	

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VCUT07B1-HD1	LLP1006-2L	U	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	TEST CONDITIONS SYMBOL		UNIT		
Peak pulse current	acc. IEC 61000-4-5; t <sub>p</sub> = 8/20 μs; single shot	I <sub>PPM</sub>	4	Α		
Peak pulse power	Pin 1 to pin 2 acc. IEC 61000-4-5; t <sub>p</sub> = 8/20 μs; single shot	P <sub>PP</sub>	60	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C		
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C		

### **ELECTRICAL CHARACTERISTICS** (pin 1 to pin 2 or pin 2 to pin1)

(T<sub>amb</sub> = 25 °C, unless otherwise specified)

(Tamb = 26 G, unicos ourierwise specimed)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines	
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	ı	-	7	V	
Reverse voltage	at I <sub>R</sub> = 0.1 μA	$V_R$	7	-	-	V	
Reverse current	at V <sub>RWM</sub> = 7 V	I <sub>R</sub>	-	-	0.1	μΑ	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	7.3	-	-	V	
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V	-	9	12	V	
	at I <sub>PP</sub> = I <sub>PPM</sub> = 4 A	V <sub>C</sub>	-	-	15	V	
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>		14	16	pF	
	at $V_R = 2.5 \text{ V}$ ; $f = 1 \text{ MHz}$	$C_D$		11	-	pF	

#### **CUT THE SPIKES WITH VCUT07B1-HD1:**

The VCUT07B1-HD1 is a bidirectional and symmetrical (BiSy) ESD-protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT07B1-HD1 offers a high isolation (low leakage current, low capacitance) within the specified working range. Due to the short leads and small package size of the tiny LLP1006-2L package the line inductance is very low, so that fast transients like an ESD-strike can be clamped with minimal over- or undershoots.

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

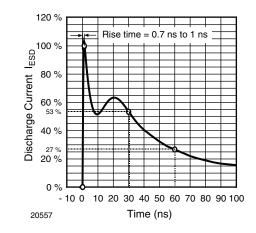


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$ /150 pF)

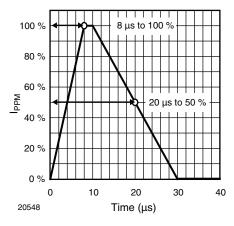


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

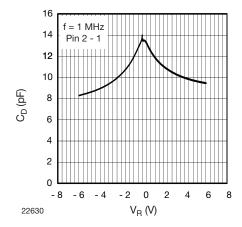


Fig. 3 - Typical Capacitance C<sub>D</sub> vs. Reverse Voltage V<sub>R</sub>

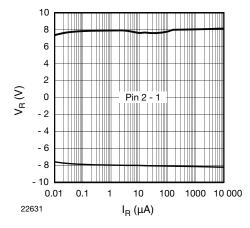


Fig. 4 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

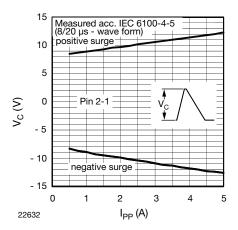


Fig. 5 - Typical Peak Clamping Voltage  $V_{\rm C}$  vs. Peak Pulse Current I\_{PP}

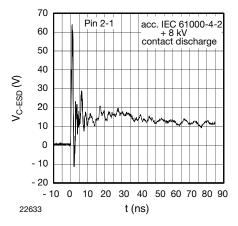


Fig. 6 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

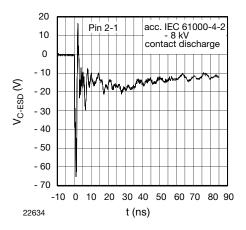


Fig. 7 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

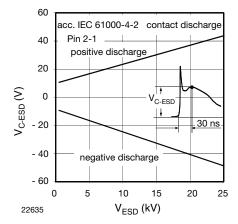
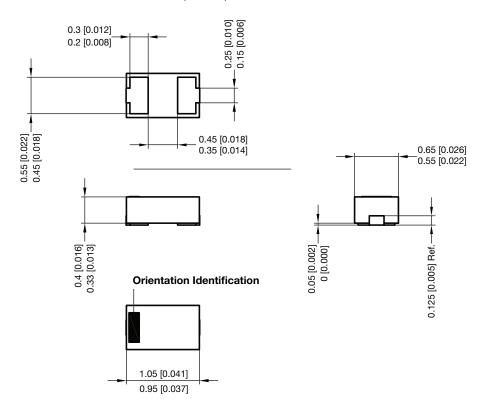
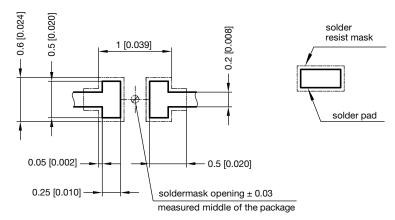


Fig. 8 - Typical Peak. Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

### PACKAGE DIMENSIONS in millimeters (Inches): LLP1006-2L



### Foot print recommendation:



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