



PHOTOCOUPLER

PS8502,PS8502L1,PS8502L2,PS8502L3

HIGH SPEED ANALOG OUTPUT TYPE 8 mm CREEPAGE 8-PIN PHOTOCOUPLER

-NEPOC Series-

DESCRIPTION

The PS8502L1, PS8502L2 and PS8502L3 are 8-pin high speed photocouplers containing a GaAlAs LED on input side and a PN photodiode and a high speed amplifier transistor on output side on one chip. The PS8502 is in a plastic DIP (Dual In-line Package) with 8 mm creepage distance product.

The PS8502L1 is lead bending type for long creepage distance.

The PS8502L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

The PS8502L3 is lead bending type (Gull-wing) for surface mounting.

FEATURES

- Long creepage distance (8 mm MIN.: PS8502L1, PS8502L2)
- High common mode transient immunity (CMH, CML = ± 15 kV/ μ s MIN.)
- High supply voltage (Vcc = 35 V MAX.)
- High speed response (tphL, tpLH = $0.8 \mu s$ MAX.)
- High isolation voltage (BV = 5 000 Vr.m.s.)
- TTL, CMOS compatible with a resistor
- Ordering number of tape product: PS8502L2-E3: 1 000 pcs/reel

: PS8502L3-E3: 1 000 pcs/reel

Pb-Free product

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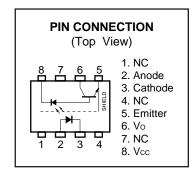
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- Safety standards
 - UL approved: No. E72422
 - BSI approved: No. 8937, 8938
 - SEMKO approved: No. 615433
 - NEMKO approved: No. P06207243
 - DEMKO approved: No. 314091
 - FIMKO approved: No. FI 22827
 - DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40019182 (Option)

CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)

APPLICATIONS

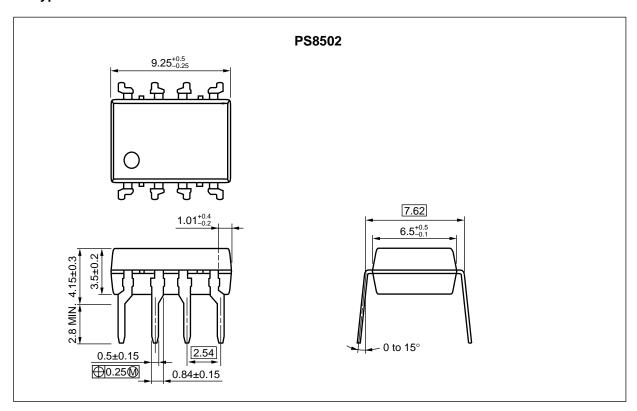
- · Interface for measurement or control equipment
- · Substitutions for relays and pulse transformers
- · Modem, communications device
- General purpose inverter



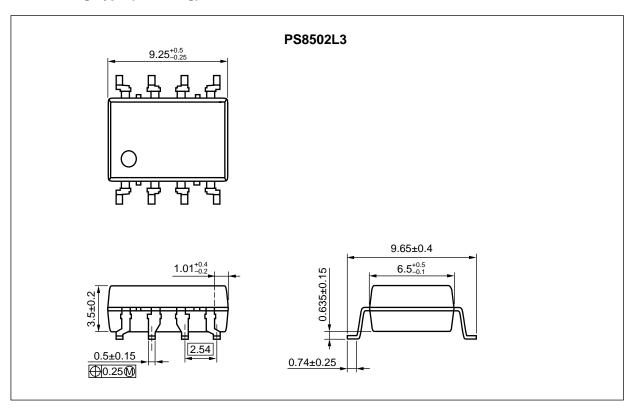
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<R> PACKAGE DIMENSIONS (UNIT: mm)

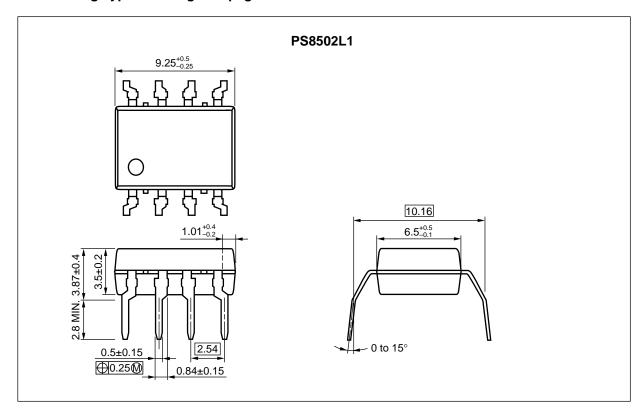
DIP Type



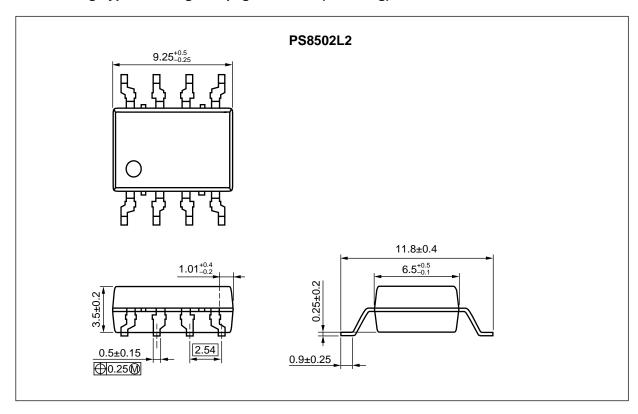
Lead Bending Type (Gull-wing) For Surface Mount



Lead Bending Type For Long Creepage Distance



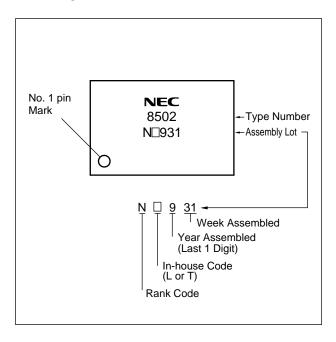
Lead Bending Type For Long Creepage Distance (Gull-wing) For Surface Mount



PHOTOCOUPLER CONSTRUCTION

Parameter	PS8502, PS8502L3	PS8502L1, PS8502L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

<R> MARKING EXAMPLE



ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS8502	PS8502-AX	Pb-Free	Magazine case 50 pcs	Standard products	PS8502
PS8502L1	PS8502L1-AX	(Ni/Pd/Au)		(UL, CSA, BSI,	PS8502L1
PS8502L2	PS8502L2-AX			SEMKO, NEMKO,	PS8502L2
PS8502L3	PS8502L3-AX			DEMKO, FIMKO	PS8502L3
PS8502L2-E3	PS8502L2-E3-AX		Embossed Tape 1 000 pcs/reel	approved)	PS8502L2
PS8502L3-E3	PS8502L3-E3-AX				PS8502L3
PS8502-V	PS8502-V-AX		Magazine case 50 pcs	DIN EN60747-5-2	PS8502
PS8502L1-V	PS8502L1-V-AX			(VDE0884 Part2)	PS8502L1
PS8502L2-V	PS8502L2-V-AX			Approved (Option)	PS8502L2
PS8502L3-V	PS8502L3-V-AX				PS8502L3
PS8502L2-V-E3	PS8502L2-V-E3-AX		Embossed Tape 1 000 pcs/reel		PS8502L2
PS8502L3-V-E3	PS8502L3-V-E3-AX				PS8502L3

^{*1} For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current *1	lF	25	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	35	V
	Output Voltage	Vo	35	V
	Output Current	lo	8	mA
	Power Dissipation ²	Pc	100	mW
Isolation	Voltage *3	BV	5 000	Vr.m.s.
Operating	g Ambient Temperature	TA	-55 to +100	°C
Storage Temperature		T _{stg}	-55 to +125	°C

^{*1} Reduced to 0.33 mA/ $^{\circ}$ C at T_A = 70 $^{\circ}$ C or more.

^{*2} Reduced to 2.0 mW/ $^{\circ}$ C at T_A = 75 $^{\circ}$ C or more.

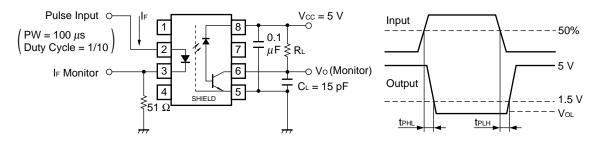
^{*3} AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-4 shorted together, 5-8 shorted together.

ELECTRICAL CHARACTERISTICS (TA = 25°C)

	Parameter	Symbol	Conditions	MIN.	TYP. ^{*1}	MAX.	Unit
Diode	Forward Voltage	VF	I _F = 16 mA		1.7	2.2	V
	Reverse Current	lR	V _R = 3 V			10	μΑ
	Forward Voltage Temperature Coefficent	⊿Vf/⊿Ta	IF = 16 mA		-2.1		mV/°C
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz		30		pF
Detector	High Level Output Current	Іон (1)	$I_F = 0 \text{ mA}, V_{CC} = V_0 = 5.5 \text{ V}$		3	500	nA
	High Level Output Current	Іон (2)	IF = 0 mA, Vcc = Vo = 35 V			100	μΑ
	Low Level Output Voltage	Vol	IF = 16 mA, Vcc = 4.5 V, Io = 2.4 mA		0.15	0.4	V
-	Low Level Supply Current	Iccl	IF = 16 mA, Vo = Open, Vcc = 35 V		150		μΑ
	High Level Supply Current	Іссн	IF = 0 mA, Vo = Open, Vcc = 35 V		0.01	1	μΑ
Coupled	upled Current Transfer Ratio CTR I _F = 16 mA, Vcc = 4.5 V, Vo = 0.4 V	15			%		
	Isolation Resistance	Rı-o	Vi-o = 1 kVdc	10 ¹¹			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1 MHz		0.01		pF
	Propagation Delay Time $(H \to L)^{'2}$	tрнL	I _F = 16 mA, Vcc = 5 V, R _L = 1.9 k Ω		0.22	0.8	μs
	Propagation Delay Time $(L \rightarrow H)^2$	tрLH	I _F = 16 mA, V_{CC} = 5 V, R_L = 1.9 $k\Omega$		0.35	0.8	μs
	Common Mode Transient Immunity at High Level Output ³	СМн	$I_F = 0 \text{ mA, } V_{CC} = 5 \text{ V, } V_{CM} = 1.5 \text{ kV,}$ $R_L = 4.1 \text{ k}\Omega$	15			kV/μs
	Common Mode Transient Immunity at Low Level Output ⁻³	CML	IF = 16 mA, Vcc = 5 V, VcM = 1.5 kV, RL = 4.1 k Ω	-15			kV/μs

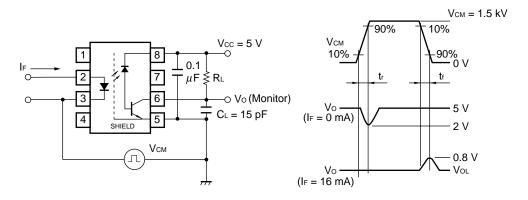
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- *1 Typical values at T_A = 25°C
- *2 Test circuit for propagation delay time



Remark CL includes probe and stray wiring capacitance.

*3 Test circuit for common mode transient immunity



Remark CL includes probe and stray wiring capacitance.

USAGE CAUTIONS

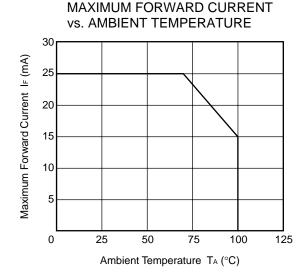
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- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of more than 0.1 μ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Pins 1, 4 (which is an NC pin) can either be connected directly to the GND pin on the LED side or left open. Also, Pin 7 (which is an NC pin) can either be connected directly to the GND pin on the detector side or left open.

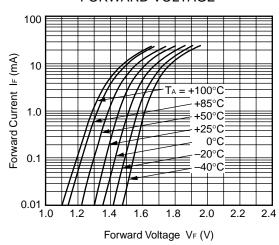
Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.

- *1 NC: Non-Connection (No Connection)
- 4. Avoid storage at a high temperature and high humidity.

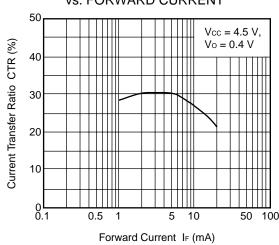
<R> TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)



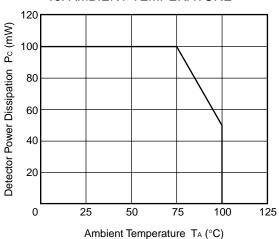
FORWARD CURRENT vs. FORWARD VOLTAGE



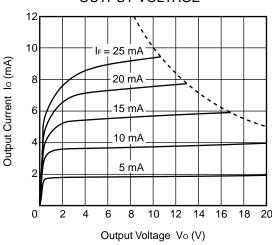
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



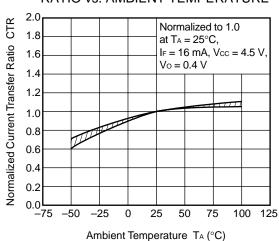
DETECTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



OUTPUT CURRENT vs. OUTPUT VOLTAGE

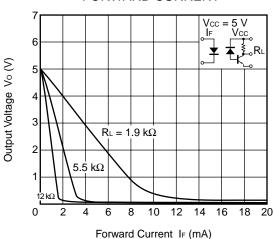


NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE

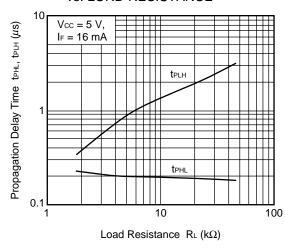


Remark The graphs indicate nominal characteristics.



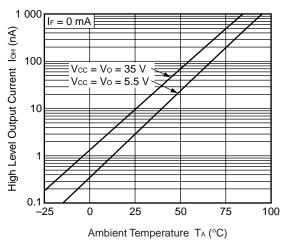


PROPAGATION DELAY TIME, vs. LORD RESISTANCE

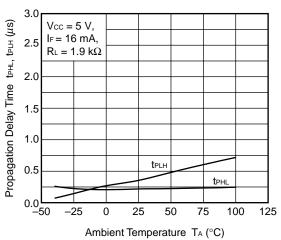


Remark The graphs indicate nominal characteristics.

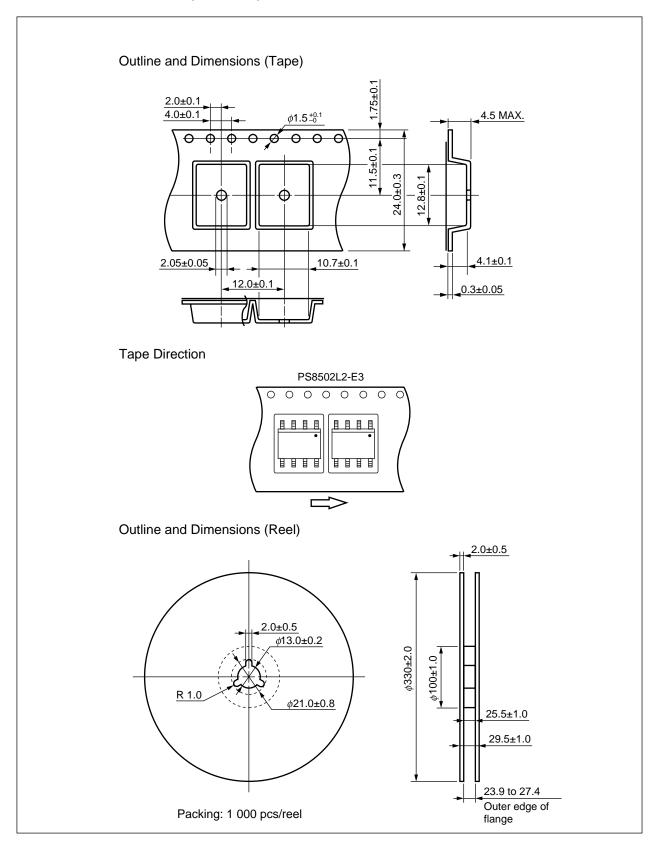
HIGH LEVEL OUTPUT CURRENT vs. AMBIENT TEMPERATURE

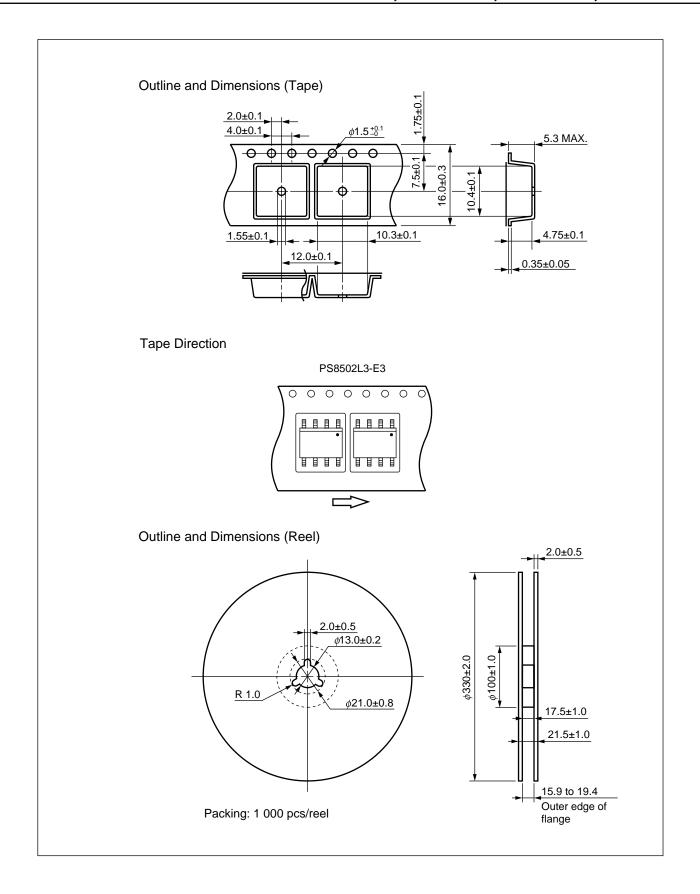


PROPAGATION DELAY TIME, vs. AMBIENT TEMPERATURE

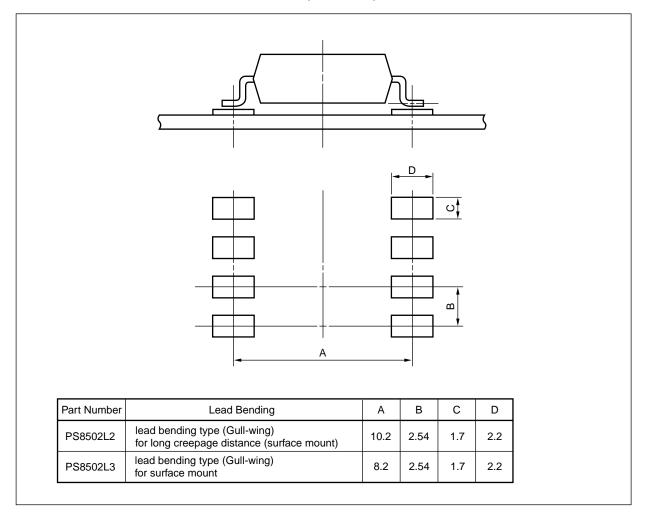


TAPING SPECIFICATIONS (UNIT: mm)





RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

Peak reflow temperature
 260°C or below (package surface temperature)

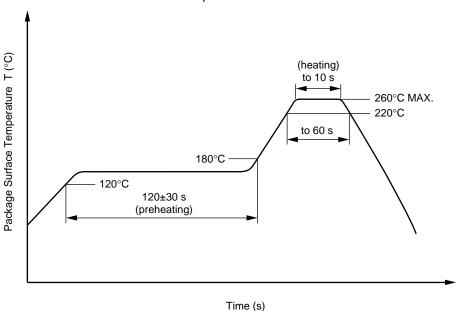
Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

Number of times
 One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Soldering by soldering iron

Peak temperature (lead part temperature)
 Time (each pins)
 350°C or below
 3 seconds or less

Flux
 Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.

(4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between Vccemitters at startup, the output side may enter the on state, even if the voltage is within the absolute maximum ratings.



<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{\text{pr}} = 1.5 \times U_{\text{IORM}}, P_{\text{d}} < 5 \text{pC}$	Uiorm Upr	1 130 1 695	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM}, P_d < 5 pC$	U_{pr}	2 119	V_{peak}
Highest permissible overvoltage	Utr	8 000	V_{peak}
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	T _{stg}	-55 to +125	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value VIO = 500 V dc at TA = 25°C VIO = 500 V dc at TA MAX. at least 100°C	Ris MIN. Ris MIN.	10 ¹²	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
V _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	10°	Ω

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M8E0904E

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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April 1st, 2010 Renesas Electronics Corporation

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