

THIS DRAWING IS UNPUBLISHED.

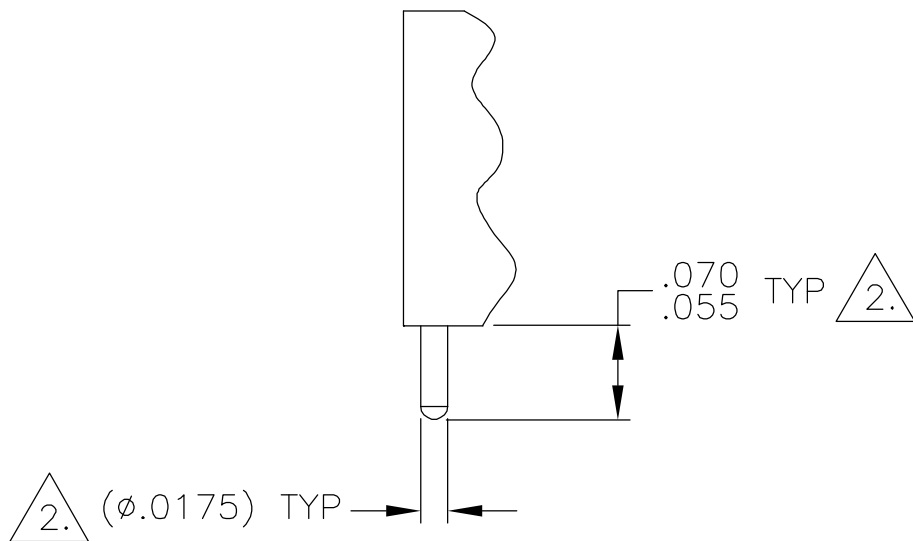
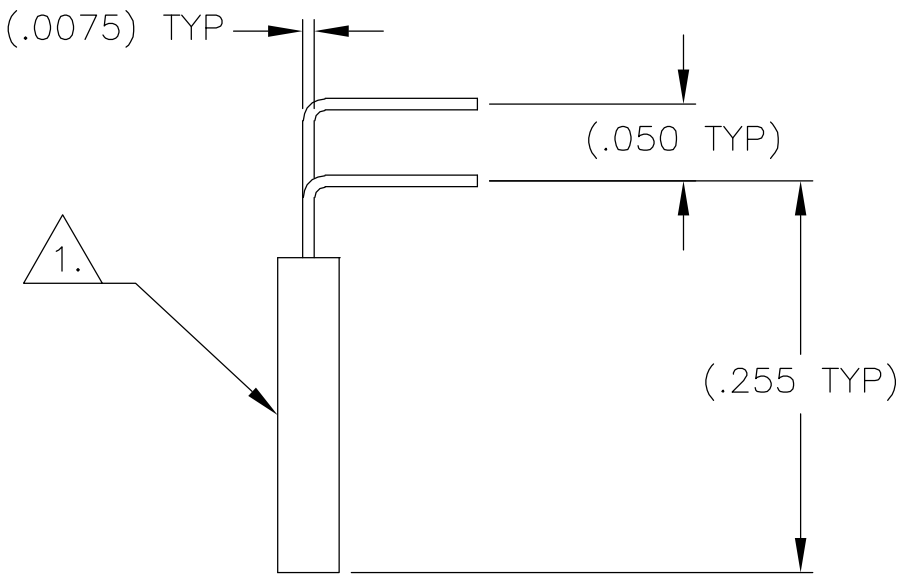
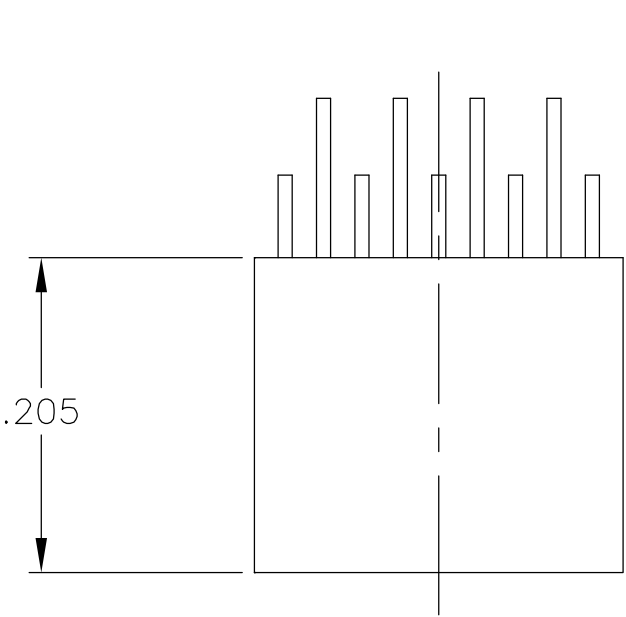
RELEASED FOR PUBLICATION

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LOC	DIST	REVISIONS				
		P	LTR	DESCRIPTION	DATE	DWN
DF	D0		N	UPDATE PER 0H2Y-0008-04	9 JAN 04	CAS
						MKS

SIZE	C DIM	(L DIM)
04	.115	(.075)
09	.240	(.200)
15	.390	(.350)

4.	25	.640	(.600)
4.	37	.940	(.900)
4.	51	1.290	(1.250)



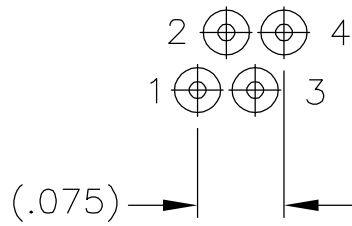
DETAIL A
POSITION "T" SHOWN FOR
REFERENCE

- T POSITION 1
- U POSITION 2
- V POSITION 3
- W POSITION 1, AND SECOND TO LAST POSITION
- Y POSITION 1, AND THIRD TO LAST POSITION
- Z POSITION 2, AND THIRD TO LAST POSITION

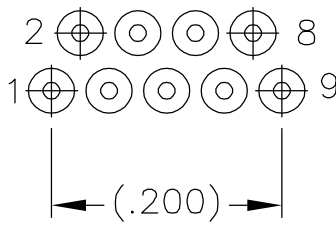
1. INSULATOR MATERIAL: LCP (LIQUID CRYSTAL POLYMER) PER ASTM D5138
2. OPTIONAL POLARIZATION GUIDE POST SHOWN FOR REFERENCE ONLY. IF GUIDE POST(S) ARE REQUIRED, THE LOCATION MUST BE SPECIFIED IN THE NANONICS PART NUMBER. GUIDE POST MATERIAL IS 303 STAINLESS STEEL, PASSIVATED PER SAE-AMS-QQ-P-35.
3. TERMINATION CODE: C6
4. WHEN ORDERING THESE SIZES, FIRST CONSULT TYCO ELECTRONICS/NANONICS FOR ADDITIONAL INFORMATION.
5. THIS DRAWING PREVIOUSLY IDENTIFIED AS NANONICS N10138/050

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN D. RYAN 20 JUN 00	tyco Electronics Tyco Electronics Corporation Harrisburg, PA 17105			
DIMENSIONS: INCHES		CHK M. STORRY 21 JUN 00				
	TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD —	NAME		
	0 PLC ± — 1 PLC ± — 2 PLC ± .010 3 PLC ± .005 4 PLC ± — ANGLES ± 1		PRODUCT SPEC —	PLUG ASSEMBLY, UNSHROUDED STRIP, HORIZONTAL MOUNT, THROUGH HOLE, 1 TO 2 ROW, .050 SPACING		
	FINISH SEE NOTES		APPLICATION SPEC —			
MATERIAL SEE NOTES		WEIGHT —	A2 00779		C=1589453	RESTRICTED TO —
CUSTOMER DRAWING				SCALE 8:1	SHEET 1 OF 2	REV N

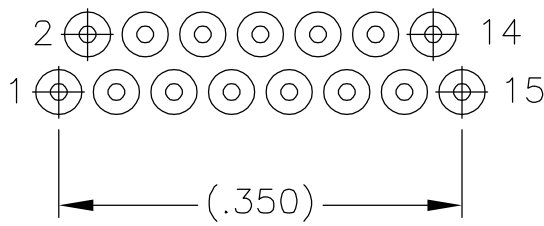
LOC	DIST	REVISIONS				
DF	D0	P	LTR	DESCRIPTION	DATE	DWN
			N	UPDATE PER 0H2Y-0008-04	9 JAN 04	APVD
					CAS	MKS



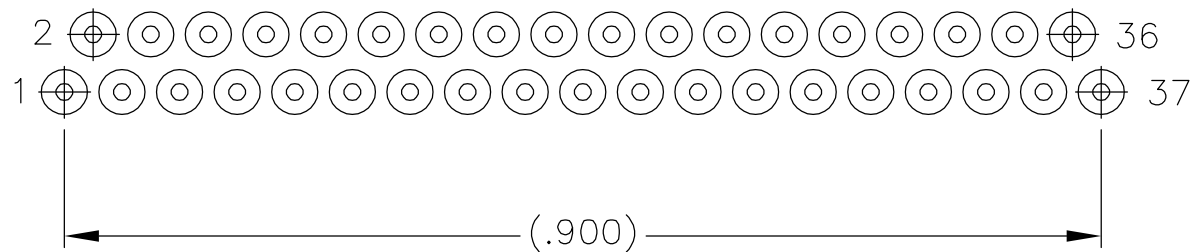
4 POSITION



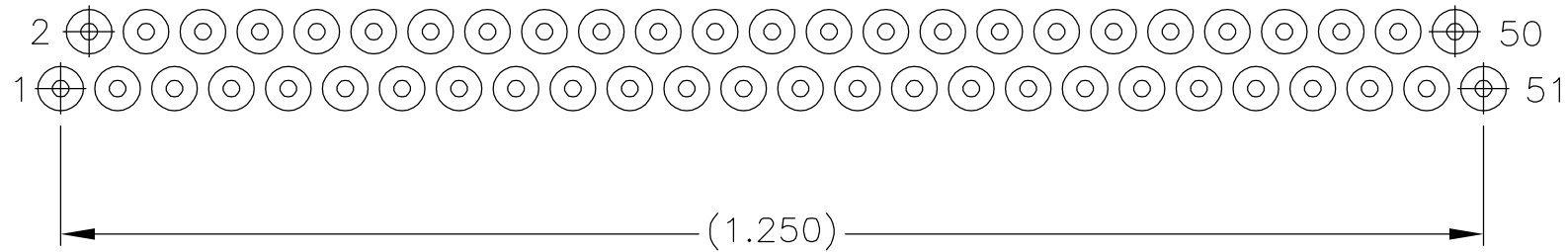
9 POSITION



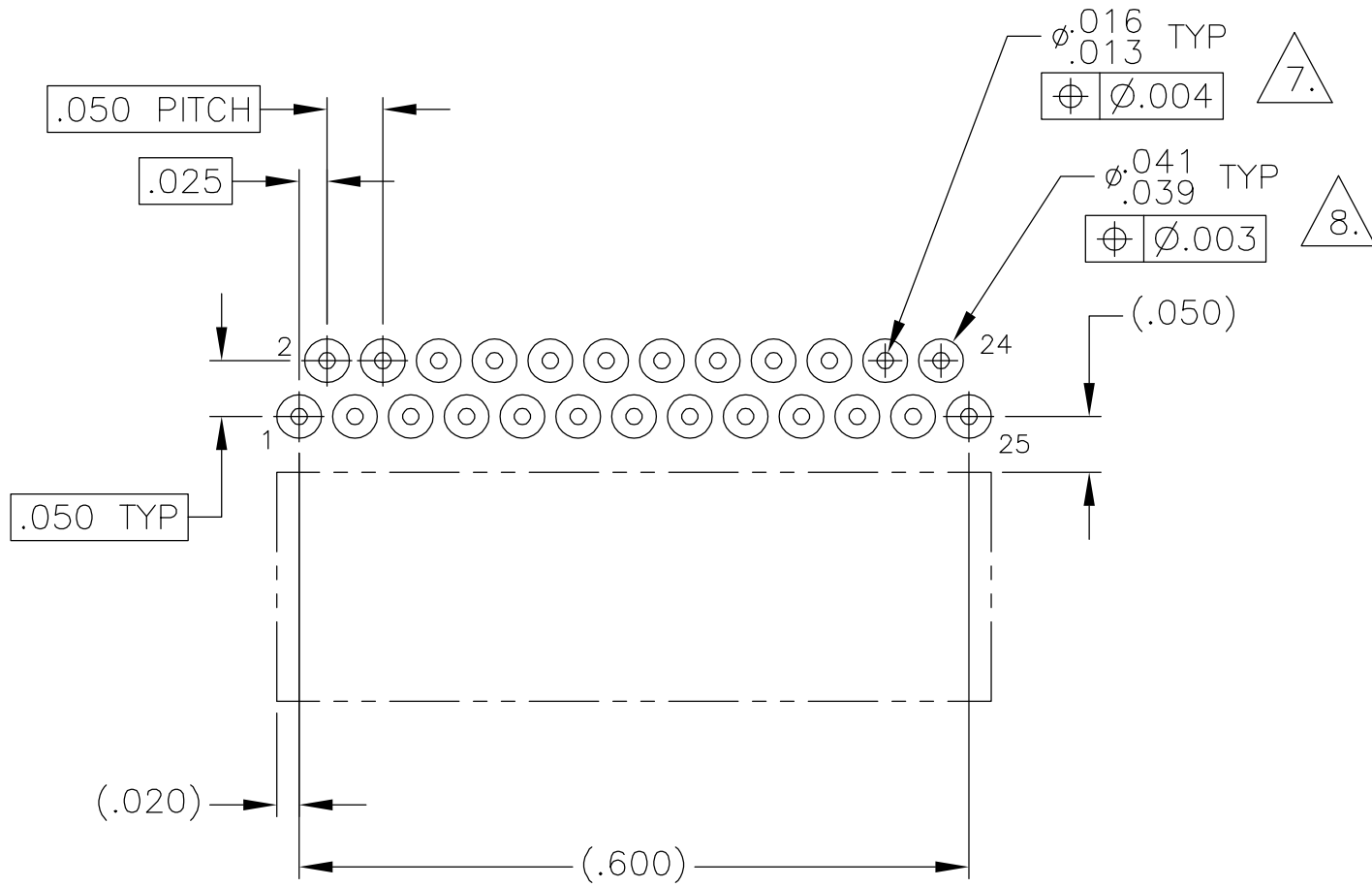
15 POSITION



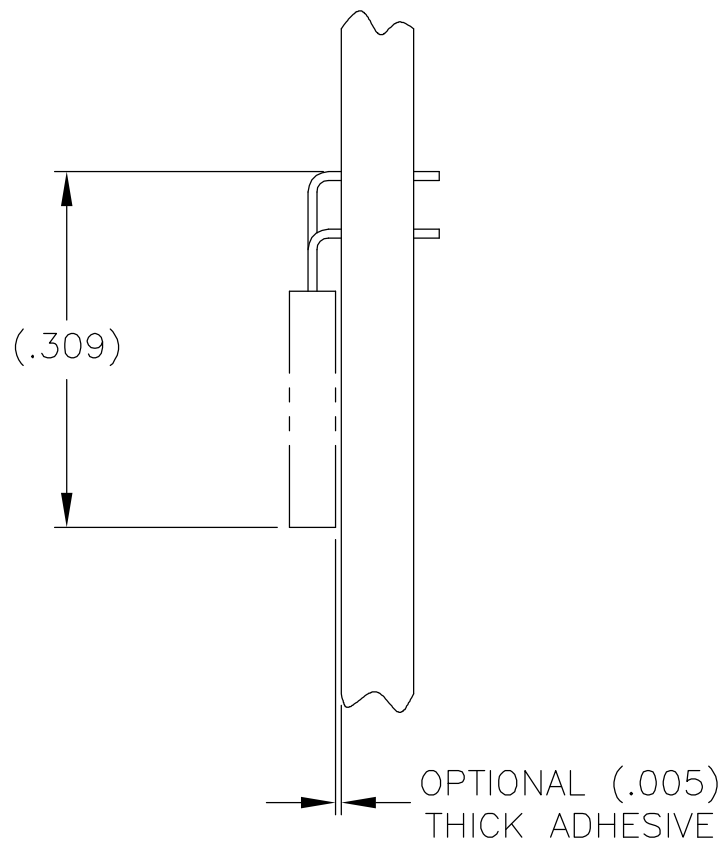
37 POSITION



51 POSITION



TYPICAL PCB LAYOUT
SIZE 25 SHOWN FOR REFERENCE



6.

POSITIONAL TOLERANCES FOR BASIC DIMENSIONED FEATURES ARE RELATIVE TO FIDUCIALS OR SOME SIMILAR DATUM REFERENCES DEFINED BY PCB DESIGNER.
7.

PLATED THROUGH HOLES
8.

SOLDER PADS
9.

ALL THROUGH HOLE LAYOUTS ARE AS VIEWED FROM TOP OF PCB.

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DIMENSIONS: INCHES		CHK —	—	NAME PLUG ASSEMBLY, UNSHROUDED STRIP, HORIZONTAL MOUNT, THROUGH HOLE, 1 TO 2 ROW, .050 SPACING			
	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD —	—				
		PRODUCT SPEC —					
		APPLICATION SPEC —					
MATERIAL —	FINISH —	WEIGHT —	—	SIZE A2	CAGE CODE 00779	DRAWING NO C=1589453	RESTRICTED TO —
CUSTOMER DRAWING				SCALE 6:1	SHEET 2	OF 2	REV N