

Power management (dual transistors)

VT6T12

Structure

PNP silicon epitaxial planar transistor

Features

- 1) Very small package with two transistors.
- 2) Suitable for current mirror circuits.

Applications

Current mirror circuits

Packaging specifications

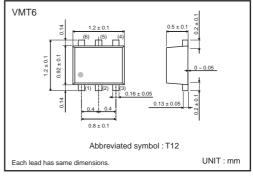
<u> </u>							
	Package	Taping					
	Code	T2R					
Туре	Basic ordering unit (pieces)	8000					
VT6T12		0					

● Absolute maximum ratings (Ta=25°C)

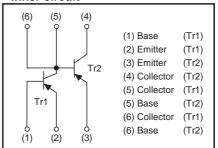
Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	-50	V
Collector-emitter voltage		VCEO	-50	V
Emitter-base voltage		Vево	-5	V
Collector current		Ic	-100	mA
		ICP *1	-200	mA
Power dissipation	Total	Pp *2 150		mW
	Element	120		mW
Junction temperature		Tj	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

^{*1} Pw=1mS Single pulse

●Dimensions (Unit: mm)



•inner circuit



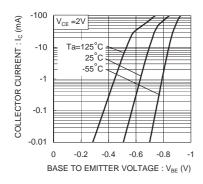
●Electrical characteristics (Ta=25°C)

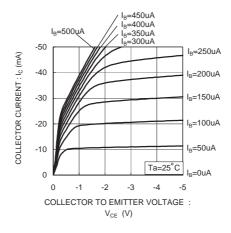
The characteristics (18-25 0)						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVceo	-50	_	_	V	Ic=-1mA
Collector-base breakdown voltage	ВУсво	-50	_	_	V	Ic= -50μA
Emitter-base breakdown voltage	ВVево	-5	_	_	V	I _E = -50μA
Collector cut-off current	Ісво	_	_	-0.1	μΑ	Vcb= -50V
Emitter cut-off current	Ієво	_	_	-0.1	μΑ	V _{EB} = -5V
Collector-emitter saturation voltage	VCE(sat)	_	-0.15	-0.40	V	Ic= -50mA, I _B = -5mA
DC current gain	hfe	120	_	560	_	VcE= -6V, Ic= -1mA
DC current gain ratio	hfe (Tr1) / hfe (Tr2)	0.9	_	1.1	-	Vce=-6V, Ic=-1mA
Transition frequency	f⊤	_	300	_	MHz	Vc=-10V, I=10mA, f=100MHz
Output capacitance	Cob	_	2	_	pF	Vcb= -10V, Ie=0A, f=1MHz

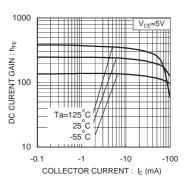
^{*1} Pw=1m5 Single pulse *2 Each terminal mounted on a recommended land

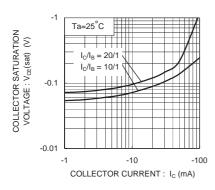
VT6T12 Data Sheet

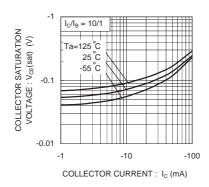
•Electrical characteristics curves

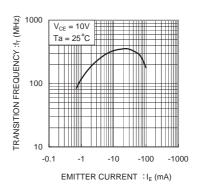


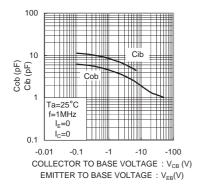












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