Magl³C Power Module FDSM – Fixed Step Down Regulator Module

7 - 42V / 1A / 3.3V or 5V Output



The FDSM series of the Magl³C Power Module family is a fixed output voltage, fully integrated dc/dc power supply. The module integrates the switching power stage, control circuitry, inductor and capacitors all in one package.

The module requires no external components for operation reducing design effort and complexity to a minimum.

The FDSM ensures fast time to market and low development costs.

It is pin compatible with the common 78xx linear regulator series. The high efficiency reduces the power dissipation and in many cases a heatsink and assembly parts are unnecessary.

12V to 3.3V conversion achieves up to 89% efficiency. 12V to 5V conversion achieves up to 92% efficiency.

The standard THT package allows for easy assembly.

FEATURES

- Peak efficiency up to 93%
- Current capability up to 1A
- Input voltage range: 7 to 42V
- Output voltage: 3.3V or 5V
- Continuous output power: 3.3W or 5W
- Integrated capacitors and inductor
- Low output voltage ripple at 1A load: typ.15mV (3.3V), typ.20mV (5V)
- Output voltage accuracy: ±2% typ.
- Fixed 350 kHz switching frequency
- Current mode control
- Pulse skipping for high efficiency at light loads
- Internal soft-start
- Thermal shutdown
- Short circuit protection
- Cycle by cycle current limit
- -40°C to 85°C ambient operating temperature range
- RoHS & REACH compliant
- UL94V-0 package material
- EN55022 Class B compliant

TYPICAL APPLICATIONS

- Point-of-Load from 24V, 18V, 15V, 12V, 9V rail
- Replacement for linear regulators
- Interface and microcontroller supply
- General purpose

TYPICAL CIRCUIT DIAGRAM





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PACKAGE



MARKING DESCRIPTION

Marking	Description	
WE	Würth Elektronik tradename	
78	Indicates the compatibility with 78xx linear regulator	
173xxxxx	Ordering Code	
YY	Year	
WW	Calendar week	
VV	Output voltage (3.3V or 5V)	

PIN DESCRIPTION

SYMBOL	PIN	ТҮРЕ	DESCRIPTION
VIN	1	Power	Input voltage
GND	2	Power	Ground reference for VIN and VOUT
VOUT	3	Power	Output voltage

ORDERING INFORMATION

ORDER CODE	PART DESCRIPTION	SPECIFICATIONS	PACKAGE	PACKAGING UNIT
173010542	WPMDH1100501S	42V / 5V - 1.0A version	SIP-3	Tube with 42 pieces
173010342	WPMDH1100331S	42V / 3.3V - 1.0A version	SIP-3	Tube with 42 pieces

PIN COMPATIBLE DEVICES OF OTHER FAMILIES

ORDER CODE	PART DESCRIPTION	SPECIFICATIONS	PACKAGE	PACKAGING UNIT
173950578	WPMDL9500501S	28V / 5V - 0.5A version	SIP-3	Tube with 42 pieces
173950378	WPMDL9500501S	28V / 3.3V - 0.5A version	SIP-3	Tube with 42 pieces
173010578	WPMDL1100501S	28V / 5V - 1.0A version	SIP-3	Tube with 42 pieces
173010378	WPMDL1100331S	28V / 3.3V - 1.0A version	SIP-3	Tube with 42 pieces

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PACKAGE SPECIFICATIONS

Weight	Flammability
2.0g	Meets UL 94 V-O

SALES INFORMATION

SALES CONTACTS	6
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ABSOLUTE MAXIMUM RATINGS

Caution:

Exceeding the listed absolute maximum ratings may affect the device negatively and may cause permanent damage. These are stress ratings only, which do not imply functional operation of the device at these or any other condition beyond those indicated under "Operation Conditions".

SYMBOL	PARAMETER	LIN	LIMITS		
STWBOL			MAX ⁽¹⁾	UNIT	
VIN	Input pin voltage	0	43	V	
VOUT	Output pin voltage	-0.2	8	V	
T _{storage}	Assembled, non-operating storage temperature	-55	125	°C	
V _{ESD}	ESD Voltage (Human Body Model), according to EN61000-4-2		±2000	V	

OPERATING CONDITIONS

Operating conditions are conditions under which operation of the device is intended to be functional. All values are referenced to GND.

MIN and MAX limits are valid for the recommended ambient temperature range of **-40°C to 85°C**. Typical values represents statistically the utmost probability at following conditions: $V_{IN} = 24V$, $V_{OUT} = 3.3V$ (173010342), $V_{OUT} = 5V$ (173010542), $I_{OUT} = 1A$ unless otherwise noted.

SYMBOL	PARAMETER	MIN ⁽¹⁾	TYP ⁽²⁾	MAX ⁽¹⁾	UNIT
V _{IN}	Input voltage (173010342)	7	-	42	V
Vin	Input voltage (173010542)	8	-	42	V
TA	Ambient temperature range	-40	-	85 ⁽³⁾	°C
Ιουτ	Nominal output current			1	А

THERMAL SPECIFICATIONS

SYMBOL	PARAMETER	TYP ⁽²⁾	UNIT
ΘCA	Case to ambient thermal impedance (4)	70	°C/W
T _{SD}	Thermal shutdown, junction temperature rising	150	°C

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ELECTRICAL SPECIFICATIONS

MIN and MAX limits are valid for the recommended ambient temperature range of **-40°C to 85°C**. Typical values represent statistically the utmost probability at the following conditions: $V_{IN} = 24V$, $V_{OUT} = 3.3V$ (173010342), $V_{OUT} = 5V$ (173010542), $I_{OUT} = 0.1$ to $1A^{(5)}$ unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	MIN ⁽¹⁾	TYP ⁽²⁾	MAX ⁽¹⁾	UNIT
		Output current	·			
IOCP	Over current protection	V _{IN} = 12V	2.9	3.5	-	А
		Accuracy				
	Line regulation	$V_{IN} = 42V, T_A = 25^{\circ}C, I_{OUT} = 1A$	-	0.2	-	%
	Load regulation		-	0.4	-	%
	Total output voltage variation	$T_A = 25^{\circ}C, I_{OUT} = 1A$	-	±2	±3	%
Vout	Temperature coefficient			0.015		%/°C
	Output voltage ripple (20MHz BW limited) ⁽⁶⁾	with 10µF MLCC output capacitor	-	75 30	100	mV _{pp} mV _{pp}
		Switching frequency				
fsw	Switching frequency	······································	280	350	420	kHz
1011		Input current	200	000	120	
l _{IN}	No load input current	Operating, switching	-	1	-	mA
		Undervoltage behaviour				
	Under-voltage lockout	$V_{OUT} = 3.3V, V_{IN}$ increasing	-	5.7	6.3	V
		Vout = 3.3V, VIN decreasing	4.4	4.9		V
Vuvlo		Vout = 5V, VIN increasing	-	7.6	8	V
		Vout = 5V, VIN decreasing	6.8	7.2	-	V
		Efficiency		1	11	
		$V_{OUT} = 3.3V, V_{IN} = 7V$	-	-	89	%
n	Efficiency	V _{OUT} = 3.3V, V _{IN} = 42V	-	-	79	%
η	Efficiency	$V_{OUT} = 5V, V_{IN} = 8V$	-	-	93	%
		$V_{OUT} = 5V, V_{IN} = 42V$	-	-	85	%
		Short circuit protection	·			
I _{IN,short}	Input Current under short circuit condition at output		-	65	-	mA
	I	Transient response	1	1	1	
Vtr	Transient Response V _{OUT}	I_{OUT} = from 0.5A to 1A or from 1A to 0.5A	-	±75	-	mV
		I_{OUT} = from 0.1A to 1A or from 1A to 0.1A	-	±200	-	mV

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EMC BEHAVIOR

PARAMETER	TEST CONDITIONS	CLASSIFICATION
Conducted Emissions	With filter (see page 18)	Class B, according to EN55022 (see results on pages 7-8)
Radiated Emissions	With filter (see page 18)	Class B, according to EN55022 (see results on pages 7-8)

RELIABILITY

SYMBOL	PARAMETER	TEST CONDITIONS	MIN ⁽¹⁾	TYP ⁽³⁾	MAX ⁽¹⁾	UNIT
MTBF	Mean Time Between Failures,	$T_A = 25^{\circ}C$	8600 ·10 ³		4	
	according to MIL-HDBK 217F	$T_A = 68^{\circ}C$		3880 ·10 ³		n

NOTES

- (1) Min and Max limits are 100% production tested at 25°C. Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods.
- (2) Typical numbers are valid at 25°C ambient temperature and represent statistically the utmost probability assuming the Gaussian distribution.
- (3) Depending on load current, see derating diagram
- (4) Measured without heatsink, no air flow
- (5) Under light load conditions the devices may not meet all specifications
- (6) The industry standard for comparison of the output voltage ripple between switching regulators or modules requires a 10µF ceramic (sometimes additional 1µF ceramic in parallel) at the point of load where the voltage measurement is done using an oscilloscope with its probe and probe jack for low voltage/high frequency (low impedance) measurement. The oscilloscopes bandwidth is limited at 20MHz.

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TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: $T_{AMB} = 25^{\circ}C$.

EMI MEASUREMENTS



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TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: $T_{AMB} = 25^{\circ}C$.

EMI MEASUREMENTS



Magl³C Power Module FDSM – Fixed Step Down Regulator Module



TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: $T_{AMB} = 25^{\circ}C$.

EFFICIENCY



173010342 3.3V Output

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TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: T_{AMB} = 25°C.

EFFICIENCY



173010342 and 173010542 - $I_{OUT} = 1A$

OUTPUT POWER DERATING



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TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: $T_{AMB} = 25^{\circ}C$.

LOAD REGULATION



173010342 3.3V Output

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TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: $T_{AMB} = 25^{\circ}C$.

LINE REGULATION



173010342 3.3V output

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TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: $T_{AMB} = 25^{\circ}C$.

OUTPUT VOLTAGE RIPPLE



173010342 $V_{OUT} = 3.3V$, $I_{OUT} = 1A$, $V_{IN} = 24V$



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TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: $T_{AMB} = 25^{\circ}C$.

OUTPUT VOLTAGE RIPPLE





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BLOCK DIAGRAM



CIRCUIT DESCRIPTION

The Magl³C Power Module 17301xx42 is based on a non-synchronous step-down regulator with integrated MOSFET, freewheeling diode, power inductor, input and output capacitors. The control scheme is based on a Current Mode (CM) regulation loop.

The V_{OUT} of the regulator is divided with the internal feedback resistor network and fed into the error amplifier which compares this signal with the internal 0.8V reference. The error signal is amplified and controls the on-time of a fixed frequency pulse width generator. This signal drives the power MOSFET.

The Current Mode architecture features a constant frequency during load steps. Only the on-time is modulated. It is internally compensated and stable.

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LIGHT LOAD OPERATION

At light load conditions, the device skips switching cycles in order to keep the output voltage regulated as well as to increase the efficiency under this condition.

SOFT-START

In order to prevent the output voltage from overshooting during start-up, a soft-start is implemented. The soft-start is internally set and lasts around 1.3ms for both the 173010342 and 173010542. The figure below shows a start-up example, in this case with 173010342.



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PROTECTIVE FEATURES

Over temperature protection (OTP)

The junction temperature of the Magl³C Power Module should not be allowed to exceed its maximum ratings. Thermal protection is implemented by an internal Thermal Shutdown circuit which activates at 150°C (typ.), causing the device to enter a low power standby state. In this state the MOSFET remains off causing V_{OUT} to fall. Thermal protection helps to prevent catastrophic failures from accidental device overheating. When the junction temperature falls back below 150° (hysteresis is implemented) V_{OUT} rises smoothly and normal operation resumes.

Short circuit protection (SCP)

The short circuit protection is realized via cycle by cycle current monitoring. Recovery from short circuit protection mode occurs during the switching cycle following the removal of the short circuit condition. When the 173010x42 recovers from a short circuit condition, the soft-start is not active. Therefore an overshoot at output voltage can be observed (see figure below). Under short circuit condition the input current is limited to I_{IN,short} (typ. 65mA)



173010542 - Output voltage overshoot, VIN = 24V

Over current protection (OCP)

For protection against load faults, the 173010x42 incorporates cycle-by-cycle current monitoring. During an overcurrent condition the output current is limited and the output voltage drops. When the overcurrent condition is removed, the output voltage returns to the nominal voltage.

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APPLICATION CONSIDERATIONS

Input filter suggestion for conducted EMI

The input filter shown in the schematic below is recommended to achieve conducted EMI compliance according to EN55022 Class B (see results on pages 21 and 22).



Bill of Material of the Pi-Filter

Designator	Description	Quantity	Order Code	Manufacturer
C _{f1a}	Filter ceramic chip capacitor 10µF/50V 1206 X5R	1		
C _{f1b}	Filter ceramic chip capacitor 10µF/50V 1206 X5R	1		
C _{f2}	Filter ceramic chip capacitor 10µF/50V 1206 X5R	1		
Lf	Filter inductor, 22µH, PD2 family	1	744774122	Würth Elektronik

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Generating negative output voltage

Many industrial applications require negative voltages. The 173010x42 can easily provide a negative voltage using the circuit shown below.



It is important to be aware that in this configuration the 173010x42 must withstand the sum of the input voltage and the absolute value of the output voltage ($V_{IN} + |V_{OUT}|$), instead of just the input voltage. This means that the maximum operating voltage should be limited to $42V - |V_{OUT}|$ (e.g. if the 171010542 is used in this configuration, the input voltage should not exceed 37V). Moreover, the maximum output current of this configuration is no longer 1A, instead it must be reduced according to the below mentioned formula (see also the graph below):

$$I_{OUT-} = (1 - D) \cdot 1A$$
 (1)

where D is the duty cycle, in this case defined according to:

$$D = \frac{V_{OUT}}{V_{IN} + V_{OUT}}$$
(2)

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Compared with a standard positive buck configuration, the negative output buck contains an additional critical loop (between V_{IN} and V_{OUT}), which needs an additional capacitor, as shown in the circuit above.

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Another common requirement in industrial application is to provide a complementary voltage (e.g. ±5V). The circuit below shows how this target can be achieved simply combining a 173010x42 used in a standard configuration (delivering a positive output voltage) with the above mentioned solution for negative voltages.



Complementary output voltage

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PHYSICAL DIMENSIONS





Tolerance: $xx.x = \pm 0.5mm$; $xx.xx = \pm 0.25mm$

RECOMMENDED DRILL HOLES

TUBE





all dimensions in mm

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DOCUMENT HISTORY

Revision	Date	Description	Comment
1.0	June 2016	Final version	

CAUTIONS AND WARNINGS

The following conditions apply to all goods within the product series of Magl³C of Würth Elektronik eiSos GmbH & Co. KG:

General:

All recommendations according to the general technical specifications of the data-sheet have to be complied with.

The usage and operation of the product within ambient conditions which probably alloy or harm the component surface has to be avoided.

The responsibility for the applicability of customer specific products and use in a particular customer design is always within the authority of the customer. All technical specifications for standard products do also apply for customer specific products.

Residual washing varnish agent that is used during the production to clean the application might change the characteristics of the body, pins or termination. The washing varnish agent could have a negative effect on the long term function of the product. Direct mechanical impact to the product shall be prevented as the material of the body, pins or termination could flake or in the worst case it could break. As these devices are sensitive to electrostatic discharge customer shall follow proper IC Handling Procedures.

Customer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of Würth Elektronik eiSos GmbH & Co. KG components in its applications, notwithstanding any applications-related information or support that may be provided by Würth Elektronik eiSos GmbH & Co. KG. Customer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Customer will fully indemnify Würth Elektronik eiSos and its representatives against any damages arising out of the use of any Würth Elektronik eiSos GmbH & Co. KG components in safety-critical applications.

Product specific:

Follow all instructions mentioned in the datasheet, especially:

- The solder profile has to comply with the technical reflow or wave soldering specification, otherwise this will void the warranty.
- All products are supposed to be used before the end of the period of 12 months based on the product date-code.
- Violation of the technical product specifications such as exceeding the absolute maximum ratings will void the warranty.
- It is also recommended to return the body to the original moisture proof bag and reseal the moisture proof bag again.
- ESD prevention methods need to be followed for manual handling and processing by machinery.

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It has to be clearly pointed out that the possibility of a malfunction of electronic components or failure before the end of the usual lifetime cannot be completely eliminated in the current state of the art, even if the products are operated within the range of the specifications. In certain customer applications requiring a very high level of safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health it must be ensured by most advanced technological aid of suitable design of the customer application that no injury or damage is caused to third parties in the event of malfunction or failure of an electronic component.

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Some products within the product range may contain substances which are subject to restrictions in certain jurisdictions in order to serve specific technical requirements. Necessary information is available on request. In this case the field sales engineer or the internal sales person in charge should be contacted who will be happy to support in this matter.

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Due to constant product improvement product specifications may change from time to time. As a standard reporting procedure of the Product Change Notification (PCN) according to the JEDEC-Standard we inform about minor and major changes. In case of further queries regarding the PCN, the field sales engineer or the internal sales person in charge should be contacted. The basic responsibility of the customer as per Section 1 and 2 remains unaffected.

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Due to technical progress and economical evaluation we also reserve the right to discontinue production and delivery of products. As a standard reporting procedure of the Product Termination Notification (PTN) according to the JEDEC-Standard we will inform at an early stage about inevitable product discontinuance. According to this we cannot guarantee that all products within our product range will always be available. Therefore it needs to be verified with the field sales engineer or the internal sales person in charge about the current product availability expectancy before or when the product for application design-in disposal is considered. The approach named above does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.

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