AUTOMOTIVI GRADE

HALOGEN

FREE GREEN



# Vishay Semiconductors

# High Speed Infrared Emitting Diodes, 940 nm, Surface Emitter Technology



#### **DESCRIPTION**

As part of the <u>SurfLight<sup>TM</sup></u> portfolio, the VSMY1940X01 is an infrared, 940 nm emitting diode based on GaAlAs surface emitter chip technology with high radiant intensity, high optical power and high speed, molded in clear, untinted 0805 plastic package for surface mounting (SMD).

### **FEATURES**

• Package type: surface-mount

• Package form: 0805

• Dimensions (L x W x H in mm): 2 x 1.25 x 0.85

AEC-Q101 qualified

• Peak wavelength:  $\lambda_p = 940 \text{ nm}$ 

· High reliability

• High radiant power

High radiant intensity

· High speed

• Angle of half sensitivity:  $\varphi = \pm 60^{\circ}$ 

· Suitable for high pulse current operation

0805 standard surface-mountable package

• Floor life: 168 h, MSL 3, according to J-STD-020

• Lead (Pb)-free reflow soldering

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **APPLICATIONS**

- IrDA compatible data transmission
- Miniature light barrier
- Photointerrupters
- Optical switch
- · Emitter source for proximity sensors
- IR touch panels
- IR flash
- IR illumination
- 3D TV

PRODUCT SUMMARY					
COMPONENT	I <sub>e</sub> (mW/sr)	φ (deg)	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)	
VSMY1940X01	10	± 60	940	10	

#### Note

· Test conditions see table "Basic Characteristics"

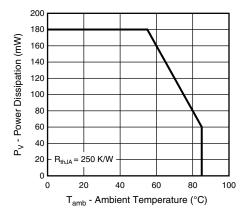
ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY1940X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805		

#### Note

MOQ: minimum order quantity



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		$V_R$	5	V	
Forward current		I <sub>F</sub>	100	mA	
Peak forward current	$t_p/T = 0.1$ , $t_p = 100 \ \mu s$	I <sub>FM</sub>	200	mA	
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1	Α	
Power dissipation		$P_V$	180	mW	
Junction temperature		T <sub>j</sub>	100	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +85	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C	
Soldering temperature	According to figure 10, J-STD-020	T <sub>sd</sub>	260	°C	
Thermal resistance junction / ambient	J-STD-051, leads 7 mm, soldered on PCB	R <sub>thJA</sub>	250	K/W	



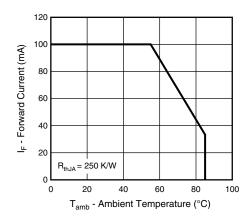


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Farmend wells as	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	V <sub>F</sub>	-	1.45	1.8	V
Forward voltage	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	V <sub>F</sub>	=	2.2	-	V
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 100 mA	TK <sub>VF</sub>	-	-2	-	mV/K
Reverse current		I <sub>R</sub>	Not designed for reverse operation		μΑ	
Junction capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz,}$ $E = 0 \text{ mW/cm}^2$	CJ	-	125	-	pF
Padient intensity	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	I <sub>e</sub>	5	10	15	mW/sr
Radiant intensity	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	I <sub>e</sub>	-	75	-	mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фе	=	50	-	mW
Temperature coefficient of radiant power	I <sub>F</sub> = 100 mA	TΚφ <sub>e</sub>	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 60	-	deg
Peak wavelength	I <sub>F</sub> = 100 mA	$\lambda_{p}$	920	940	960	nm
Spectral bandwidth	I <sub>F</sub> = 100 mA	Δλ	-	40	-	nm
Temperature coefficient of λ <sub>p</sub>	I <sub>F</sub> = 30 mA	$TK_{\lambda p}$	-	0.25	-	nm/K
Rise time	I <sub>F</sub> = 100 mA, 20 % to 80 %	t <sub>r</sub>	-	10	-	ns
Fall time	I <sub>F</sub> = 100 mA, 20 % to 80 %	t <sub>f</sub>	-	10	-	ns

## BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

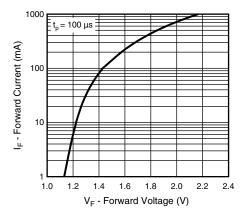


Fig. 3 - Forward Current vs. Forward Voltage

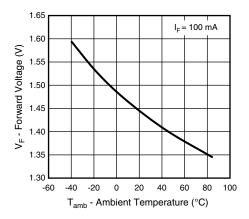


Fig. 4 - Forward Voltage vs. Ambient Temperature

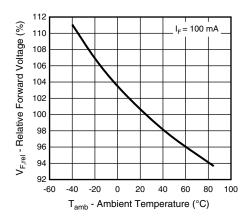


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

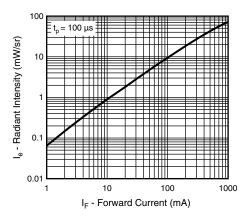


Fig. 6 - Radiant Intensity vs. Forward Current

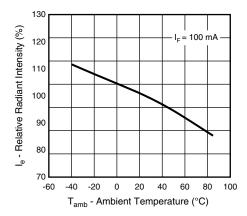


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

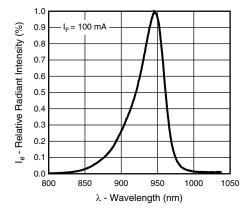


Fig. 8 - Relative Radiant Intensity vs. Wavelength



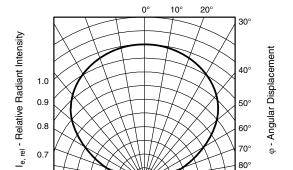


Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

0

#### **REFLOW SOLDER PROFILE**

0.4 0.2

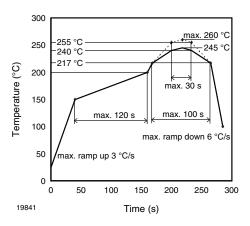


Fig. 10 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

Floor life: 168 h

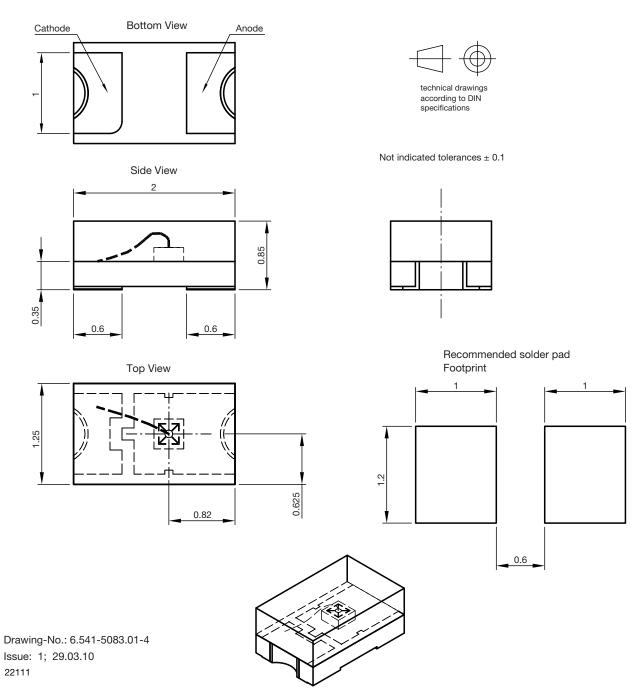
Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

### **DRYING**

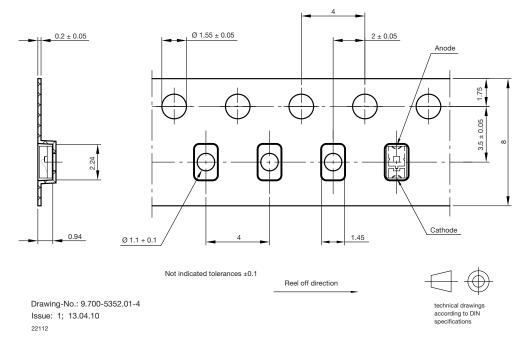
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5  $^{\circ}$ M.



## **PACKAGE DIMENSIONS** in millimeters

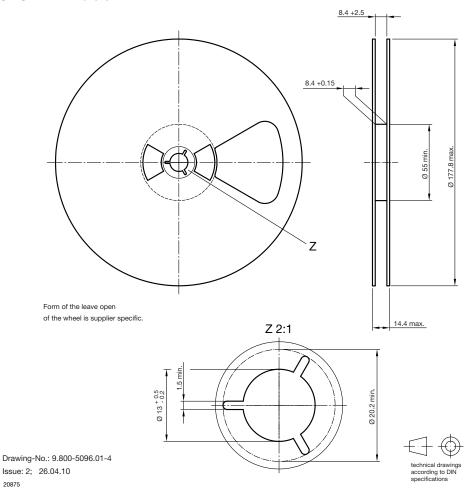


## **BLISTER TAPE DIMENSIONS** in millimeters



#### **REEL DIMENSIONS** in millimeters

20875





## **Legal Disclaimer Notice**

Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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