



All dimensions are in mm; tolerances according to ISO 2768 m-H

**Interface**

According to IEC 61169-35  
Mechanically compatible with RPC-3.50 and SMA

**Contents and Documentation**

- This kit is delivered with
- **Standard Definitions Card**  
Printed Standard Definitions that can be used on nearly all Vector Network Analyzers
  - **Test Results Documentation**
  - **Hard Shell Case**

**Material and plating**

**Connector parts**

Center conductor  
Outer conductor  
Body  
Dielectric  
Substrate

**Material**

Beryllium copper  
Stainless steel  
Aluminum  
PS  
Al<sub>2</sub>O<sub>3</sub>

**Plating**

Gold, min. 1.27 µm, over nickel  
Passivated  
black anodized

**Electrical data**

Frequency range DC to 40.0 GHz

**Open**

Error from nominal phase<sup>1</sup>  
 ≤ 1.5°, DC to 4 GHz  
 ≤ 4.0°, 4 GHz to 26.5 GHz  
 ≤ 5.0°, 26.5 GHz to 40.0 GHz

**Short**

Error from nominal phase<sup>2</sup>  
 ≤ 1.5°, DC to 4 GHz  
 ≤ 4.0°, 4 GHz to 26.5 GHz  
 ≤ 5.0°, 26.5 GHz to 40.0 GHz

**Load**

Return loss  
 ≥ 40.0 dB, DC to 4 GHz  
 ≥ 28.0 dB, 4 GHz to 26.5 GHz  
 ≥ 25.0 dB, 26.5 GHz to 40.0 GHz

DC Resistance 50 Ω ± 0.5 Ω  
 Power handling ≤ 0.5 W

<sup>1</sup> The nominal phase is defined by the Offset Delay, the Offset Loss and the Fringing Capacitances

<sup>2</sup> The nominal phase is defined by the Offset Delay, the Offset Loss and the Short Inductance

**Mechanical data**

Mating cycles ≥ 500  
 Maximum torque 1.70 Nm  
 Recommended torque 0.90 Nm  
 Gauge 0.00 mm to 0.08 mm

**General standard definitions**

For proper operation the vector network analyzer (VNA) needs a model describing the electrical behaviour of this calibration standard. The different models, units, and terms used will depend on the VNA type and they will have to be entered into the VNA. All values are based on typical geometry and plating.

**Open**

Offset Z<sub>o</sub> / Impedance / Z<sub>o</sub> 50 Ω  
 Offset Delay 28.353 ps  
 Length (electrical) / Offset Length 8.50 mm  
 Offset Loss 2.40 GΩ/s  
 Loss 0.0118 dB/√GHz  
 Fringing Capacitances  
 C<sub>0</sub> = -4.30000 x 10<sup>-15</sup> F / -4.30000 fF  
 C<sub>1</sub> = 431.000 x 10<sup>-27</sup> F/Hz / 0.43100 fF /GHz  
 C<sub>2</sub> = -11.5000 x 10<sup>-36</sup> F/Hz<sup>2</sup> / -0.01150 fF /GHz<sup>2</sup>  
 C<sub>3</sub> = 0.12000 x 10<sup>-45</sup> F/Hz<sup>3</sup> / 0.00012 fF /GHz<sup>3</sup>

**Short**

Offset $Z_0$ / Impedance / $Z_0$	50 $\Omega$		
Offset Delay	28.353 ps		
Length (electrical) / Offset Length	8.50 mm		
Offset Loss	2.40 G $\Omega$ /s		
Loss	0.0118 dB/ $\sqrt{\text{GHz}}$		
Short Inductance	$L_0 = 0.0000 \times 10^{-12}$ H	/	0.0000 pH
	$L_1 = 0.0000 \times 10^{-24}$ H/Hz	/	0.0000 pH/GHz
	$L_2 = 0.0000 \times 10^{-33}$ H/Hz <sup>2</sup>	/	0.0000 pH/GHz <sup>2</sup>
	$L_3 = 0.0000 \times 10^{-42}$ H/Hz <sup>3</sup>	/	0.0000 pH/GHz <sup>3</sup>

**Load**

Offset $Z_0$ / Impedance / $Z_0$	50 $\Omega$
Offset Delay	0.0000 ps
Length (electrical) / Offset Length	0.000 mm
Offset Loss	0.00 G $\Omega$ /s
Loss	0.0000 dB/ $\sqrt{\text{GHz}}$

**Environmental data**

Operating temperature range <sup>3</sup>	+20 °C to +26 °C
Rated temperature range of use <sup>4</sup>	0 °C to +50 °C
Storage temperature range	-40 °C to +85 °C
RoHS	compliant

<sup>3</sup> Temperature range over which these specifications are valid.

<sup>4</sup> This range is underneath and above the operating temperature range, within the calibration kit is fully functional and could be used without damage

**Declaration of documentation**

Standard delivery for this kit includes Test Results. The documentation issued reports which quantities were tested individually, traceable to national / international standards. Model based standard definitions of the calibration standards are reported in Agilent / Keysight, Rohde & Schwarz and Anritsu compatible VNA format.

**Inspection interval**

Recommendation	12 months
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**Packing**

Standard	1 pce in bag
Weight	27.6 g/pce

While the information has been carefully compiled to the best of our knowledge, nothing is intended as representation or warranty on our part and no statement herein shall be construed as recommendation to infringe existing patents. In the effort to improve our products, we reserve the right to make changes judged to be necessary.

Draft	Date	Approved	Date	Rev.	Engineering change number	Name	Date
Marcel Panicke	05.11.15	Markus Müller	02.05.18	f00	18-0787	Marion Striegler	02.05.18

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