

BFU690F NPN wideband silicon RF transistor Rev. 2 — 14 March 2014

Product data sheet

1. Product profile

1.1 General description

NPN silicon microwave transistor for high speed, low noise applications in a plastic, 4-pin dual-emitter SOT343F package.

1.2 Features and benefits

- Low noise high linearity microwave transistor
- High output third-order intercept point 34 dBm at 1.8 GHz
- 40 GHz f_T silicon technology

1.3 Applications

- Ka band oscillators DRO's
- C-band high output buffer amplifier
- ZigBee
- LTE, cellular, UMTS

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	-	16	V
V _{CEO}	collector-emitter voltage	open base	-	-	5.5	V
V _{EBO}	emitter-base voltage	open collector	-	-	2.5	V
I _C	collector current		-	70	100	mA
P _{tot}	total power dissipation	$T_{sp} \le 85 \ ^{\circ}C$ [1]	-	-	490	mW
h _{FE}	DC current gain	$I_{C} = 20 \text{ mA}; V_{CE} = 2 \text{ V}; T_{j} = 25 \text{ °C}$	90	135	180	
C _{CBS}	collector-base capacitance	V _{CB} = 2 V; f = 1 MHz	-	404	-	fF
f _T	transition frequency	I_{C} = 60 mA; V_{CE} = 1 V; f = 2 GHz; T_{amb} = 25 °C	-	18	-	GHz
G _{p(max)}	maximum power gain	$ I_{C} = 60 \text{ mA}; \text{ V}_{CE} = 1 \text{ V}; \text{ f} = 1.8 \text{ GHz}; $	-	20.5	-	dB
NF	noise figure	I_C = 15 mA; V_{CE} = 2 V; f = 1.8 GHz; Γ_S = Γ_{opt}	-	0.65	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	I_{C} = 70 mA; V _{CE} = 4 V; Z _S = Z _L = 50 Ω; f = 1.8 GHz; T _{amb} = 25 °C	-	22	-	dBm

 $\label{eq:spin} [1] \quad T_{sp} \text{ is the temperature at the solder point of the emitter lead.}$

 $\label{eq:gpmax} [2] \quad G_{p(max)} \text{ is the maximum power gain, if } K > 1. \ If \ K < 1 \ then \ G_{p(max)} = Maximum \ Stable \ Gain \ (MSG).$



NPN wideband silicon RF transistor

2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	emitter		
2	base		4
3	emitter		2
4	collector		1, 3
			mbb159

3. Ordering information

Table 3. Ordering information						
Type number	Package	ckage				
	Name	Description	Version			
BFU690F	-	plastic surface-mounted flat pack package; reverse pinning; 4 leads	SOT343F			

4. Marking

Table 4. Marking

Type number	Marking	Description
BFU690F	D4* * = p : made in Hon	
		* = t : made in Malaysia
		* = w : made in China

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

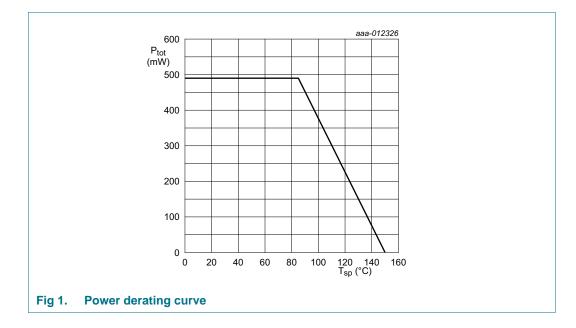
Symbol	Parameter	Conditions		Min	Max	Unit	
V _{CBO}	collector-base voltage	open emitter		-	16	V	
V _{CEO}	collector-emitter voltage	open base		-	5.5	V	
V _{EBO}	emitter-base voltage	open collector		-	2.5	V	
I _C	collector current			-	100	mA	
P _{tot}	total power dissipation	$T_{sp} \le 85 \ ^{\circ}C$	<u>[1]</u>	-	490	mW	
T _{stg}	storage temperature			-65	+150	°C	
Tj	junction temperature			-	150	°C	

[1] T_{sp} is the temperature at the solder point of the emitter lead.

6. Thermal characteristics

Tabl	le 6.	Thermal characteristics			
Syn	nbol	Parameter	Conditions	Тур	Unit
R _{th(j}	j-sp)	thermal resistance from junction to solder point	<u>[1]</u>	132	K/W

[1] Determined by simulation.



NPN wideband silicon RF transistor

7. Characteristics

Table 7. Characteristics

 $T_i = 25 \ ^{\circ}C$ unless otherwise specified

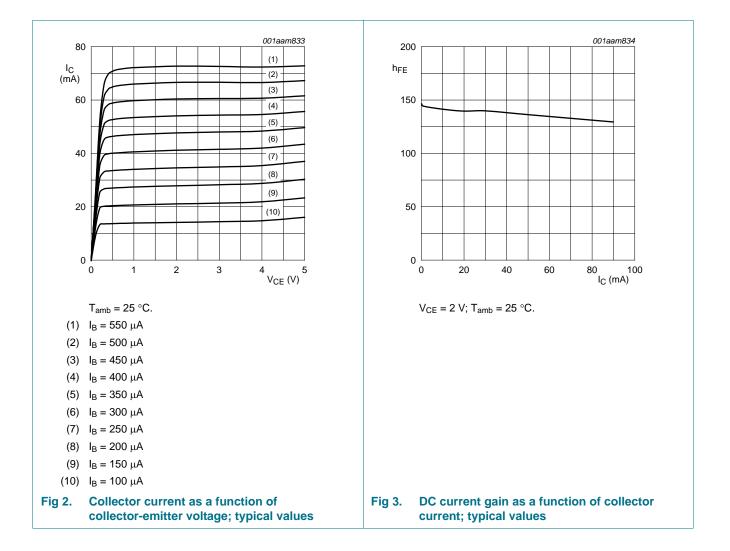
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_{C} = 2.5 \ \mu A; I_{E} = 0 \ mA$	16	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_{\rm C} = 1 \text{ mA}; I_{\rm B} = 0 \text{ mA}$	5.5	-	-	V
l _C	collector current		-	70	100	mA
I _{CBO}	collector-base cut-off current	I _E = 0 mA; V _{CB} = 8 V	-	-	100	nA
h _{FE}	DC current gain	I _C = 20 mA; V _{CE} = 2 V	90	135	180	
C _{CES}	collector-emitter capacitance	V _{CB} = 2 V; f = 1 MHz	-	527	-	fF
C _{EBS}	emitter-base capacitance	V _{EB} = 0.5 V; f = 1 MHz	-	1699	-	fF
C _{CBS}	collector-base capacitance	V _{CB} = 2 V; f = 1 MHz	-	404	-	fF
f _T	transition frequency	I_{C} = 60 mA; V _{CE} = 1 V; f = 2 GHz; T _{amb} = 25 °C	-	18	-	GHz
G _{p(max)}	maximum power gain	$I_{C} = 60 \text{ mA}; V_{CE} = 1 \text{ V}; T_{amb} = 25 \text{ °C}$ [1]				
		f = 1.5 GHz	-	22	-	dB
		f = 1.8 GHz	-	20.5	-	dB
		f = 2.4 GHz	-	17	-	dB
s ₂₁ ²	insertion power gain	$I_{C} = 60 \text{ mA}; V_{CE} = 1 \text{ V}; T_{amb} = 25 \text{ °C}$				
		f = 1.5 GHz	-	15	-	dB
		f = 1.8 GHz	-	13.5	-	dB
		f = 2.4 GHz	-	11	-	dB
NF	noise figure	I_{C} = 15 mA; V_{CE} = 2 V; Γ_{S} = Γ_{opt} ; T_{amb} = 25 °C				
		f = 1.5 GHz	-	0.60	-	dB
		f = 1.8 GHz	-	0.65	-	dB
		f = 2.4 GHz	-	0.70	-	dB
G _{ass}	associated gain	$I_C = 15 \text{ mA}; V_{CE} = 2 \text{ V}; \Gamma_S = \Gamma_{opt};$ $T_{amb} = 25 \text{ °C}$				
		f = 1.5 GHz	-	18.5	-	dB
		f = 1.8 GHz	-	17.5	-	dB
		f = 2.4 GHz	-	15.5	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	I_C = 70 mA; V_{CE} = 4 V; Z_S = Z_L = 50 Ω; T_{amb} = 25 °C				
		f = 1.5 GHz	-	22	-	dBm
		f = 1.8 GHz	-	22	-	dBm
		f = 2.4 GHz	-	20	-	dBm
IP3	third-order intercept point	$\label{eq:linear} \begin{array}{l} I_{C} = 70 \; mA; V_{CE} = 4 \; V; Z_{S} = Z_{L} = 50 \; \Omega; \\ T_{amb} = 25 \; ^{\circ}C \end{array}$				
		f = 1.5 GHz	-	34	-	dBm
		f = 1.8 GHz	-	34	-	dBm
		f = 2.4 GHz	-	33	-	dBm

[1] $G_{p(max)}$ is the maximum power gain, if K > 1. If K < 1 then $G_{p(max)}$ = MSG.

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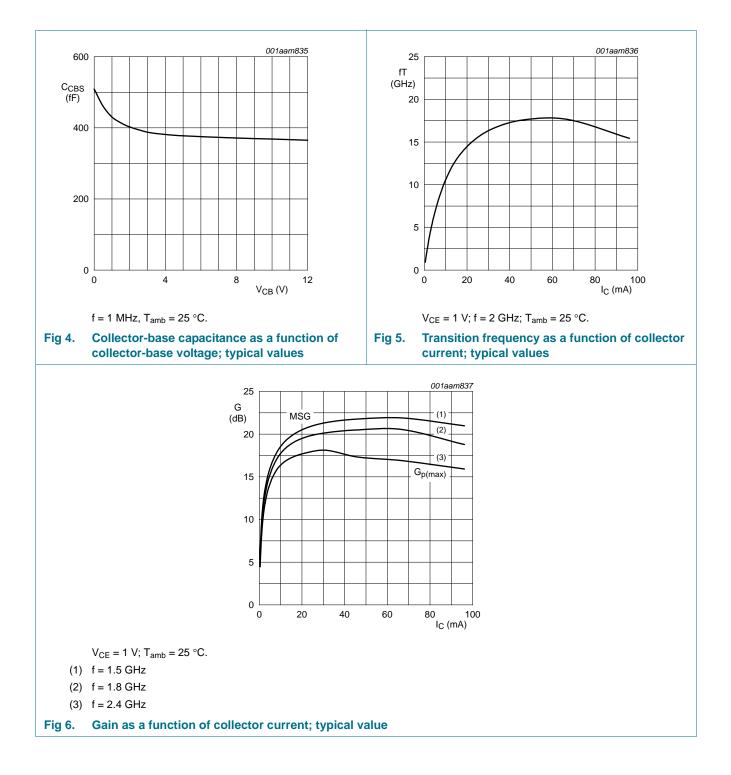
NPN wideband silicon RF transistor



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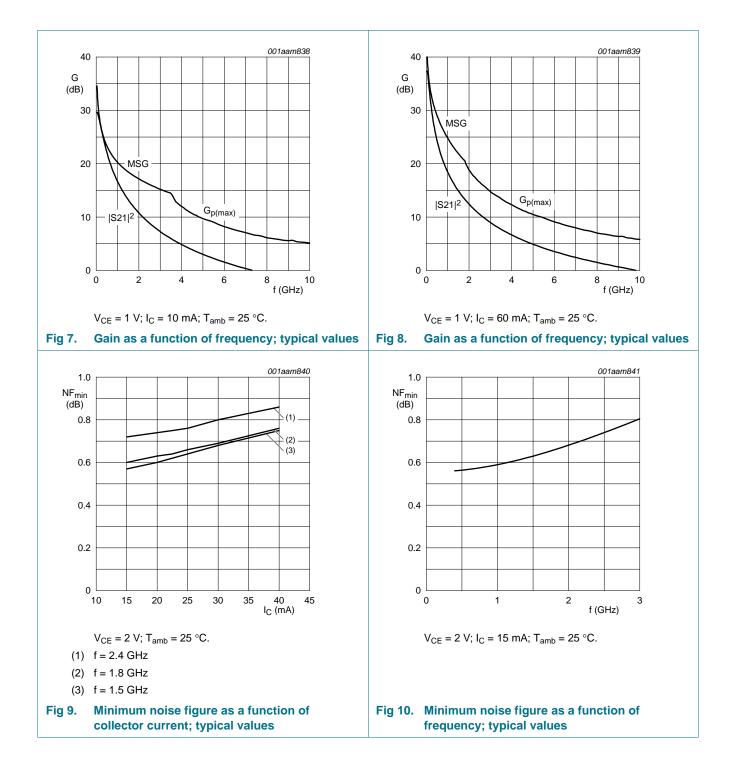
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8. Package outline

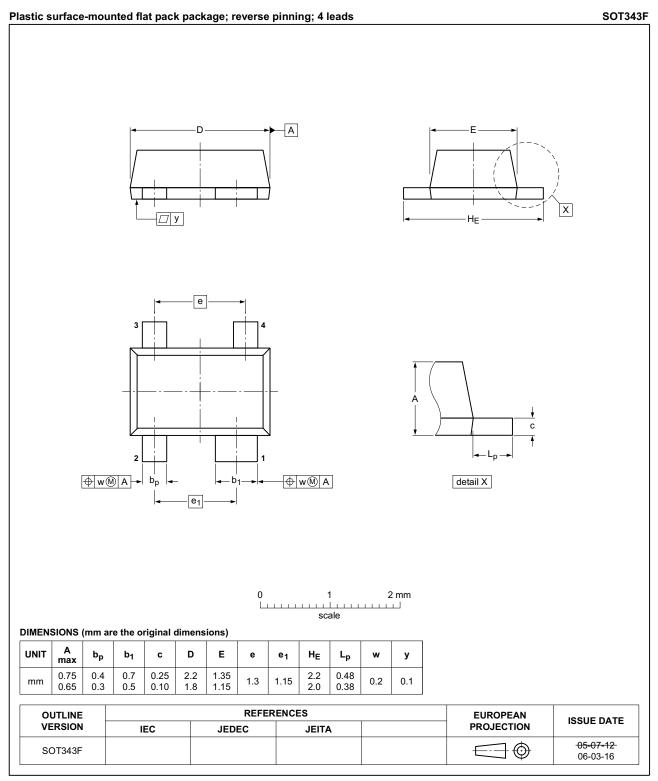


Fig 11. Package outline SOT343F

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9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

10. Abbreviations

Acronym	Description
DRO	Dielectric Resonator Oscillator
Ка	Kurtz above
LTE	Long Term Evolution
NPN	Negative-Positive-Negative
UMTS	Universal Mobile Telecommunications System

11. Revision history

Table 9.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFU690F v.2	20140314	Product data sheet	-	BFU690F v.1
Modifications:	• Table 1 on pa	ge 1: The value and conditions	s for P _{tot} have been	updated.
 <u>Table 5 on page 2</u>: The value and conditions for P_{tot} have been updated 				updated.
	 <u>Table 6 on page 3</u>: The value and conditions for R_{th(j-sp)} have been update 			
• Figure 1 on page 3: The graph has been updated.				
 <u>Section 9 on page 9</u>: The ESD caution has been moved here from <u>Section 1.</u> 				
BFU690F v.1	20101216	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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BFU690F

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11 of 12

BFU690F

NPN wideband silicon RF transistor

14. Contents

1	Product profile 1
1.1	General description 1
1.2	Features and benefits 1
1.3	Applications 1
1.4	Quick reference data 1
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values 2
6	Thermal characteristics 3
7	Characteristics 4
8	Package outline 8
9	Handling information 9
10	Abbreviations
11	Revision history 9
12	Legal information 10
12.1	Data sheet status 10
12.2	Definitions 10
12.3	Disclaimers
12.4	Trademarks 11
13	Contact information 11
14	Contents 12

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Date of release: 14 March 2014 Document identifier: BFU690F