# VS-VSKCS440/030

**Vishay Semiconductors** 



## ADD-A-PAK Generation VII Power Modules Schottky Rectifier, 440 A



ADD-A-PAK

PRODUCT SUMMARY			
I <sub>F(AV)</sub>	440 A		
V <sub>R</sub>	30 V		
Package	ADD-A-PAK		
Circuit	Two diodes common cathodes		

### **MECHANICAL DESCRIPTION**

The ADD-A-PAK generation VII, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

### FEATURES

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Low thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- High surge capability
- Easy mounting on heatsink

### **ELECTRICAL DESCRIPTION**

The VS-VSKCS440/030 Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	440	А	
V <sub>RRM</sub>		30	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	27 000	А	
V <sub>F</sub>	200 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.61	V	
TJ	Range	-55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-VSKCS440/030	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	30	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	50	v	

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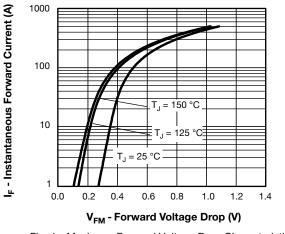
ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per module		50 % duty cycle at $T_C$ = 97 °C, rectangular waveform		440	
forward current	per leg	I <sub>F(AV)</sub>			220	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	27 000	A	
non-repetitive surge current		IFSM	10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	3000	
Non-repetitive avalanche energ	у	E <sub>AS</sub>	$E_{AS}$ T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 20 A, L = 1 mH		198	mJ
Repetitive avalanche current		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical 44		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V <sub>FM</sub>	220 A	T <sub>J</sub> = 25 °C	0.68	v
Maximum forward voltage drop		440 A		1.0	
Maximum forward voltage drop		220 A	T <sub>J</sub> = 125 °C	0.61	
		440 A		0.93	
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	20	mA
Maximum reverse leakage current		T <sub>J</sub> = 125 °C		1120	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		14 800	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C	
Maximum thermal resistance, junction to case per leg		R <sub>thJC</sub>	DC operation	0.26	°C/W	
Typical thermal resistance, case to heatsink per module		R <sub>thCS</sub>		0.1	C/W	
				75	g	
Approximate weight				2.7	oz.	
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torqu should be rechecked after a period of 3 h to allow for th		Nm	
	busbar		spread of the compound.	3		
Case style			JEDEC®	TO-240AA co	ompatible	

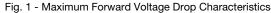
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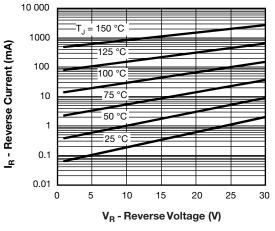
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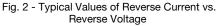


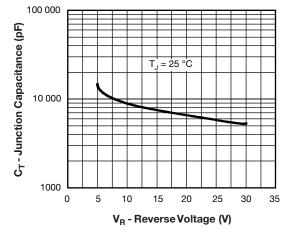
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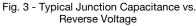
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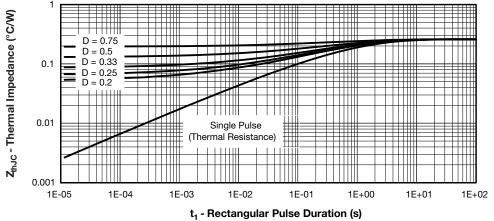






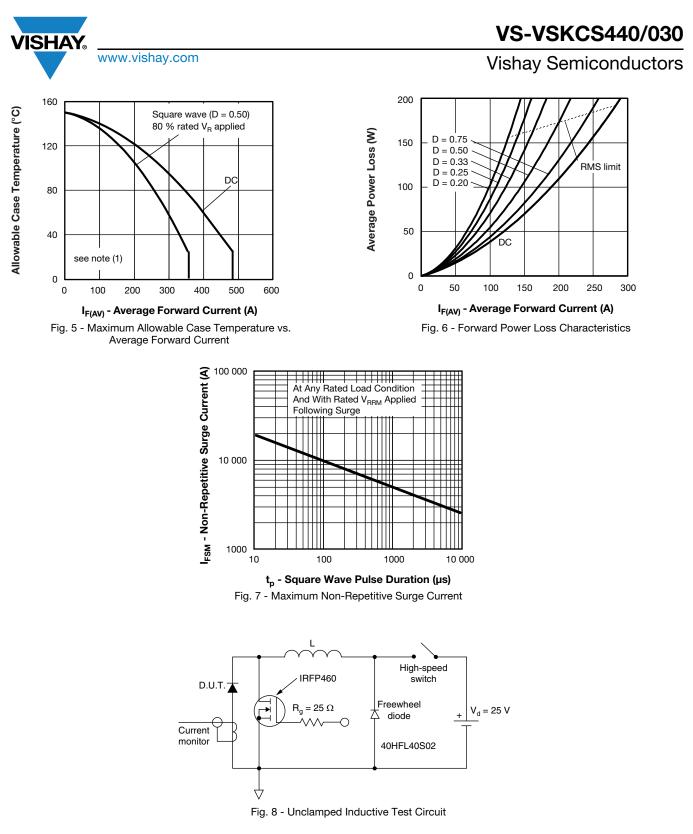








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#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6);
- $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 D)$ ;  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$

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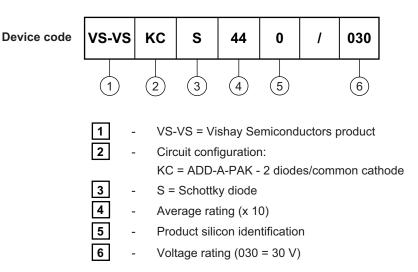
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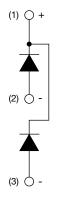
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### **ORDERING INFORMATION TABLE**



### **CIRCUIT CONFIGURATION**



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95369		

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## **ADD-A-PAK Generation VII - Diode**

### **DIMENSIONS** in millimeters (inches)





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