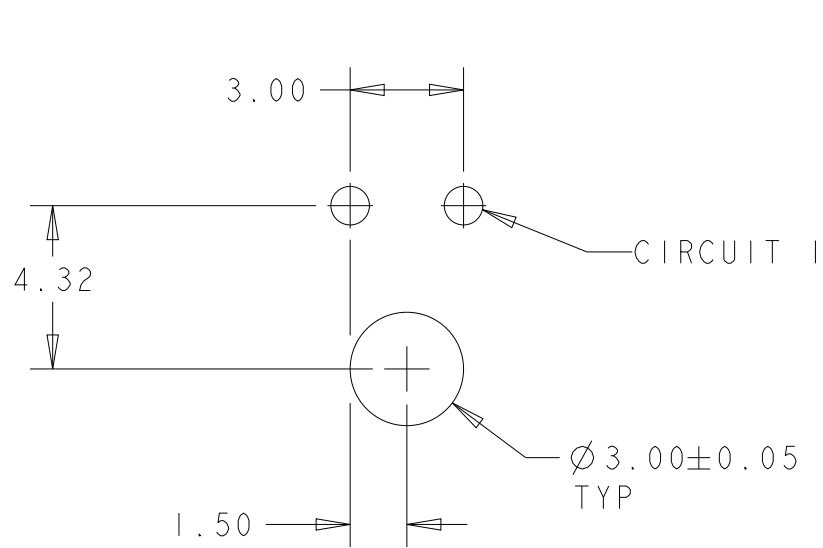
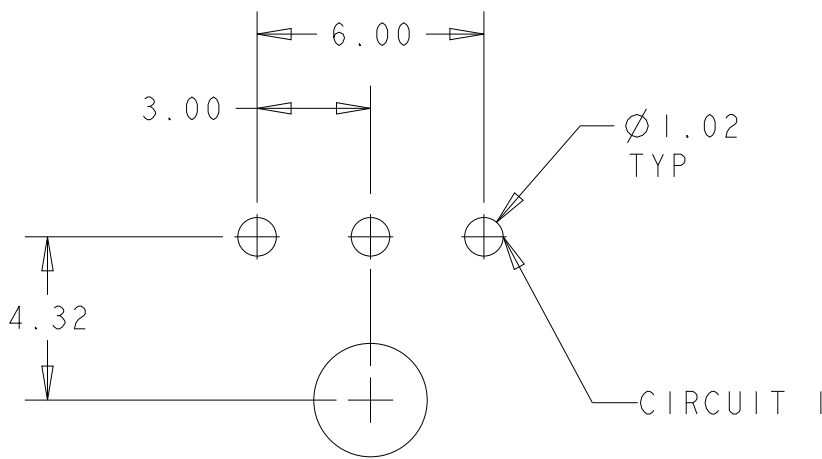


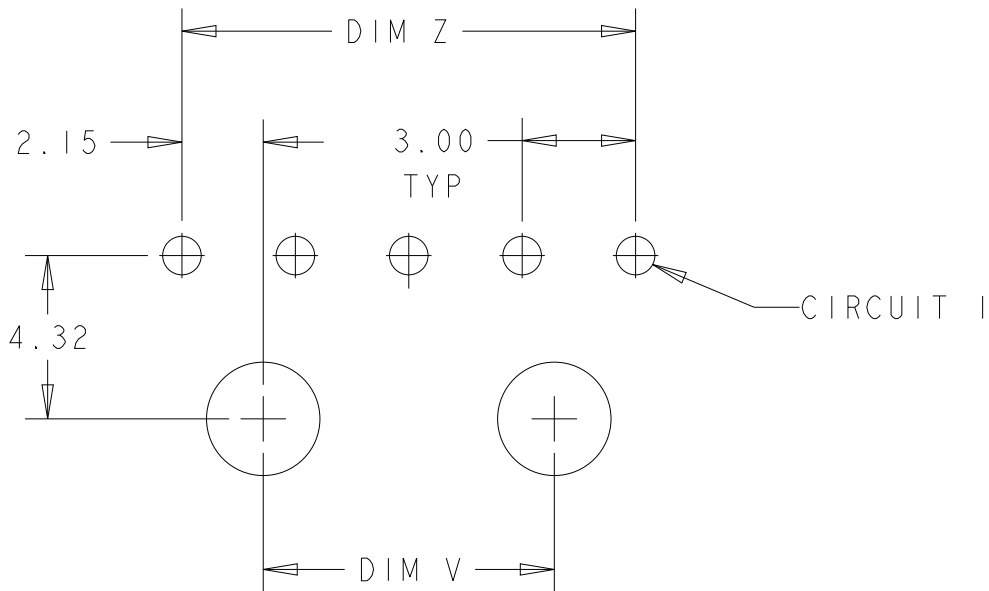
P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-



PCB LAYOUT FOR 2 POSITION 
COMPONENT SIDE



PCB LAYOUT FOR 3 POSITION 
COMPONENT SIDE



PCB LAYOUT FOR 4-12 POSITION 
COMPONENT SIDE