

FGH75T65SQDNL4

IGBT - Field Stop IV/4 Lead

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop IV Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. In addition, this new device is packaged in a TO-247-4L package that provides significant reduction in E_{on} Losses compared to standard TO-247-3L package. The IGBT is well suited for UPS and solar applications. Incorporated into the device is a soft and fast co-packaged free wheeling diode with a low forward voltage.

Features

- Extremely Efficient Trench with Field Stop Technology
- $T_{Jmax} = 175^{\circ}C$
- Improved Gate Control Lowers Switching Losses
- Separate Emitter Drive Pin
- TO-247-4L for Minimal E_{on} Losses
- Optimized for High Speed Switching
- These are Pb-Free Devices

Typical Applications

- Solar Inverter
- Uninterruptible Power Inverter Supplies (UPS)
- Neutral Point Clamp Topology

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V_{CES}	650	V
Collector current @ $T_c = 25^{\circ}C$ @ $T_c = 100^{\circ}C$	I_C	200 75	A
Diode Forward Current @ $T_c = 25^{\circ}C$ @ $T_c = 100^{\circ}C$	I_F	200 75	A
Diode Pulsed Current T_{PULSE} Limited by T_J Max	I_{FM}	200	A
Pulsed collector current, T_{pulse} limited by T_{Jmax}	I_{CM}	200	A
Gate-emitter voltage	V_{GE}	± 20	V
Transient gate-emitter voltage ($T_{PULSE} = 5 \mu s$, $D < 0.10$)		± 30	V
Power Dissipation @ $T_c = 25^{\circ}C$ @ $T_c = 100^{\circ}C$	P_D	375 188	W
Operating junction temperature range	T_J	-55 to $+175$	$^{\circ}C$
Storage temperature range	T_{stg}	-55 to $+175$	$^{\circ}C$
Lead temperature for soldering, 1/8" from case for 5 seconds	T_{SLD}	260	$^{\circ}C$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



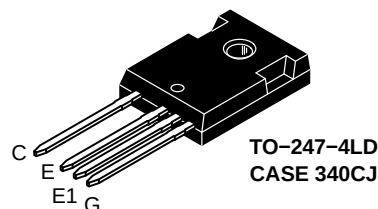
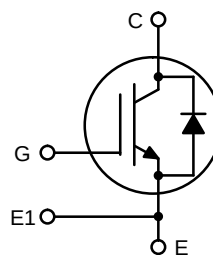
ON Semiconductor®

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75 A, 650 V

$V_{CEsat} = 1.50$ V

$E_{on} = 1.25$ mJ



MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
FGH75T65SQDNL4	TO-247 (Pb-Free)	30 Units / Rail

FGH75T65SQDNL4

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{\theta JC}$	0.4	°C/W
Thermal resistance junction-to-case, for Diode	$R_{\theta JC}$	0.65	°C/W
Thermal resistance junction-to-ambient	$R_{\theta JA}$	40	°C/W

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
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STATIC CHARACTERISTIC

Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0\text{ V}, I_C = 500\text{ }\mu\text{A}$	$V_{(BR)CES}$	650	–	–	V
Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}, I_C = 75\text{ A}$ $V_{GE} = 15\text{ V}, I_C = 75\text{ A}, T_J = 175^\circ\text{C}$	V_{CEsat}	– –	1.43 1.73	2.1 –	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_C = 250\text{ }\mu\text{A}$	$V_{GE(th)}$	3.6	4.0	4.35	V
Collector-emitter cut-off current, gate-emitter short-circuited	$V_{GE} = 0\text{ V}, V_{CE} = 650\text{ V}$ $V_{GE} = 0\text{ V}, V_{CE} = 650\text{ V}, T_J = 175^\circ\text{C}$	I_{CES}	– –	– 6.0	0.25 –	mA
Gate leakage current, collector-emitter short-circuited	$V_{GE} = 20\text{ V}, V_{CE} = 0\text{ V}$	I_{GES}	–	–	±250	nA

DYNAMIC CHARACTERISTIC

Input capacitance	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{ies}	–	5100	–	pF
Output capacitance		C_{oes}	–	115	–	
Reverse transfer capacitance		C_{res}	–	12	–	
Gate charge total	$V_{CE} = 400\text{ V}, I_C = 75\text{ A}, V_{GE} = 15\text{ V}$	Q_g	–	152	–	nC
Gate to emitter charge		Q_{ge}	–	29	–	
Gate to collector charge		Q_{gc}	–	39	–	

SWITCHING CHARACTERISTIC, INDUCTIVE LOAD

Turn-on delay time	$T_J = 25^\circ\text{C}$ $V_{CC} = 400\text{ V}, I_C = 75\text{ A}$ $R_g = 10\text{ }\Omega$ $V_{GE} = 15\text{ V}$	$t_{d(on)}$	–	44	–	ns
Rise time		t_r	–	49	–	
Turn-off delay time		$t_{d(off)}$	–	208	–	
Fall time		t_f	–	66	–	
Turn-on switching loss		E_{on}	–	1.25	–	mJ
Turn-off switching loss		E_{off}	–	1.26	–	
Total switching loss		E_{ts}	–	2.51	–	
Turn-on delay time	$T_J = 175^\circ\text{C}$ $V_{CC} = 400\text{ V}, I_C = 75\text{ A}$ $R_g = 10\text{ }\Omega$ $V_{GE} = 15\text{ V}$	$t_{d(on)}$	–	45	–	ns
Rise time		t_r	–	54	–	
Turn-off delay time		$t_{d(off)}$	–	227	–	
Fall time		t_f	–	70	–	
Turn-on switching loss		E_{on}	–	1.82	–	mJ
Turn-off switching loss		E_{off}	–	1.42	–	
Total switching loss		E_{ts}	–	3.24	–	

DIODE CHARACTERISTIC

Forward voltage	$V_{GE} = 0\text{ V}, I_F = 75\text{ A}$ $V_{GE} = 0\text{ V}, I_F = 75\text{ A}, T_J = 175^\circ\text{C}$	V_F	– –	1.60 1.70	2.0 –	V
Reverse recovery time	$T_J = 25^\circ\text{C}$ $I_F = 75\text{ A}, V_R = 200\text{ V}$ $di_F/dt = 200\text{ A}/\mu\text{s}$	t_{rr}	–	134	–	ns
Reverse recovery charge		Q_{rr}	–	0.78	–	μC
Reverse recovery current		I_{rrm}	–	10	–	A
Reverse recovery time	$T_J = 175^\circ\text{C}$ $I_F = 75\text{ A}, V_R = 200\text{ V}$ $di_F/dt = 200\text{ A}/\mu\text{s}$	t_{rr}	–	202	–	ns
Reverse recovery charge		Q_{rr}	–	2.54	–	μC
Reverse recovery current		I_{rrm}	–	20.2	–	A

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

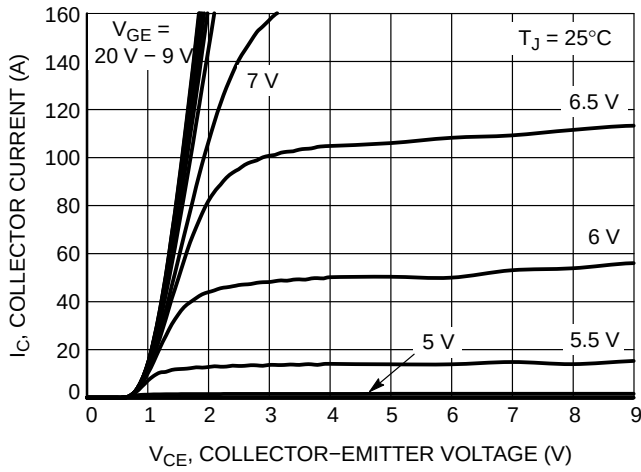


Figure 1. Output Characteristics

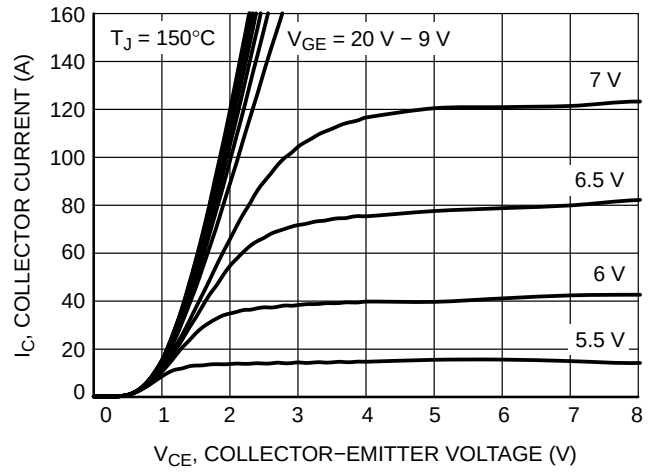


Figure 2. Output Characteristics

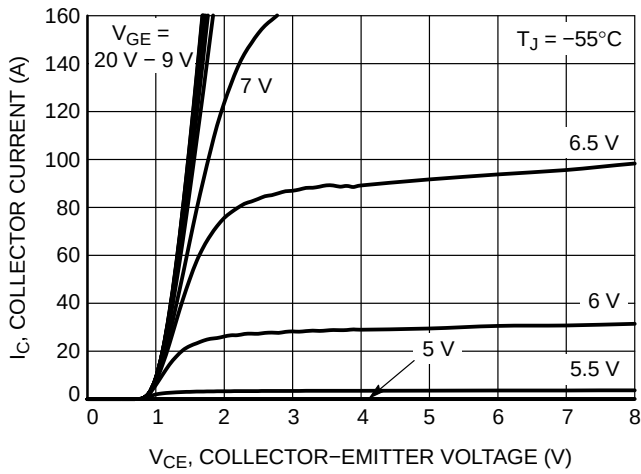


Figure 3. Output Characteristics

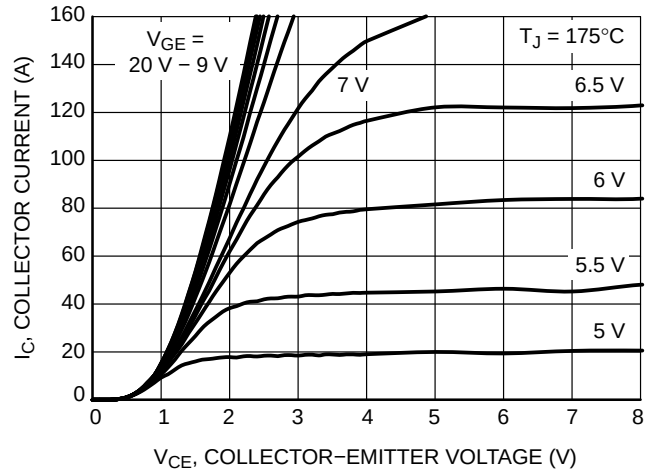


Figure 4. Output Characteristics

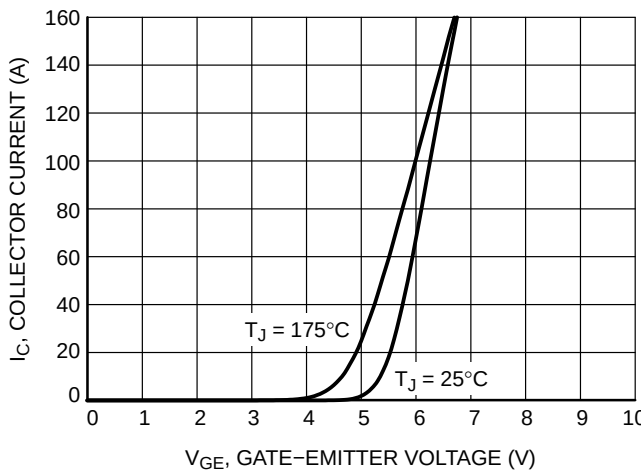


Figure 5. Typical Transfer Characteristics

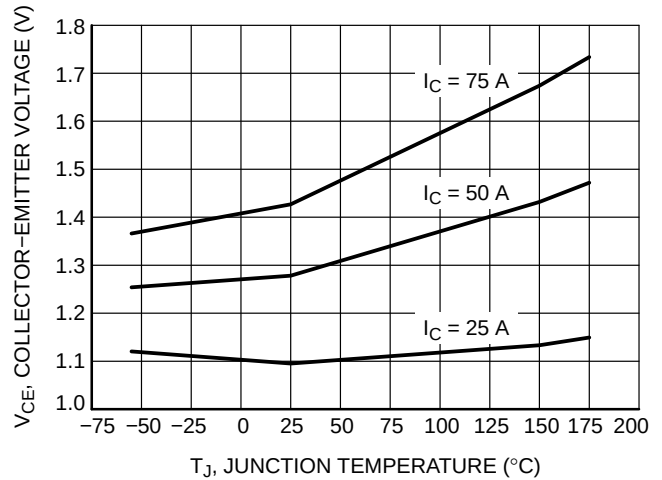


Figure 6. $V_{CE(sat)}$ vs. T_J

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TYPICAL CHARACTERISTICS

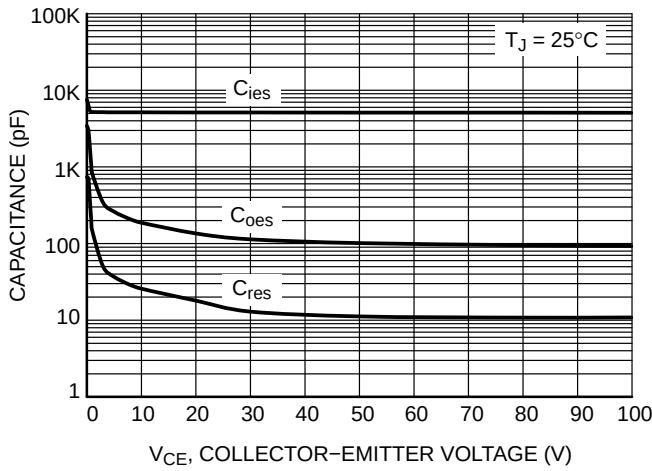


Figure 7. Typical Capacitance

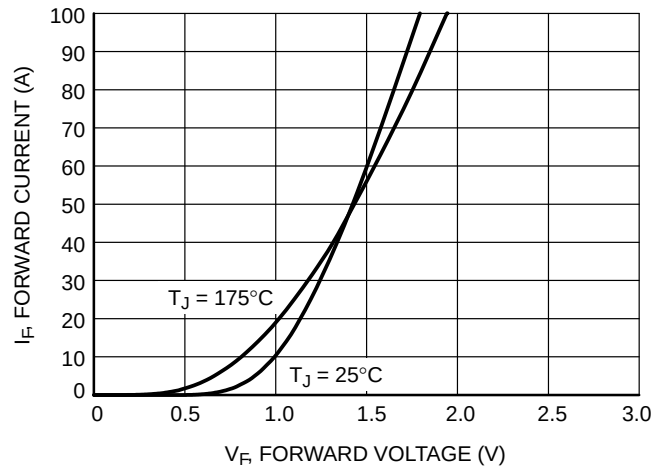


Figure 8. Diode Forward Characteristics

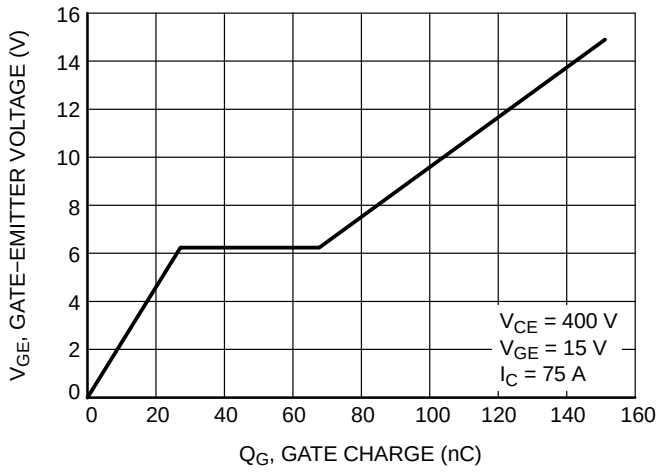


Figure 9. Typical Gate Charge

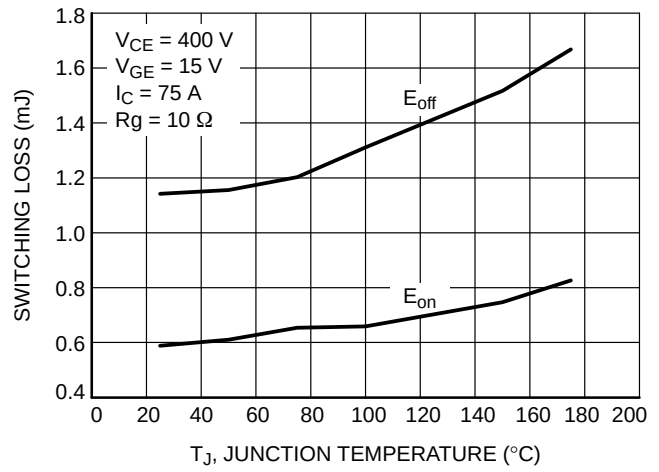


Figure 10. Switching Loss vs. Temperature

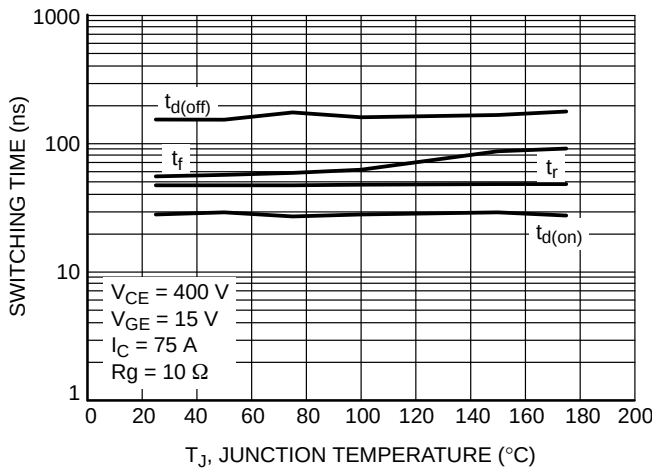


Figure 11. Switching Time vs. Temperature

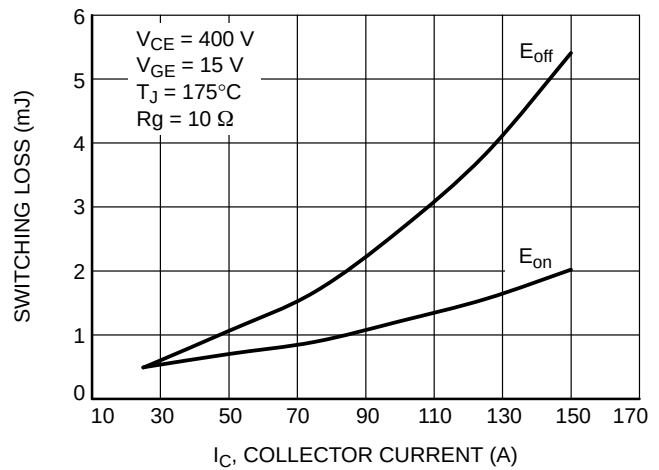


Figure 12. Switching Loss vs. I_C

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TYPICAL CHARACTERISTICS

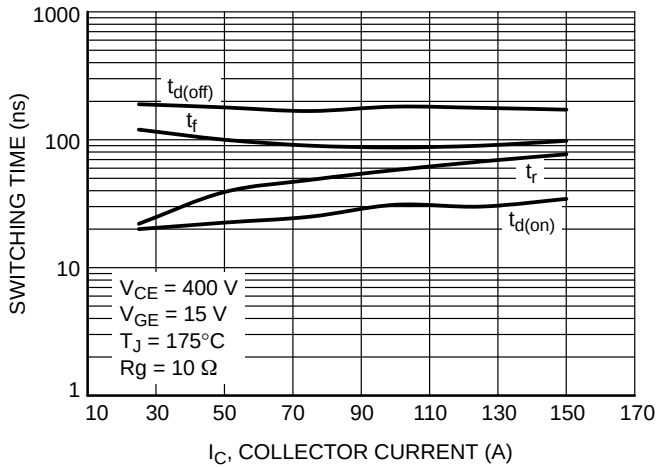


Figure 13. Switching Time vs. I_C

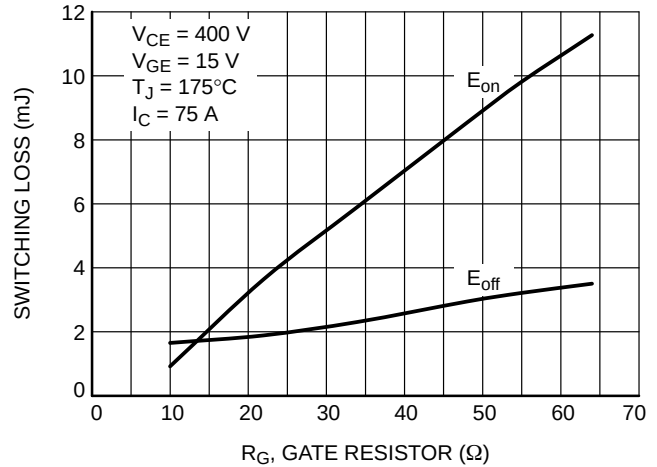


Figure 14. Switching Loss vs. R_G

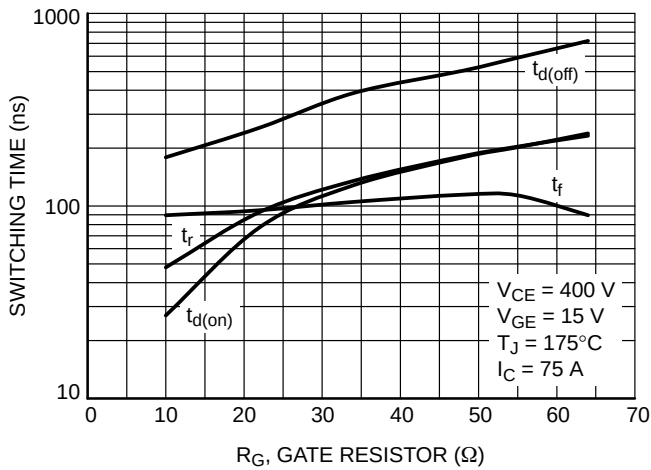


Figure 15. Switching Time vs. R_G

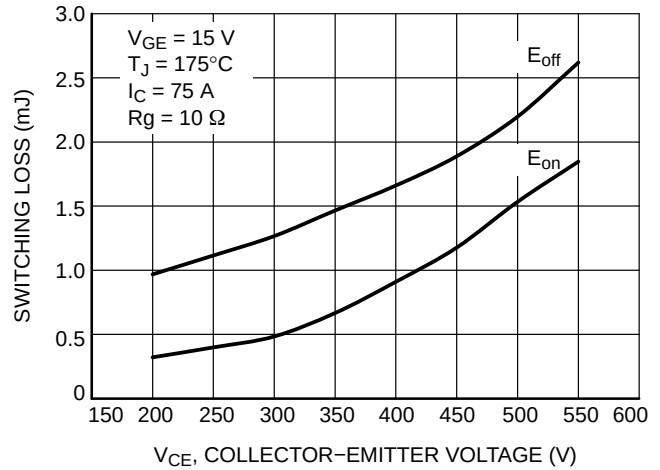


Figure 16. Switching Loss vs. V_{CE}

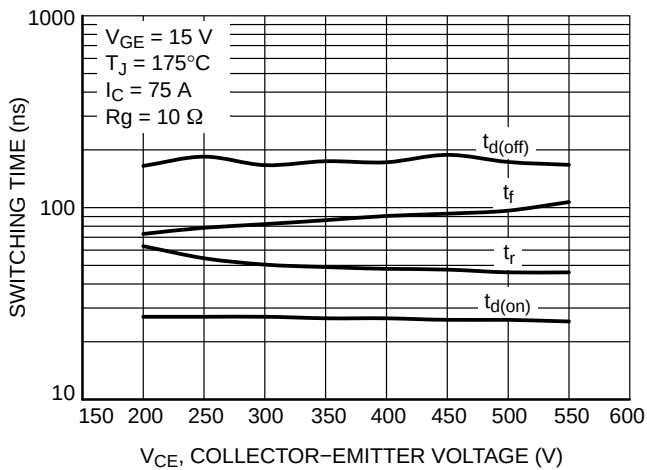


Figure 17. Switching Time vs. V_{CE}

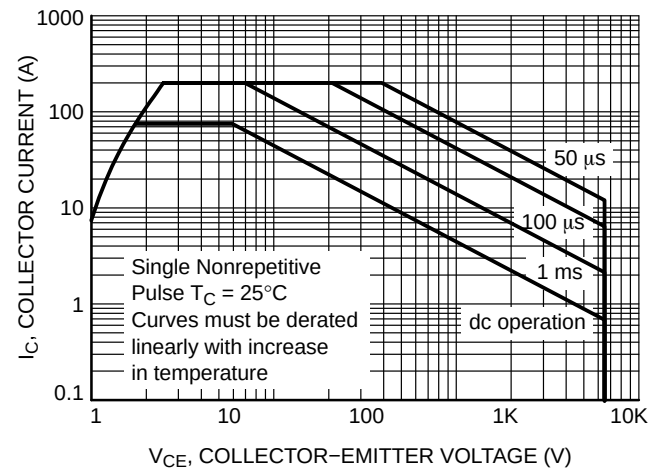


Figure 18. Safe Operating Area

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TYPICAL CHARACTERISTICS

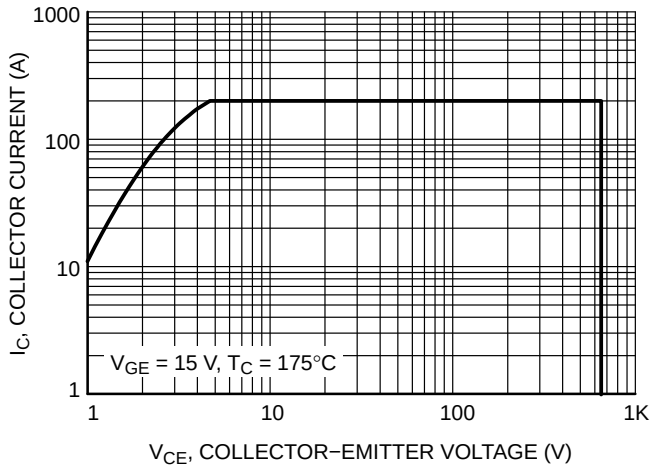


Figure 19. Reverse Bias Safe Operating Area

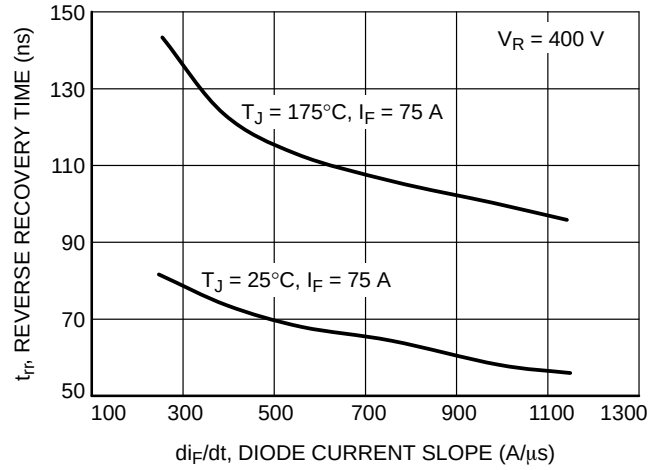


Figure 20. t_{rr} vs. di_F/dt

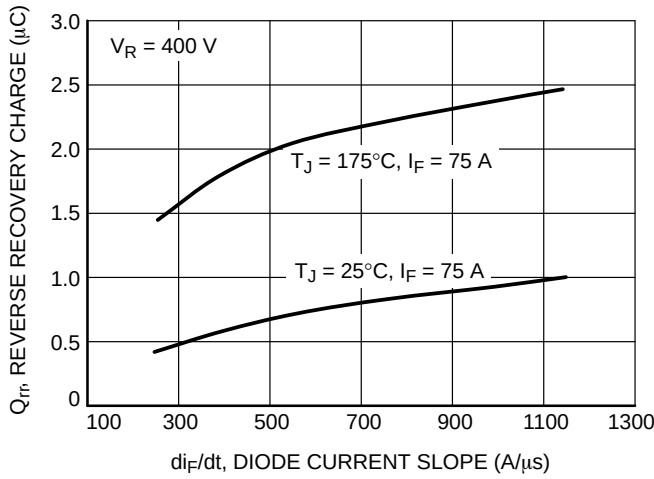


Figure 21. Q_{rr} vs. di_F/dt

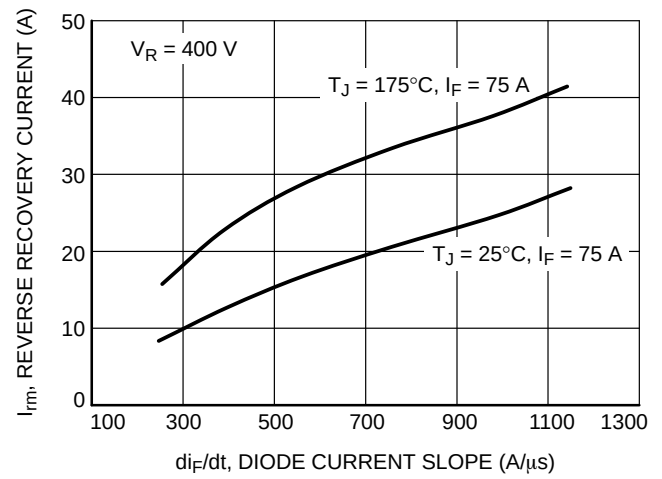


Figure 22. I_{rm} vs. di_F/dt

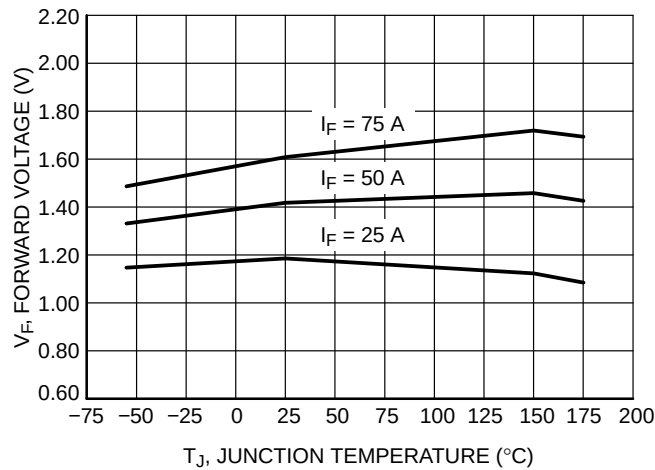


Figure 23. V_F vs. T_J

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TYPICAL CHARACTERISTICS

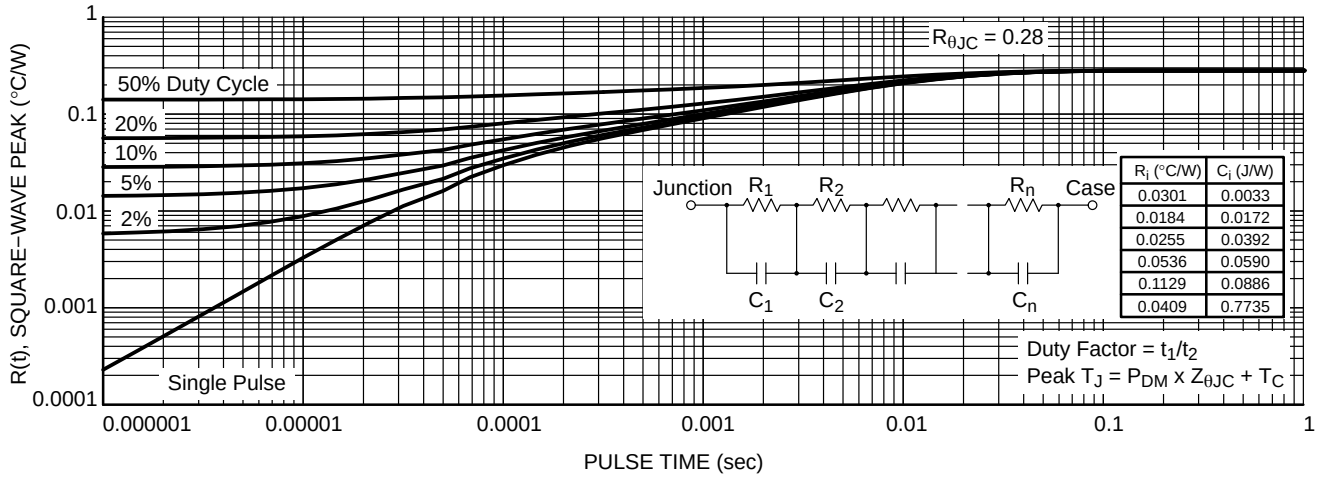


Figure 24. IGBT Transient Thermal Impedance

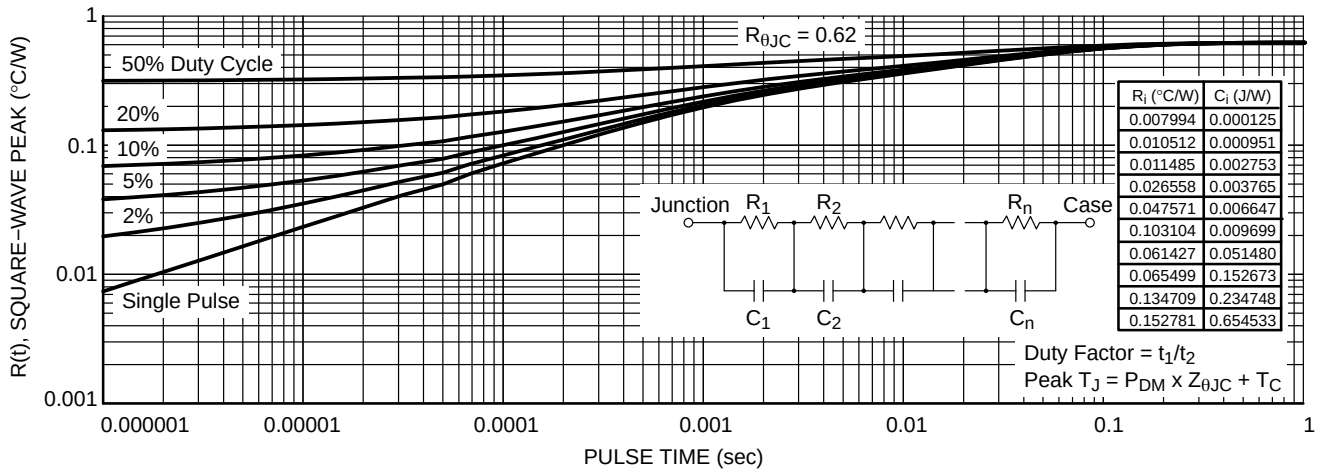


Figure 25. Diode Transient Thermal Impedance

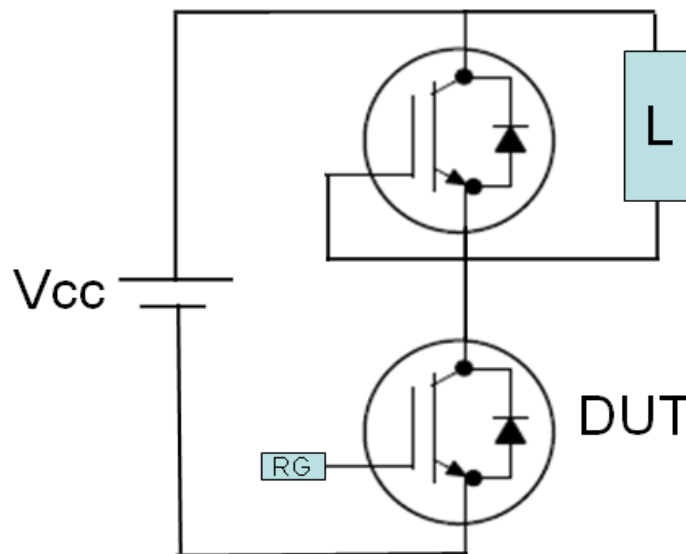


Figure 26. Test Circuit for Switching Characteristics

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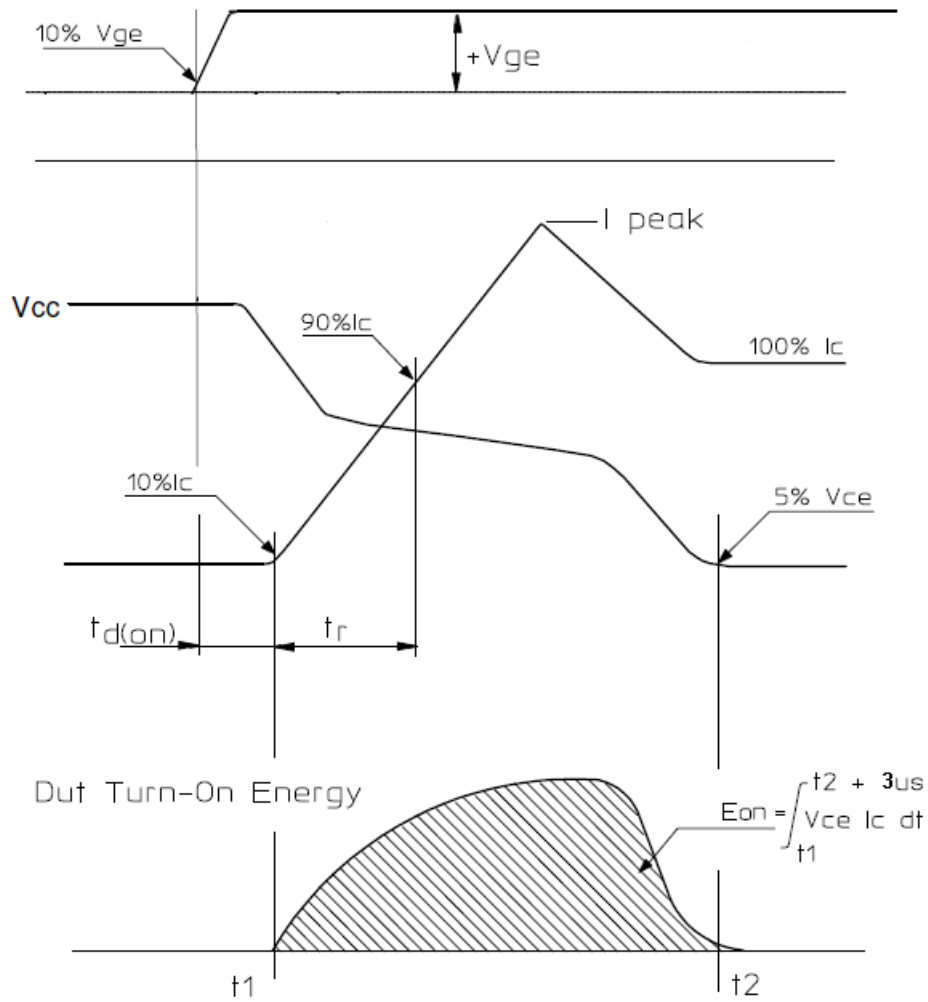


Figure 27. Definition of Turn On Waveform

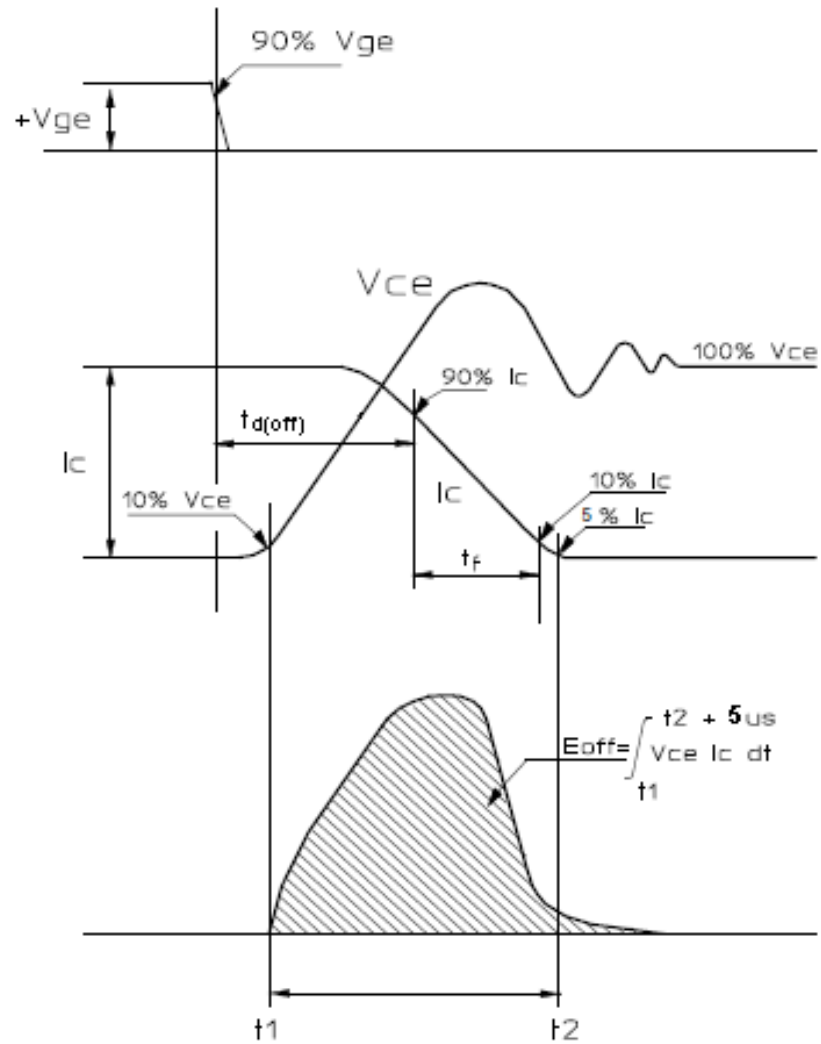
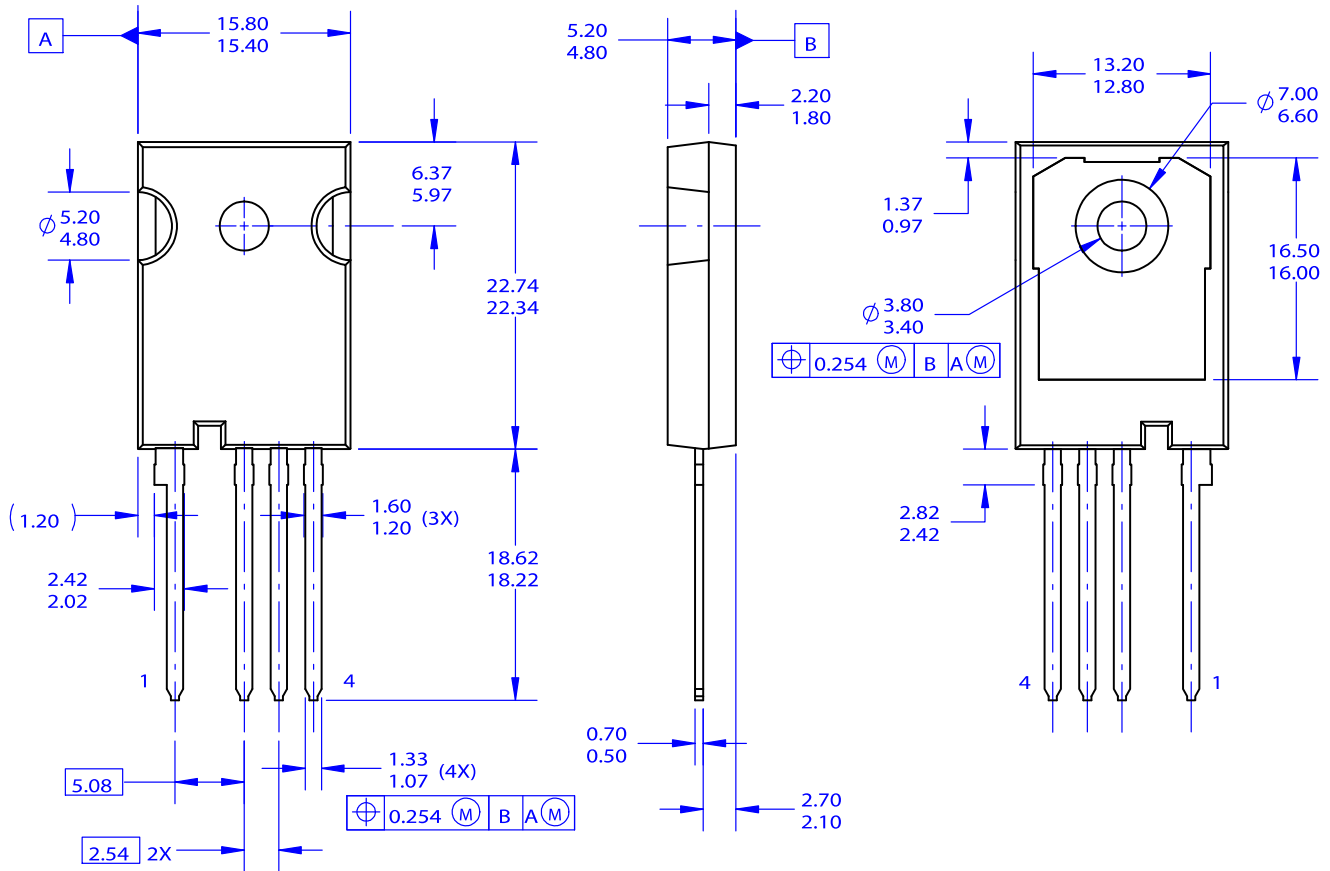


Figure 28. Definition of Turn Off Waveform

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PACKAGE DIMENSIONS

TO-247-4LD
CASE 340CJ
ISSUE O



NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DRAWING CONFORMS TO ASME Y14.5-2009.

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