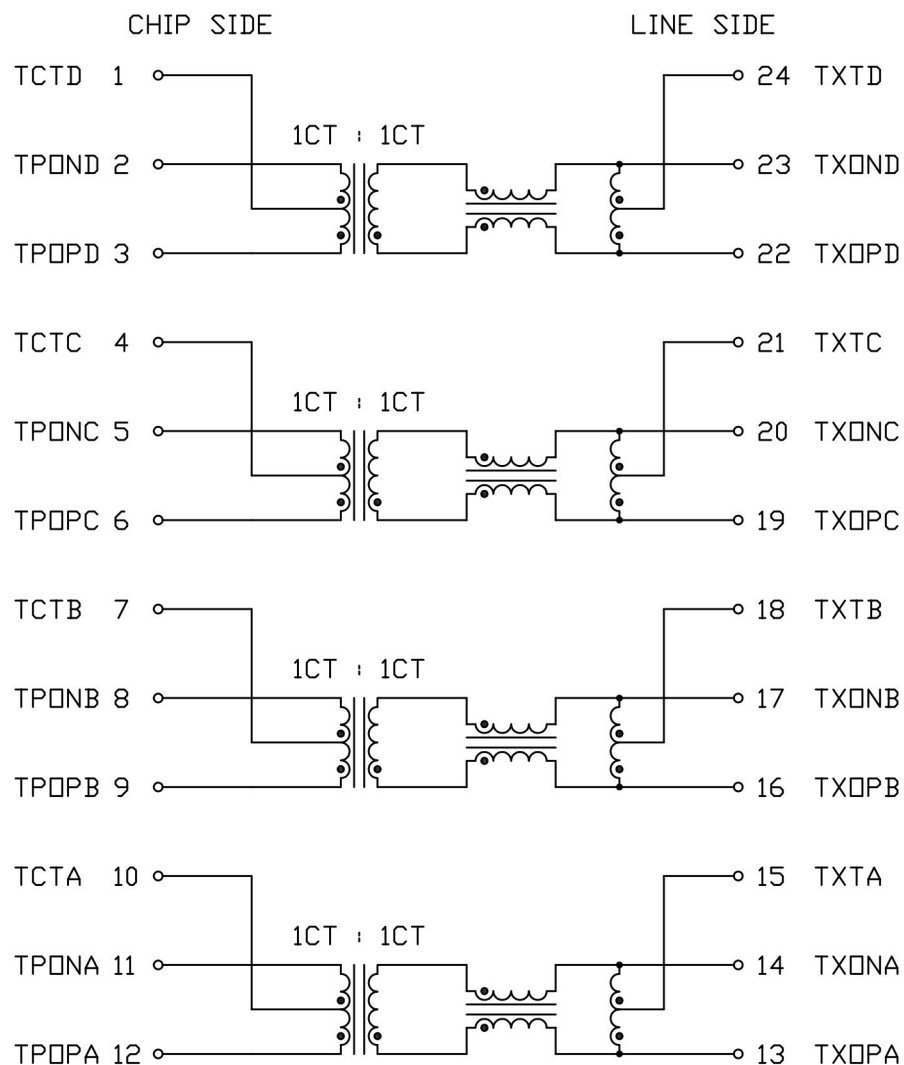




SCHEMATIC

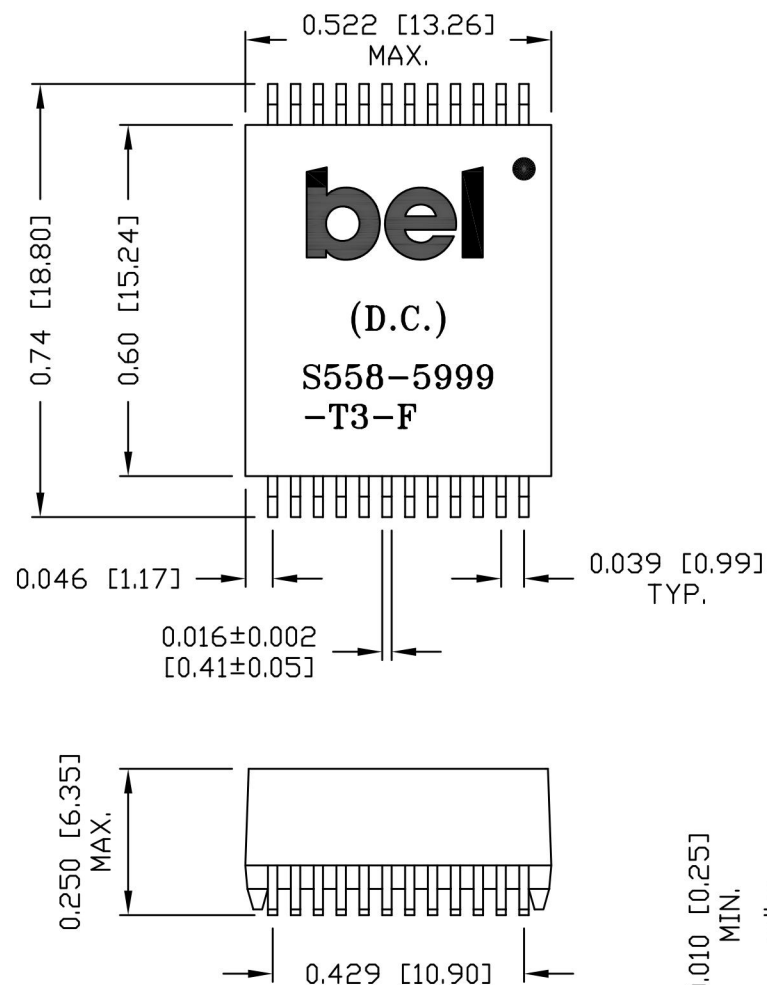


ELECTRICAL CHARACTERISTICS @25°C

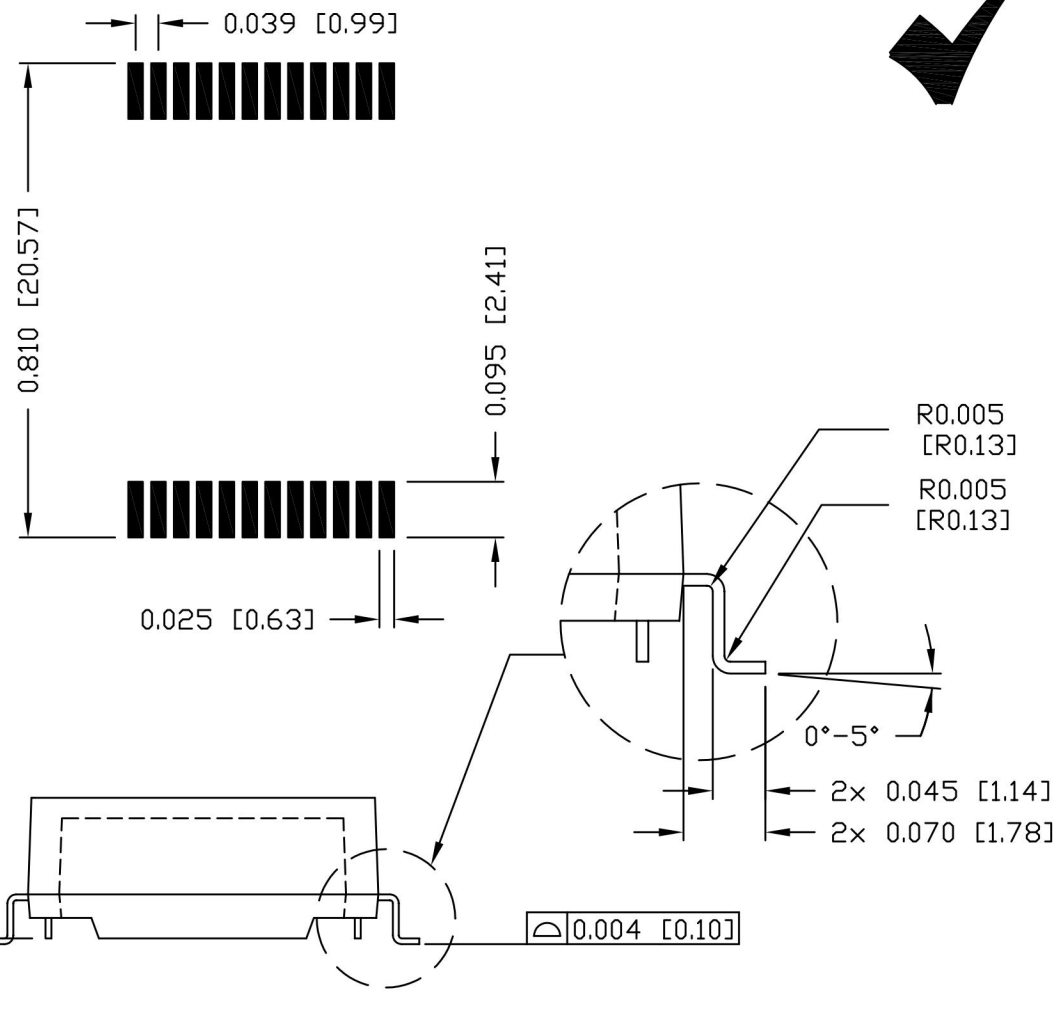
TURNS RATIO	(3-1-2) : (22-24-23)	1CT : 1CT
	(6-4-5) : (19-21-20)	1CT : 1CT
	(9-7-8) : (16-18-17)	1CT : 1CT
	(12-10-11) : (13-15-14)	1CT : 1CT
PHASE	PER DOT CONVENTION	
□CL	PIN (23-22), (20-19)	350μH MIN.
	PIN (17-16), (14-13)	@100kHz, 100mV WITH 8mA DC BIAS
INSERTION LOSS		1.1dB MAX. @0.1 TO 1MHz
		1.0dB MAX. @1 TO 80MHz
		2.0dB MAX. @125MHz
RETURN LOSS	UNDER 100 OHM ±15% ENVIRONMENT	18dB MIN. @0.5 TO 40MHz
		12-20LOG (f/80MHz) MIN. @40 TO 100 MHz
COMMON TO COMMON REJECTION		30dB MIN. @1 TO 100 MHz
CROSSTALK		33-20LOG (f/100MHz) dB MIN. @ 1 TO 100 MHz
HIPOT		PER HAND-WORK-03

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REF.	REV. : B	PAGE : 2
Alice Pang	2015-10-28	ELECTRICAL SPECIFICATION S558-5999-T3-F	X5585999T3-F	TOL. IN INCH	UNIT : INCH [mm]		
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A		
ZC Guo	2015-10-28		X5585999T3FB.DWG	.XX	SIZE : A4		
				.XXX			

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SUGGESTED PCB PAD LAYOUT



RoHS



NOTES:

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "QTS001".

ORIGINATED BY	DATE	TITLE MECHANICAL DRAWING S558-5999-T3-F	PART NO. / DRAWING NO.	STANDARD DIM.		[] METRIC DIM. AS REF.		 POWER PROTECT CONNECT
Lawrence Tsang	2015-10-28		X5585999T3-F	TOL. IN INCH		UNIT : INCH [mm]		
DRAWN BY	DATE		FILE NAME X5585999T3FB.DWG	.X		SCALE : N/A		
ZC Guo	2015-10-28			.XX	±0.01	 SIZE : A4		
				.XXX	±0.005			