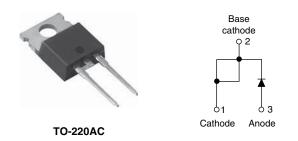
**Vishay Semiconductors** 





PRODUCT SUMMARY							
Package	TO-220AC						
I <sub>F(AV)</sub>	30 A						
V <sub>R</sub>	600 V						
V <sub>F</sub> at I <sub>F</sub>	2.60 V						
t <sub>rr</sub> (typ.)	23 ns						
T <sub>J</sub> max.	175 °C						
Diode variation	Single die						

#### FEATURES

- Reduced Q<sub>rr</sub> and soft recovery
- 175 °C T<sub>J</sub> maximum
- For PFC CRM/CCM operation
- Low forward voltage drop
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION/APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 103 °C	30	٨					
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	200	A					
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-					
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A	-	2.0	2.6	V				
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.34	1.75					
Reverse leakage current	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	0.3	50					
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	60	500	μA				
Junction capacitance	CT	C <sub>T</sub> V <sub>R</sub> = 600 V		33	-	pF				
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH				

Revision: 10-Oct-11

1



**RoHS** COMPLIANT

HALOGEN

FREE



For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



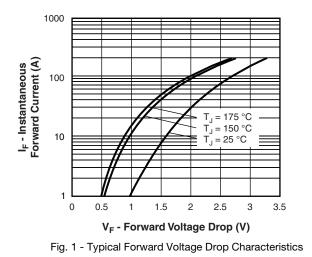
www.vishay.com

### Vishay Semiconductors

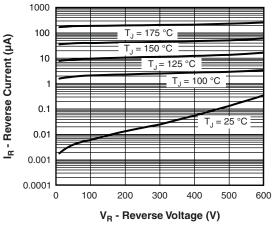
<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1 \text{ A}, dI_F/dt = 50$	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		28	35			
	t <sub>rr</sub>	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 10$	-	23	30	ns			
		T <sub>J</sub> = 25 °C		-	31	-	115		
		T <sub>J</sub> = 125 °C	$I_F = 30 A$	-	77	-			
Deels receiver a current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	3.5	-	•		
Peak recovery current		T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/µs V <sub>B</sub> = 200 V	-	7.7	-	A		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	65	-			
		T <sub>J</sub> = 125 °C		-	345	-	nC		

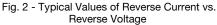
THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C				
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.7	1.1					
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W				
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-					
Weight			-	2.0	-	g				
weight			-	0.07	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-220AC	30ETH06							

### **Vishay Semiconductors**



www.vishay.com





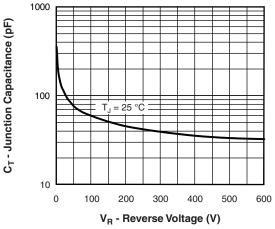


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

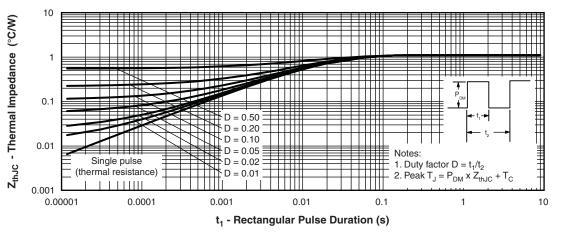


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

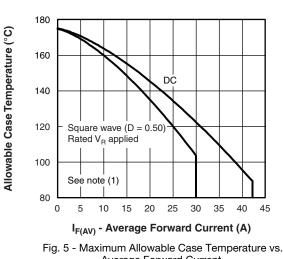
Revision: 10-Oct-11

Document Number: 94019

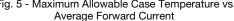
For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



**Vishay Semiconductors** 



www.vishay.com



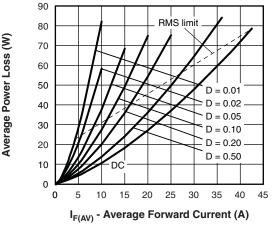
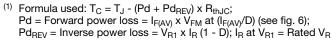
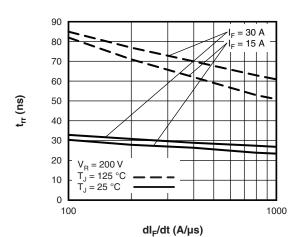


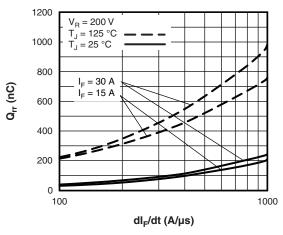
Fig. 6 - Forward Power Loss Characteristics

#### Note











Revision: 10-Oct-11 Document Number: 94019 4 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



**Vishay Semiconductors** 

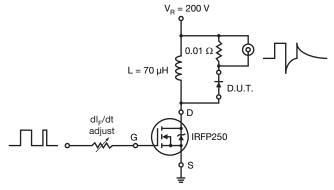
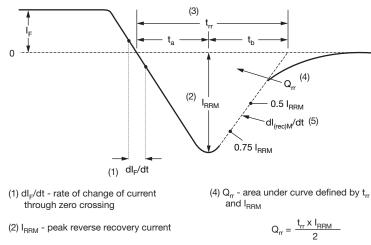


Fig. 9 - Reverse Recovery Parameter Test Circuit



(3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through 0.75  $I_{RRM}$  and 0.50  $I_{RRM}$ extrapolated to zero current.

(5) dI\_{(rec)M}/dt - peak rate of change of current during  $t_{\rm b}$  portion of  $t_{\rm rr}$ 

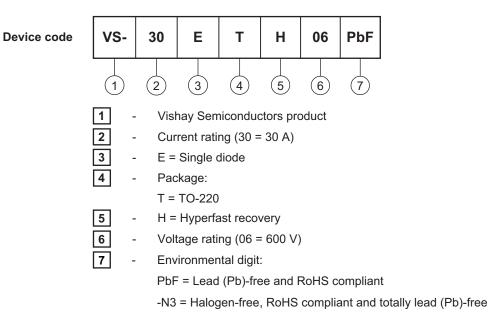
Fig. 10 - Reverse Recovery Waveform and Definitions

N TABLE

**Vishay Semiconductors** 

### **ORDERING INFORMATION TABLE**

www.vishay.com



ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-30ETH06PbF	50	1000	Antistatic plastic tube						
VS-30ETH06-N3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS								
Dimensions		www.vishay.com/doc?95221						
Port marking information	TO-220ACPbF	www.vishay.com/doc?95224						
Part marking information	TO-220AC-N3	www.vishay.com/doc?95068						
SPICE model		www.vishay.com/doc?95422						



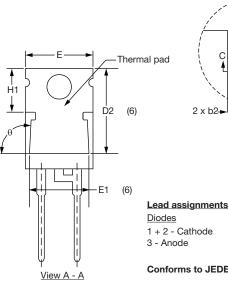
**Vishay Semiconductors** 

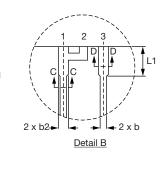
**TO-220AC** 

plane

#### **DIMENSIONS** in millimeters and inches









**Diodes** 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

⊕ 0.015 **()** BA()

SYMBOL	MILLIN	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	STNDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6		θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6							

Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(4)</sup> Dimension b1, b3 and c1 apply to base metal only
- <sup>(5)</sup> Controlling dimension: inches
- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2 and E1
- <sup>(7)</sup> Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- <sup>(8)</sup> Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk and agree to fully indemnify and hold Vishay and its distributors harmless from and against any and all claims, liabilities, expenses and damages arising or resulting in connection with such use or sale, including attorneys fees, even if such claim alleges that Vishay or its distributor was negligent regarding the design or manufacture of the part. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

# **Material Category Policy**

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.