# TECHNOLOGY

Model No: MTÖ€Ï €€ÝZÕËH

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# **Record of Revision**

Date	Revision No.	Summary
2015-07-22	1.0	Rev 1.0 was issued
2015-08-21	1.1	Correct the definition of 27th pin
2016-07-04	1.2	Modify some details
2016-07-07	1.3	Update some dimensions on the outline drawing

#### 1. <u>Scope</u>

This data sheet is to introduce the specification **cZAH8**\$+\$\$LN; **!' žU**ctive matrix TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC and a backlight unit. The 7.0" display area contains 1024x3(RGB) x 600 pixels.

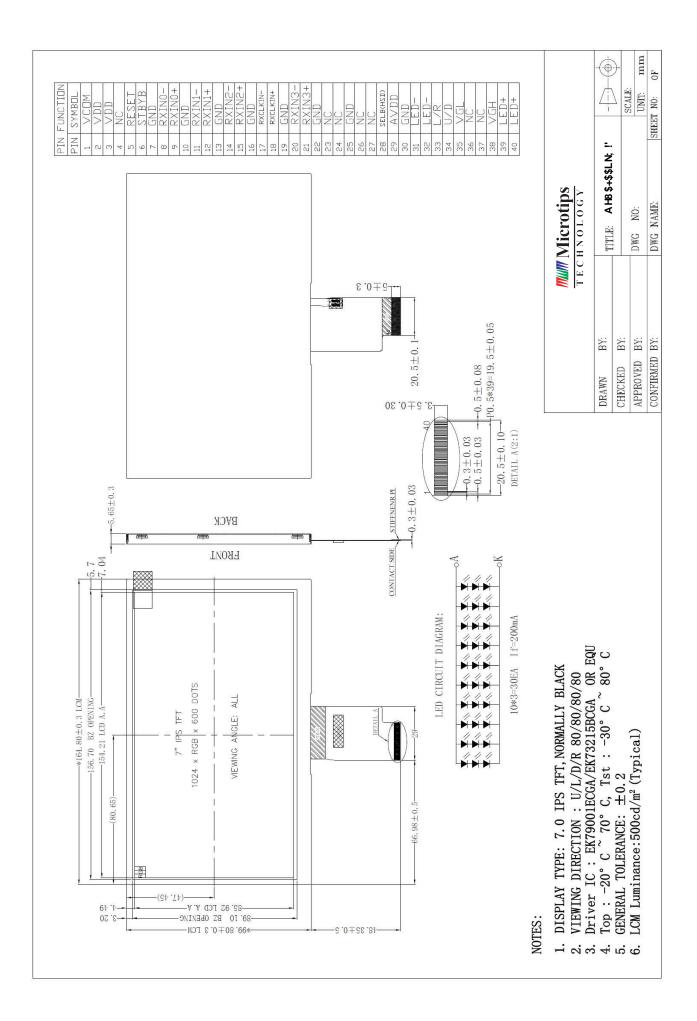
As to basic specification of the driver IC, refer to the IC specification and datasheet.

#### 2. Application

Digital equipments which need color display, mobile navigator/video systems.

#### 3. General Information

Item	Contents	Unit
Size	7.0	inch
Resolution	1024 x 3(RGB) x 600	/
Interface	LVDS	/
Technology type	IPS TFT	/
Pixel Configuration	R.G.B. Stripe	
Outline Dimension (W x H x D)	164.8 x 99.8 x 5.65	mm
Active Area	154.21 x 85.92	mm
Backlight Type	LED	/
Controller/ driver	EK79001ECGA/EK73215BCGA	
Weight	TBD	g



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# 5. Interface signals

Recommend connector Part No. FH12S-40S-0.5SH

No	Symbol	Description	Remark
1	VCOM	Common voltage	
2	VDD	Power supply	
3	VDD	Power supply	
4	NC	No connection	
5	RESET	Global reset pin	
6	STBYB	Standby mode	
7	GND	Power Ground	
8	RXIN0-	Differential Data Input ,CH0(Negative)	
9	RXIN0+	Differential Data Input ,CH0(Positive)	
10	GND	Power Ground	
11	RXIN1-	Differential Data Input ,CH1(Negative)	
12	RXIN1+	Differential Data Input ,CH1(Positive)	
13	GND	Power Ground	
14	RXIN2-	Differential Data Input ,CH2(Negative)	
15	RXIN2+	Differential Data Input ,CH2(Positive)	
16	GND	Power Ground	
17	RXCLKIN-	Differential Clock Input Negative)	
18	RXCLKIN+	Differential Clock Input ((Positive)	
19	GND	Power Ground	
20	RXIN3-	Differential Data Input ,CH3(Negative)	
21	RXIN3+	Differential Data Input ,CH3(Positive)	
22	GND	Power Ground	
23	NC	No connection	
24	NC	No connection	

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25	GND	Power Ground	
26	NC	No connection	
27	NC	No connection	
28	SELB	In LVDS interface connected HSD to FPC for Pin Setting HSD=" L" :8 bit HSD=" H" :6 bit	
29	AVDD	Power supply for analog circuits	
30	GND	Power Ground	
31	LED-	Power for LED backlight (Cathode)	
32	LED-	Power for LED backlight (Cathode)	
33	L/R	Source Right or Left sequence control.	Note
34	U/D	Gate Up or Down scan control.	Note
35	VGL	Gate OFF Voltage	
36	NC	No connection	
37	NC	No connection	
38	VGH	Gate ON Voltage	
39	LED+	Power for LED backlight (Anode)	
40	LED+	Power for LED backlight (Anode)	

Note:

UPDN	SHLR	FUNCTION			
0	1	Normal Display			
0	0	Inverse Left and Right			
1	1	Inverse Up and Down			
1	0	Inverse Left and Right Inverse Up and Down			

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#### 6. Absolute maximum Ratings

6.1. Electrical Absolute max. ratings

Parameter	Symbol	MIN	MAX	Unit	Remark
	VDD	-0.3	5.0	V	
Dowor Supply Voltage	AVDD	-0.5	15.0	V	
Power Supply Voltage	VGH	-0.3	40.0	V	
	VGL	-20	0.3	V	
Logic Signal Input /Output Voltage	VIOVCC	-0.3	VDD+0.5	V	
Current of LED	ILED	0	200	mA	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

- 2. Please be sure users are grounded when handing LCD Module.
- 6.2. Environment Conditions

ltem	Symbol	MIN	МАХ	Unit	Remark
Operating Temperature	TOPR	-20	70	°C	
Storage Temperature	TSTG	-30	80	°C	

Notes:

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature. The phenomenon is reversible.

3. Ta<=40°C :85%RH MAX.

Ta>=40°C : Absolute humidity must be lower than the humidity of 85%RH at 40°C.

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# 7. Electrical Specifications

#### 7.1 Electrical characteristics

GND=0V, Ta=25℃

ltem		Symbol	MIN	ТҮР	МАХ	Unit	Remark
		VDD	2.6	3.3	3.6	V	
		VCOM	2.85		3.45	V	
Power Supp	ly	AVDD	9.4	9.6	9.8	V	
			17	18	19	V	
		VGL	-6.6	-6.0	-5.4	V	
Input voltage	Н	V <sub>IH</sub>	0.8VDD		VDD	V	VDD=2.8V
vollage	L	V <sub>IL</sub>	0		0.2VDD	V	VDD-2.0V
	Current Consumption			30	45	mA	
Consump				35	45	mA	

#### 7.2 LED Backlight

Ta=25℃

ltem	Symbol	MIN	ТҮР	MAX	Unit	Remark
Forward Current	IL	-	200		mA	
Forward Voltage	VL	-	9.6	-	V	Note1
LED life time			25,000		Hr	Note2

Note:

1: The LED Supply Voltage is defined by the number of LED at Ta=25  $^\circ\!\!\mathbb{C}$  and IL =200mA.

2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at

Ta=25  $^{\circ}$ C and IL =140mA. The LED lifetime could be decreased if operating IL is larger than 200mA.

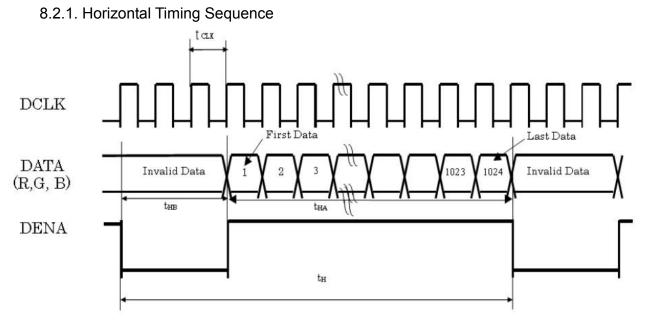
## 8. Command/AC Timing

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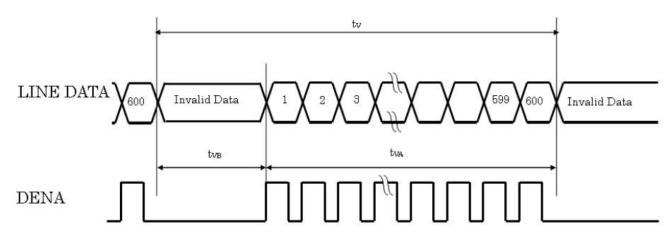
#### 8.1 Timing Characteristics of Input Signals

	SYMBOL	MIN	ТҮР	МАХ	UNIT			
LVDS Input Signal Sequence		CLK Fre	equency	tclk	45	51.2	57	MHz
	DENA		Horizontal Total Time	t <sub>H</sub>	1324	1344	1364	tCLK
		Horizontal	Horizontal Effective Time	t <sub>HA</sub>		1024		tCLK
LCD Input Signal Sequence			Horizontal Blank Time	t <sub>HB</sub>	300	320	340	tCLK
(Input LVDS Transmitter)		Vertical	Vertical Total Time	t <sub>V</sub>	625	635	645	t <sub>H</sub>
			Vertical Effective Time	t <sub>VA</sub>		600		t <sub>H</sub>
			Vertical Blank Time	t <sub>VB</sub>	25	35	45	t <sub>H</sub>

### 8.2 Timing Sequence(Timing Chart)



8.2.2 Vertical Timing Sequence



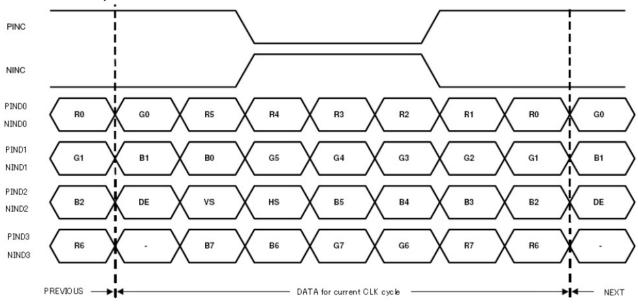
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#### 8.2.3 LVDS Input Data Mapping

6bits LVDS Input

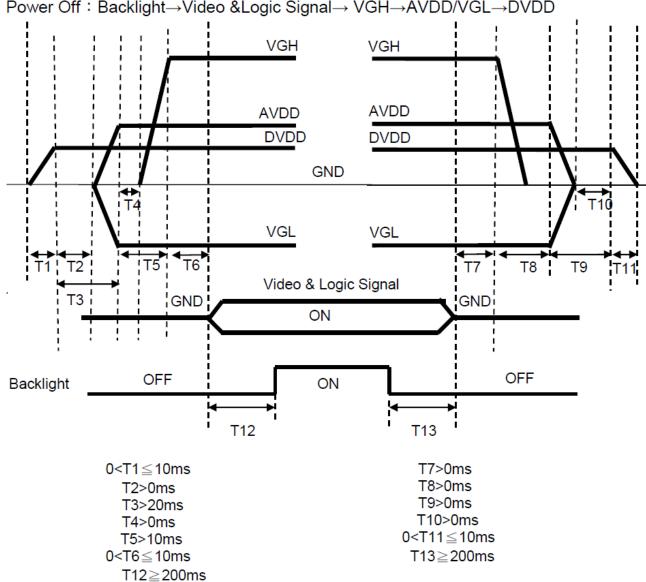
PINC NINC **PIND0** R0 GO R5 R4 R3 R2 R1 Ro G0 NINDO PIND1 G1 B1 Bo G5 G4 G3 G2 G1 B1 NIND1 PIND2 DE ٧S HS **B**2 **B**5 B3 **B**2 DE **B**4 NIND2 PREVIOUS **▶**|**4** -DATA for current CLK cycle **⊳**ا∢ NEXT





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8.3 Power ON/Off Sequence



Power On : DVDD $\rightarrow$ AVDD/VGL $\rightarrow$ VGH $\rightarrow$ Video &Logic Signal $\rightarrow$ Backlight Power Off : Backlight $\rightarrow$ Video &Logic Signal $\rightarrow$  VGH $\rightarrow$ AVDD/VGL $\rightarrow$ DVDD

9. Optical Specification

Ta=25℃

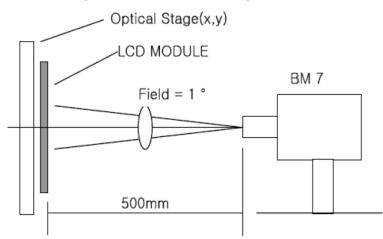
T E C H N O L O G Y

ltem		Symbol	Condition	Min	Тур.	Max.	Unit	Remark
Contrast Ratio		CR	θ=0°	-	800	-		Note1 Note2
Response Tim	е	Tr+Tf	<b>25</b> ℃	-	25	-	ms	Note1 Note3
		ΘΤ		-	80	-		
		ΘΒ	CR≧10	-	80	-	Dograa	Note 4
View Angles		ΘL	CR≡ IU	-	80	-	Degree	NOLE 4
		θR		-	80	-		
	White	х	Brightness is on		0.290	- - - Typ+0.05		
		у			0.331			Note5,
	Red	x		Тур-0.05	0.632			
Chromaticity		у			0.311			
Chromaticity	Green	х			0.297			Note1
	Oreen	у			0.536			
	Blue	х			0.140			
	Diue	у			0.154	1		
Luminance		L		-	500	-	cd/m2	Note1 Note6
Uniformity		U		75	80	-	%	Note1 Note7

Note 1: Definition of optical measurement system.

Temperature =  $25^{\circ}C(\pm 3^{\circ}C)$ 

LED back-light: ON, Environment brightness < 150 lx

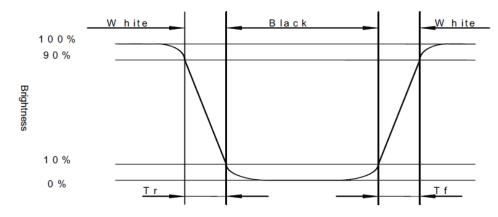


Note 2: Contrast ratio is defined as follow:

 $Contrast Ratio = \frac{Surface Luminance with all white pixels}{Surface Luminance with all black pixels}$ 

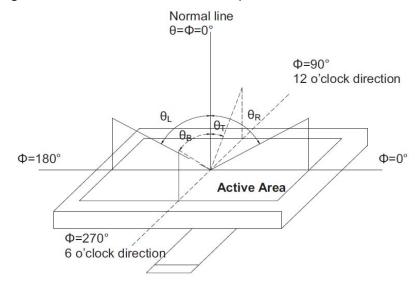
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time, Tr) and from white to black(Decay Time, Tf).



Note 4: Viewing angle range is defined as follow:

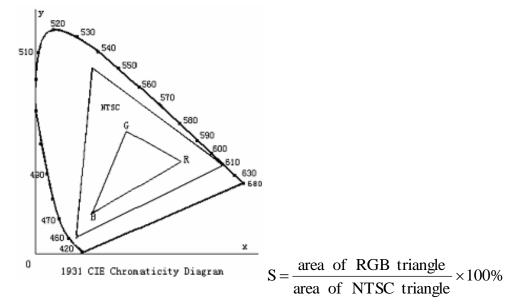
Viewing angle is measured at the center point of the LCD.



Note 5: Color chromaticity is defined as follow: (CIE1931)

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Color coordinates measured at center point of LCD.



Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels "White" at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

 $Uniformity(U) = \frac{Minimum Luminance(brightnes) in 9 points}{Maximum Luminance(brightnes) in 9 points}$ 

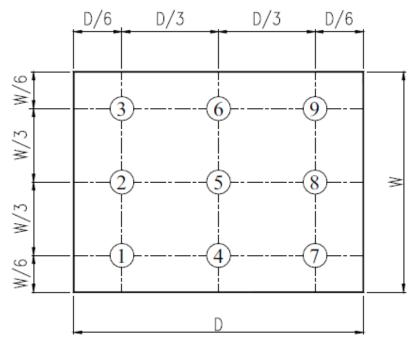


Fig. 2 Definition of uniformity

# 10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment criteria
1	High Temp Operation	Ts=+70℃, 96hrs	Per table in below
2	Low Temp Operation	Ta=-20℃, 96hrs	Per table in below
3	High Temp Storage	Ta=+80°C, 96hrs	Per table in below
4	Low Temp Storage	Ta=-30℃, 96hrs	Per table in below
5	High Temp & High Humidity Storage	Ta=+60℃, 90% RH 96 hours	Per table in below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-30 $^\circ C$ 30 min~+80 $^\circ C$ 30 min, Change time:5min, 5Cycles	Per table in below
7	ESD (Operation)	Air discharge:+/-8KV, Contact discharge:4KV	Per table in below
8	Vibration (Non-operation)	10Hz~150Hz, 100m/s2, 120min	Per table in below
9	Shock (Non-operation)	Half- sine wave,300m/s2,11ms	Per table in below
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	Per table in below

INSPECTION	CRITERION(after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel
	No other Defects of Alignment in Active area
Electrical current	Within device specifications
Function / Display	No Broken Circuit, No Short Circuit or No Black line
	No Other Defects of Display

ΤΕСΗΝΟΙΟGΥ

#### 11. Precautions for Use of LCD Modules

#### 11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

#### 11.2 Handling

A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.

B. Do not handle the product by holding the flexible pattern portion in order to assure the reliability

C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.

D. Provide a space so that the panel does not come into contact with other components.

E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.

F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.

G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.

H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

11.3 Static Electricity

A. Ground soldering iron tips, tools and testers when they are in operation.

- B. Ground your body when handling the products.
- C. Power on the LCD module before applying the voltage to the input terminals.
- D. Do not apply voltage which exceeds the absolute maximum rating.
- E. Store the products in an anti-electrostatic bag or container.

11.4Storage

A. Store the products in a dark place at  $+25^{\circ}C \pm 10^{\circ}C$  with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.

B. Storage in a clean environment, free from dust, active gas, and solvent.

11.5 Cleaning

A. Do not wipe the touch panel with dry cloth, as it may cause scratch.

B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

11.6 Cautions for installing and assembling

Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the

tolerance in the case and connector.

