

Dual INT-A-PAK Low Profile "Half Bridge" (Trench PT IGBT), 300 A

Proprietary Vishay IGBT Silicon "L Series"



Dual INT-A-PAK Low Profile

PRODUCT SUMMARY				
V _{CES}	600 V			
I _C DC at T _C = 104 °C	300 A			
V _{CE(on)} (typical) at 300 A, 25 °C	1.30 V			
Speed	DC to 1 kHz			
Package	DIAP low profile			
Circuit	Half bridge			

FEATURES

• Trench PT IGBT technology



Low V_{CE(on)}

- Square RBSOA
- HEXFRED® antiparallel diode with ultrasoft reverse recovery characteristics
- · Industry standard package
- Al₂O₃ DBC
- UL approved file E78996
- Designed for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- · Increased operating efficiency
- · Performance optimized as output inverter stage for TIG welding machines
- Direct mounting on heatsink
- · Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		600	V
Continuous collector current	I _C ⁽¹⁾	T _C = 25 °C	580	
Continuous collector current	IC (1)	T _C = 80 °C	400	
Pulsed collector current	I _{CM}		800	Α
Clamped inductive load current	I _{LM}		800	
Diode continuous forward current	IF	T _C = 25 °C	219	
		T _C = 80 °C	145	
Gate to emitter voltage	V _{GE}		± 20	V
Maximum naviar discination (ICRT)	P _D	T _C = 25 °C	1136	W
Maximum power dissipation (IGBT)		T _C = 80 °C	636	VV
RMS isolation voltage	V _{ISOL}	Any terminal to case (V _{RMS} t = 1 s, T _J = 25 °C)	3500	V
Operating junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C

Note

⁽¹⁾ Maximum continuous collector current must be limited to 500 A to do not exceed the maximum temperature of terminals



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	V _{GE} = 0 V, I _C = 500 μA	600	-	-		
	V _{CE(on)}	V _{GE} = 15 V, I _C = 150 A	-	1.12	1.21	V	
Collector to emitter voltage		V _{GE} = 15 V, I _C = 300 A	-	1.30	1.45		
Collector to entitler voltage		$V_{GE} = 15 \text{ V}, I_{C} = 150 \text{ A}, T_{J} = 125 ^{\circ}\text{C}$	-	1.03	-		
		$V_{GE} = 15 \text{ V}, I_{C} = 300 \text{ A}, T_{J} = 125 ^{\circ}\text{C}$	-	1.26	-		
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 6.4 \text{ mA}$	4.9	6.0	8.8		
		$V_{CE} = V_{GE}$, $I_C = 6.4$ mA, $T_J = 125$ °C	-	3.4	-		
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T$	$V_{CE} = V_{GE}$, $I_{C} = 6.4$ mA, (25 °C to 125 °C)	-	-26	-	mV/°C	
Forward transconductance	9 _{fe}	$V_{CE} = 20 \text{ V}, I_{C} = 50 \text{ A}$	-	67	-	S	
Transfer characteristics	V_{GE}	$V_{CE} = 20 \text{ V}, I_{C} = 300 \text{ A}$	-	11.4	-	V	
Collector to emitter leakage current	I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}$	-	4.0	150	μΑ	
		$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	100	-		
Diode forward voltage drop	V _{FM}	I _{FM} = 150 A	-	1.31	1.41		
		I _{FM} = 300 A	-	1.56	1.75	V	
		I _{FM} = 150 A, T _J = 125 °C	-	1.28	-		
		I _{FM} = 300 A, T _J = 125 °C	-	1.63	-		
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 500	nA	

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on switching energy	E _{on}		-	6.0	-	
Turn-off switching energy	E _{off}		-	33	-	mJ
Total switching energy	E _{tot}		-	39	-	
Turn-on delay time	t _{d(on)}	$I_C = 300 \text{ A}, V_{CC} = 300 \text{ V}, V_{GE} = 15 \text{ V},$ $R_g = 1.5 \Omega, L = 500 \mu\text{H}, T_J = 25 ^{\circ}\text{C}$	-	503	-	ns ns
Rise time	t _r	1 1 1 2 1 1 2 1 2 1 2 2 2 2 1 1 1 1 1 2	-	214	-	
Turn-off delay time	t _{d(off)}		-	600	-	
Fall time	t _f		-	547	-	
Turn-on switching loss	E _{on}		-	7.2	-	mJ
Turn-off switching loss	E _{off}		-	55.2	-	
Total switching loss	E _{tot}		-	62.4	-	
Turn-on delay time	t _{d(on)}	I_C = 300 A, V_{CC} = 300 V, V_{GE} = 15 V, R_g = 1.5 Ω, L = 500 μH, T_J = 125 °C	-	476	-	
Rise time	t _r	g	-	209	-]
Turn-off delay time	t _{d(off)}		-	807	-	ns
Fall time	t _f		-	918	-	
Reverse bias safe operating area	RBSOA	$\begin{array}{l} T_J = 150~^{\circ}C, \ I_C = 800~A, \ V_{CC} = 300~V \\ V_P = 600~V, \ R_g = 1.5~\Omega, \ V_{GE} = 15~V~to~0~V, \\ L = 500~\mu H \end{array}$	Fullsquare			
Diode reverse recovery time	t _{rr}		-	119	-	ns
Diode peak reverse current	I _{rr}	I_F = 300 A, R_g = 1.5 Ω, V_{CC} = 300 V, T_J = 25 °C	-	99	-	Α
Diode recovery charge	Q _{rr}	1 100 000 1, 10 20 0	-	7.3	-	μC
Diode reverse recovery time	t _{rr}		-	165	-	ns
Diode peak reverse current	I _{rr}	I_F = 300 A, R_g = 1.5 Ω, V_{CC} = 300 V, T_J = 125 °C	-	127	-	Α
Diode recovery charge	Q _{rr}	1 100 333 1, 13 120 3	-	13	-	μC



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Operating junction and storage temperature range		T _J , T _{Stg}	-40	-	150	°C	
lunation to appa per log	IGBT	R_{thJC}	-	-	0.11	°C/W	
Junction to case per leg	diode		-	-	0.4		
Case to sink per module		R _{thCS}	-	0.05	-		
Mounting torque	case to heatsink: M6 screw		4	-	6	Nm	
Mounting torque	case to terminal 1, 2, 3: M5 screw		2	-	4	INITI	
Weight			=	270	-	g	

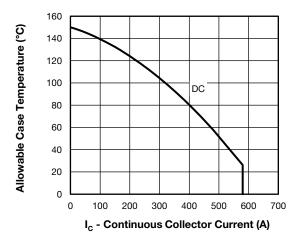


Fig. 1 - Maximum IGBT Continuous Collector Current vs.
Case Temperature

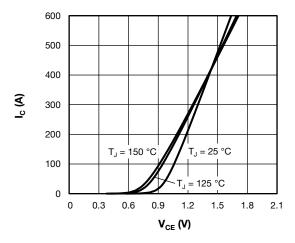


Fig. 2 - Typical IGBT Output Characteristics, $V_{GE} = 15 \text{ V}$

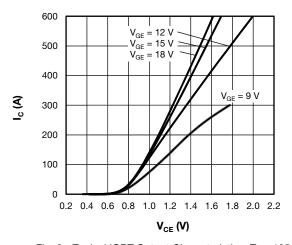


Fig. 3 - Typical IGBT Output Characteristics, T_J = 125 °C

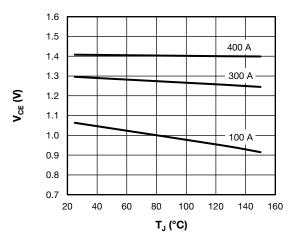


Fig. 4 - Collector to Emitter Voltage vs. Junction Temperature

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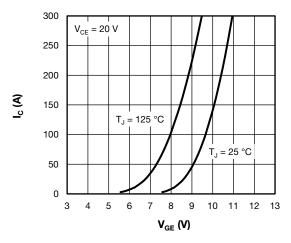


Fig. 5 - Typical IGBT Transfer Characteristics

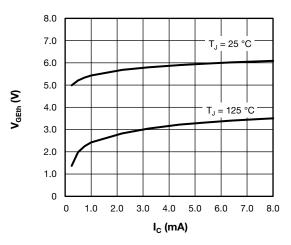


Fig. 6 - Typical IGBT Gate Threshold Voltage

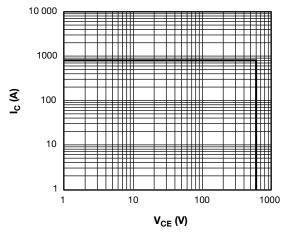


Fig. 7 - IGBT Reverse BIAS SOA T_J = 150 °C, V_{GE} = 15V

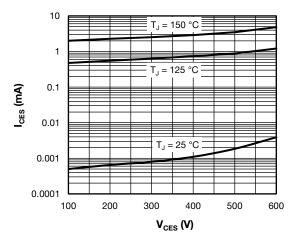


Fig. 8 - Typical IGBT Zero Gate Voltage Collector Current

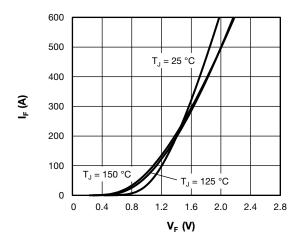


Fig. 9 - Typical Diode Forward Characteristics

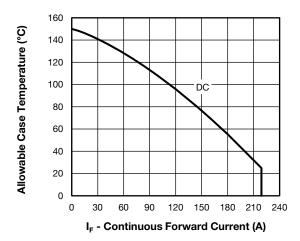


Fig. 10 - Maximum Diode Continuous Forward Current vs. Case Temperature



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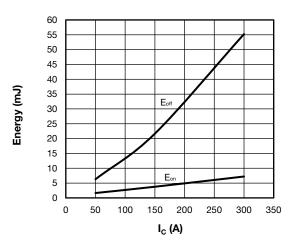


Fig. 11 - Typical IGBT Energy Loss vs. I_C T_J = 125 °C, V_{CC} = 300 V, R_g = 1.5 $\Omega,$ V_{GE} = 15 V, L = 500 μH

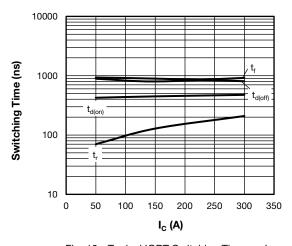


Fig. 12 - Typical IGBT Switching Time vs. I_C T $_J$ = 125 °C, V $_{CC}$ = 300 V, R $_g$ = 1.5 $\Omega,$ V $_{GE}$ = 15 V, L = 500 μH

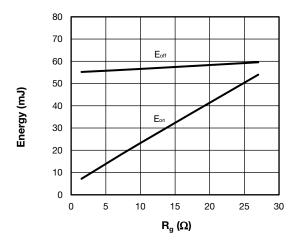


Fig. 13 - Typical IGBT Energy Loss vs. R_g $T_J=125~^{\circ}C,\,V_{CC}=300$ V, $I_C=300$ A, $V_{GE}=15$ V, $L=500~\mu H$

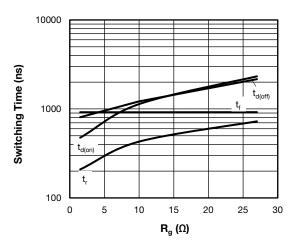


Fig. 14 - Typical IGBT Switching Time vs. R_g T_J = 125 °C, V_{CC} = 300 V, I_C = 300 A, V_{GE} = 15 V, L = 500 μH

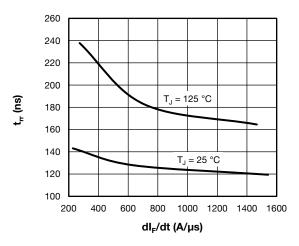


Fig. 15 - Typical Diode Reverse Recovery Time vs. dI_F/dt $V_{CC}=300\ V,\ I_F=300\ A$

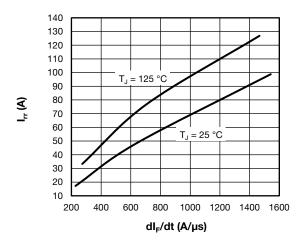


Fig. 16 - Typical Diode Reverse Recovery Current vs. dI_F/dt $V_{CC} = 300 \text{ V}, I_F = 300 \text{ A}$



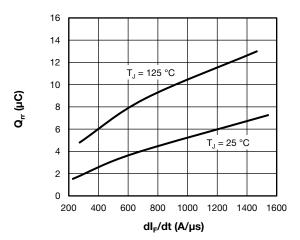


Fig. 17 - Typical Diode Reverse Recovery Charge vs. dI_F/dt $V_{CC} = 300 \text{ V}, I_F = 300 \text{ A}$

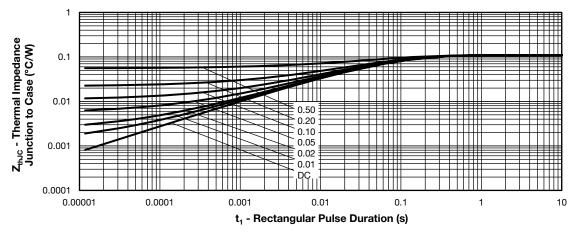


Fig. 18 - Maximum Thermal Impedance Z_{thJC} Characteristics - (IGBT)

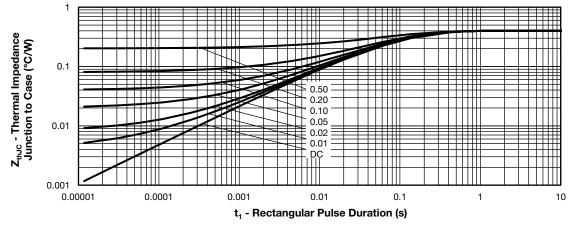
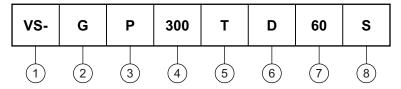


Fig. 19 - Maximum Thermal Impedance ZthJC Characteristics - (Diode)



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

- Insulated gate bipolar transistor (IGBT)

3 - Trench PT IGBT technology

- Current rating (300 = 300 A)

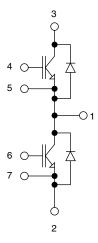
5 - Circuit configuration (T = half bridge)

6 - Package indicator (D = dual INT-A-PAK low profile)

7 - Voltage rating (60 = 600 V)

Speed / type (S = standard speed IGBT)

CIRCUIT CONFIGURATION

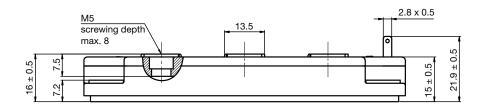


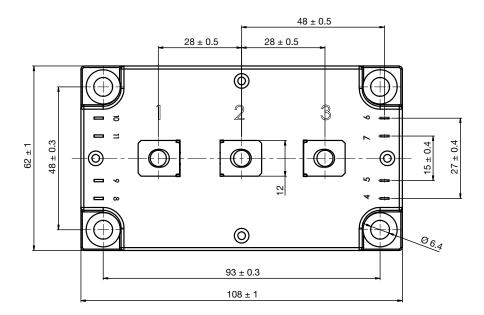
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95435		



Dual INT-A-PAK Low Profile

DIMENSIONS in millimeters







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