

CZSH6-100C

SURFACE MOUNT SILICON
DUAL, COMMON CATHODE
SCHOTTKY POWER RECTIFIER
6.0 AMP, 100 VOLT



www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CZSH6-100C is manufactured with two 3.0 Amp Schottky rectifiers assembled in a common cathode configuration and packaged in a SOT-223 case. This device has been designed for high voltage applications requiring a low forward voltage drop.



SOT-223 CASE

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T_A=25°C unless otherwise noted)

	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V _{RRM}	100	V
DC Blocking Voltage	V _R	100	V
RMS Reverse Voltage	V _R (RMS)	70	V
Average Forward Current (T _A =75°C)	I _O	6.0	A
Peak Forward Surge Current, t _p =8.3ms	I _{FSM}	75	A
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ _{JA}	62.5	°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted)

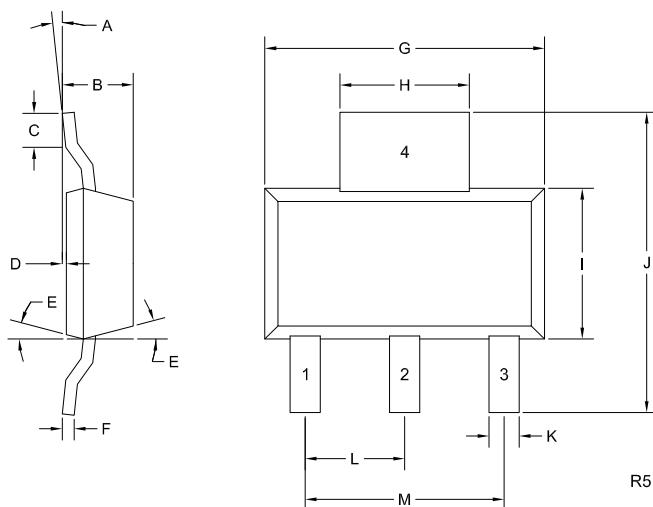
SYMBOL	TEST CONDITIONS	MAX	UNITS
I _R	V _R =100V	10	µA
I _R	V _R =100V, T _A =100°C	20	mA
V _F	I _F =1.0A	0.7	V
V _F	I _F =3.0A	0.8	V
V _F	I _F =6.0A	1.0	V

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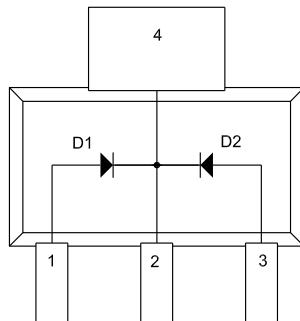
SOT-223 CASE - MECHANICAL OUTLINE



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0°	10°	0°	10°
B	0.059	0.071	1.50	1.80
C	0.018	---	0.45	---
D	0.000	0.004	0.00	0.10
E	10°	10°	10°	10°
F	0.009	0.014	0.23	0.35
G	0.248	0.270	6.30	6.85
H	0.114	0.122	2.90	3.10
I	0.130	0.146	3.30	3.70
J	0.264	0.287	6.70	7.30
K	0.024	0.033	0.60	0.85
L	0.091	0.091	2.30	2.30
M	0.181	0.181	4.60	4.60

SOT-223 (REV: R5)

PIN CONFIGURATION



LEAD CODE:

- 1) Anode D1
- 2) Cathode D1, D2
- 3) Anode D2
- 4) Cathode D1, D2

Pin 2 and Pin 4 are common.

MARKING:

FULL PART NUMBER

R3 (7-May 2019)

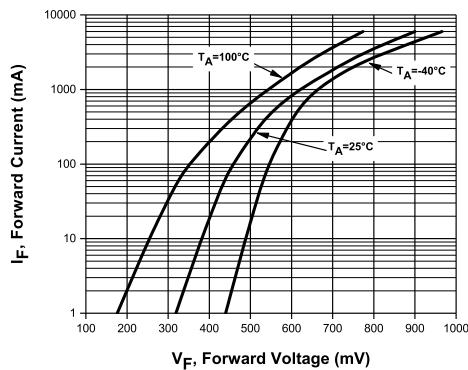
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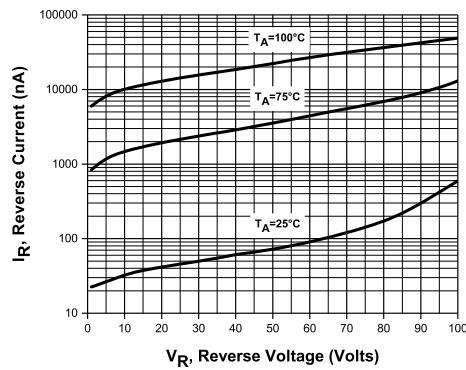
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TYPICAL ELECTRICAL CHARACTERISTICS

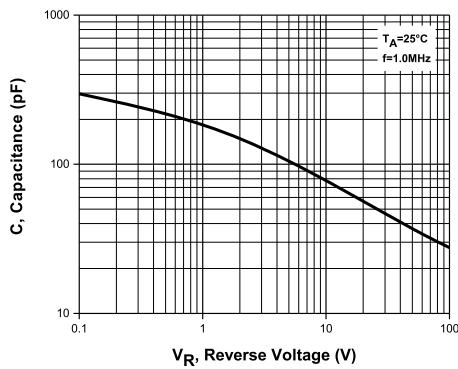
Forward Voltage



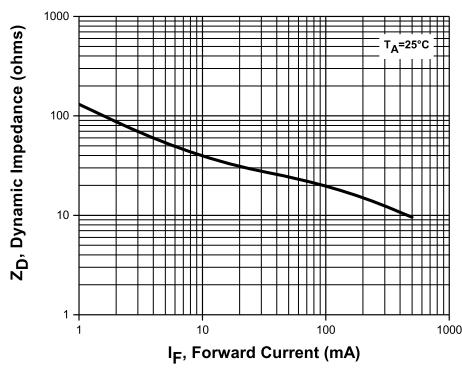
Leakage Current



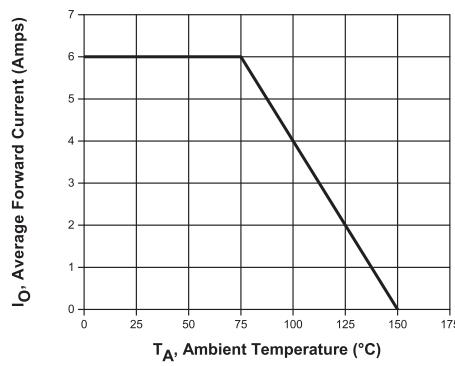
Capacitance



Forward Dynamic Impedance



Current Derating



R3 (7-May 2019)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix " TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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