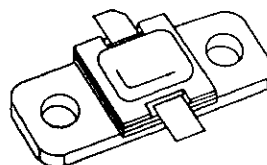


RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- 5:1 VSWR CAPABILITY
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- $P_{OUT} = 6.0 \text{ W MIN. WITH } 9.3 \text{ dB GAIN}$



.310 x .310 2LFL (S064)
hermetically sealed

ORDER CODE
AM80912-005

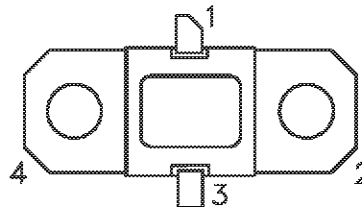
BRANDING
80912-5

DESCRIPTION

The AM80912-005 is designed for specialized avionics applications, including JTIDS, where power is provided under pulse formats utilizing short pulse widths and high burst or overall duty cycles.

The AM80912-005 is housed in the unique IMPAC™ Hermetic Metal/Ceramic package with

PIN CONNECTION



- | | |
|--------------|------------|
| 1. Collector | 3. Emitter |
| 2. Base | 4. Base |

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}\text{C}$)

Symbol	Parameter	Value	Unit
P_{DISS}	Power Dissipation* ($T_C \leq 75^{\circ}\text{C}$)	25	W
I_C	Device Current*	0.9	A
V_{CC}	Collector-Supply Voltage*	32	V
T_J	Junction Temperature (Pulsed RF Operation)	250	$^{\circ}\text{C}$
T_{STG}	Storage Temperature	- 65 to +200	$^{\circ}\text{C}$

THERMAL DATA

$R_{TH(j-c)}$	Junction-Case Thermal Resistance*	7.0	$^{\circ}\text{C/W}$
---------------	-----------------------------------	-----	----------------------

*Applies only to rated RF amplifier operation

ELECTRICAL SPECIFICATIONS ($T_{case} = 25^{\circ}C$)**STATIC**

Symbol	Test Conditions		Value			Unit
			Min.	Typ.	Max.	
BV _{CBO}	I _C = 1mA	I _E = 0mA	48	—	—	V
BV _{EBO}	I _E = 1mA	I _C = 0mA	3.5	—	—	V
BV _{CER}	I _C = 5mA	R _{BE} = 10Ω	48	—	—	V
I _{CES}	V _{BE} = 0V	V _{CE} = 28V	—	—	0.5	mA
h _{FE}	V _{CE} = 5V	I _C = 250mA	30	—	300	—

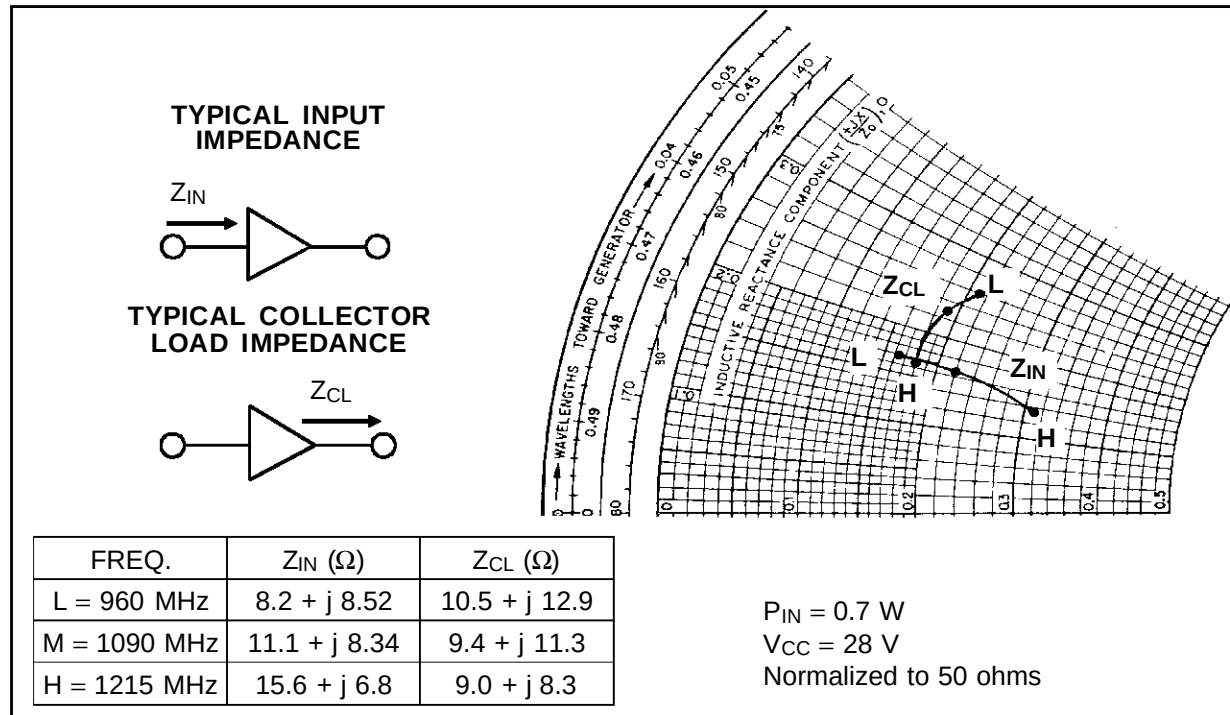
DYNAMIC

Symbol	Test Conditions			Value			Unit
				Min.	Typ.	Max.	
P _{OUT}	f = 960 — 1215MHz	P _{IN} = 0.7W	V _{CC} = 28V	6.0	—	—	W
η _c	f = 960 — 1215MHz	P _{IN} = 0.7W	V _{CC} = 28V	45	—	—	%
G _P	f = 960 — 1215MHz	P _{IN} = 0.7W	V _{CC} = 28V	9.3	—	—	dB

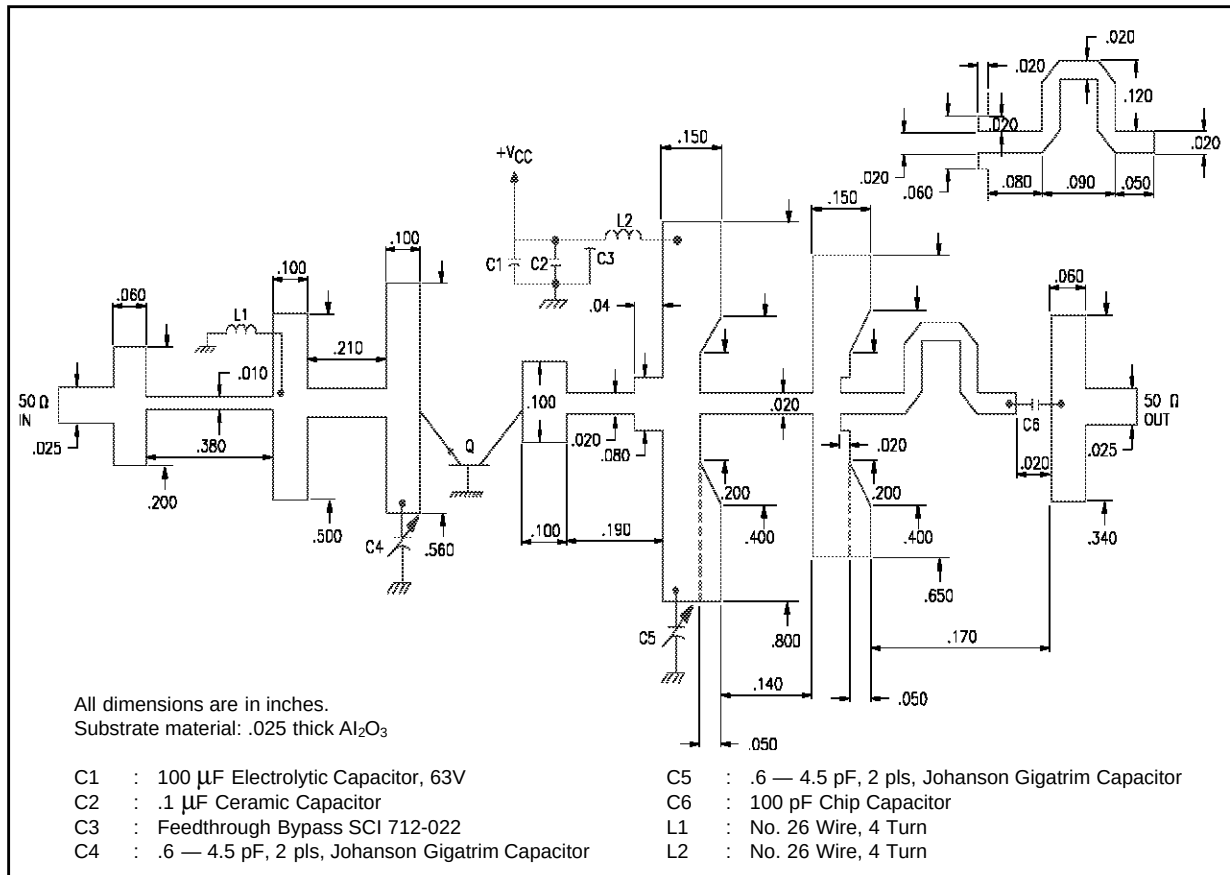
Note: Pulse format: 6.4 μS on 6.6 μS off, repeat for 3.3 ms, then off for 4.5125 ms.

Duty Cycle: Burst 49.2%, overall 20.8%

IMPEDANCE DATA

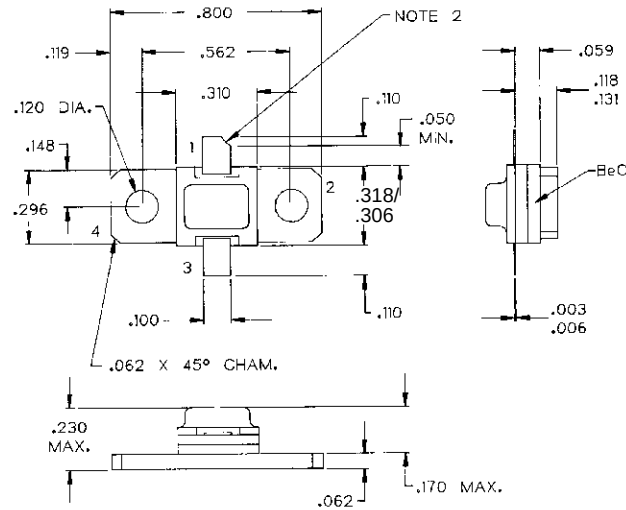


TEST CIRCUIT



PACKAGE MECHANICAL DATA

Ref.: Dwg. No.: J133100D



NOTES:

1. ALL TOLERANCE $\pm .010$ EXCEPT WHERE NOTED;
DIMENSIONS IN INCHES.
2. COLLECTOR LEAD CHAMFER 45° NOM. X .040 NOM.

Information furnished is believed to be accurate and reliable. However, SGS-THOMSON Microelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of SGS-THOMSON Microelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. SGS-THOMSON Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of SGS-THOMSON Microelectronics.

© 1994 SGS-THOMSON Microelectronics - All Rights Reserved

SGS-THOMSON Microelectronics GROUP OF COMPANIES

Australia - Brazil - France - Germany - Hong Kong - Italy - Japan - Korea - Malaysia - Malta - Morocco - The Netherlands - Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A