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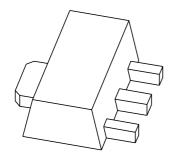
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Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



PBSS5250X 50 V, 2 A PNP low V_{CEsat} (BISS) transistor

Product data sheet Supersedes data of 2003 Jun 17 2004 Nov 04



50 V, 2 A PNP low V_{CEsat} (BISS) transistor

PBSS5250X

FEATURES

- SOT89 (SC-62) package
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability: I_C and I_{CM}
- · Higher efficiency leading to less heat generation
- Reduced printed-circuit board requirements.

APPLICATIONS

- Power management
 - DC/DC converters
 - Supply line switching
 - Battery charger
 - LCD backlighting.
- · Peripheral drivers
 - Driver in low supply voltage applications (e.g. lamps and LEDs).
 - Inductive load driver (e.g. relays, buzzers and motors).

DESCRIPTION

PNP low V_{CEsat} transistor in a SOT89 plastic package. NPN complement: PBSS4250X.

MARKING

TYPE NUMBER	MARKING CODE(1)
PBSS5250X	*1L

Note

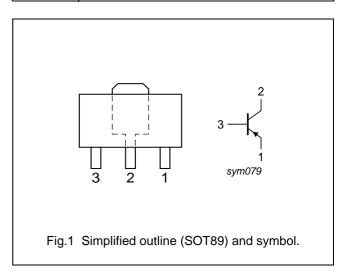
- 1. * = p: Made in Hong Kong
 - * = t: Made in Malaysia
 - * = W: Made in China.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{CEO}	collector-emitter voltage	-50	V
I _C	collector current (DC)	-2	Α
I _{CM}	peak collector current	-5	Α
R _{CEsat}	equivalent on-resistance	160	mΩ

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base



50 V, 2 A PNP low V_{CEsat} (BISS) transistor

PBSS5250X

ORDERING INFORMATION

TYPE NUMBER	PACKAGE					
TIPE NOWIBER	NAME DESCRIPTION VER					
PBSS5250X	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89			

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT	
V _{CBO}	collector-base voltage	open emitter	_	-50	V	
V _{CEO}	collector-emitter voltage	open base	-	-50	V	
V _{EBO}	emitter-base voltage	open collector	-	-5	V	
I _C	collector current (DC)		_	-2	А	
I _{CM}	peak collector current	T _{j(max)}	-	-5	Α	
I _B	base current (DC)		_	-0.5	А	
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C				
		note 1	_	550	mW	
		note 2	_	1	W	
T _{stg}	storage temperature		-65	+150	°C	
Tj	junction temperature		_	150	°C	
T _{amb}	ambient temperature		-65	+150	°C	

Notes

- 1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm².

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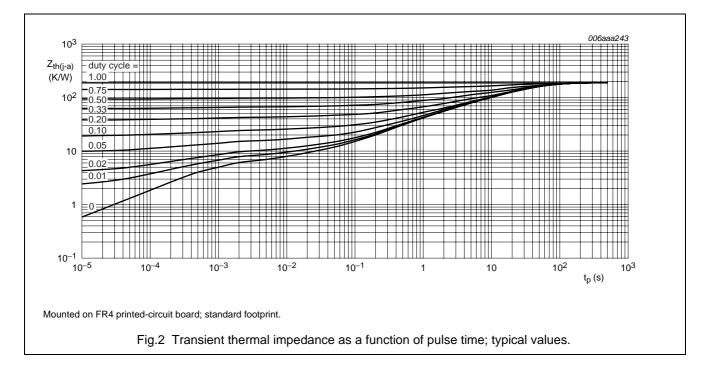
PBSS5250X

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		
		note 1	225	K/W
		note 2	125	K/W
		note 3	90	K/W
		note 4	80	K/W
R _{th(j-s)}	thermal resistance from junction to soldering point		16	K/W

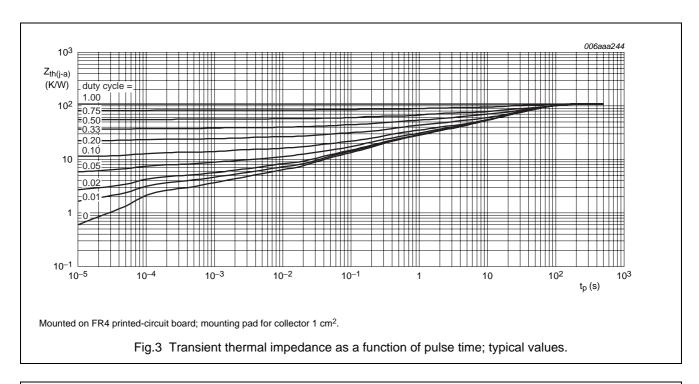
Notes

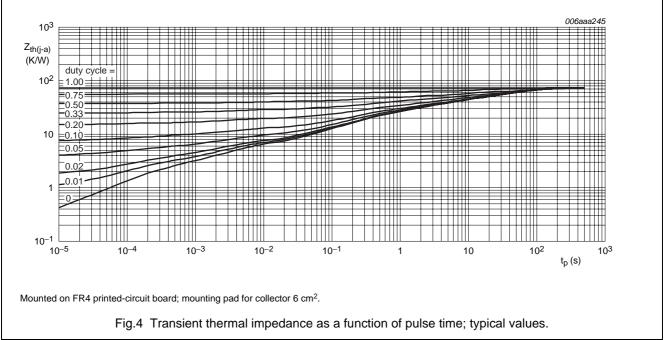
- 1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm².
- 3. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 6 cm².
- 4. Device mounted on a ceramic printed-circuit board 7 cm², single-sided copper, tin-plated.



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PBSS5250X





50 V, 2 A PNP low V_{CEsat} (BISS) transistor

PBSS5250X

CHARACTERISTICS

 T_{amb} = 25 $^{\circ}C$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}$	_	-100	nA
		$V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	_	-50	μΑ
I _{CES}	collector-emitter cut-off current	$V_{CE} = -50 \text{ V}; V_{BE} = 0 \text{ V}$	_	-100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$	=	-100	nA
h _{FE}	DC current gain	V _{CE} = -2 V			
		$I_{\rm C} = -0.1 {\rm A}$	200	_	
		$I_{\rm C} = -0.5 {\rm A}$	200	_	
		$I_{C} = -1 \text{ A}$; note 1	200	_	
		$I_{C} = -2 \text{ A}$; note 1	100	_	
V _{CEsat}	collector-emitter saturation	$I_C = -0.5 \text{ A}; I_B = -50 \text{ mA}$	_	-90	mV
	voltage	$I_C = -1 \text{ A}; I_B = -50 \text{ mA}$	_	-250	mV
		$I_C = -2 \text{ A}; I_B = -100 \text{ mA}$	_	-380	mV
		$I_C = -2 \text{ A}$; $I_B = -200 \text{ mA}$; note 1	_	-320	mV
R _{CEsat}	equivalent on-resistance	$I_C = -2 \text{ A}$; $I_B = -200 \text{ mA}$; note 1	_	160	mΩ
V _{BEsat}	base-emitter saturation voltage	$I_C = -2 \text{ A}; I_B = -100 \text{ mA}$	_	-1.1	V
V _{BEon}	base-emitter turn-on voltage	$V_{CE} = -2 \text{ V}; I_{C} = -1 \text{ A}$	-1.1	_	V
f _T	transition frequency	I _C = -100 mA; V _{CE} = -5 V; f = 100 MHz	100	-	MHz
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	-	35	pF

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

Product data sheet **NXP Semiconductors**

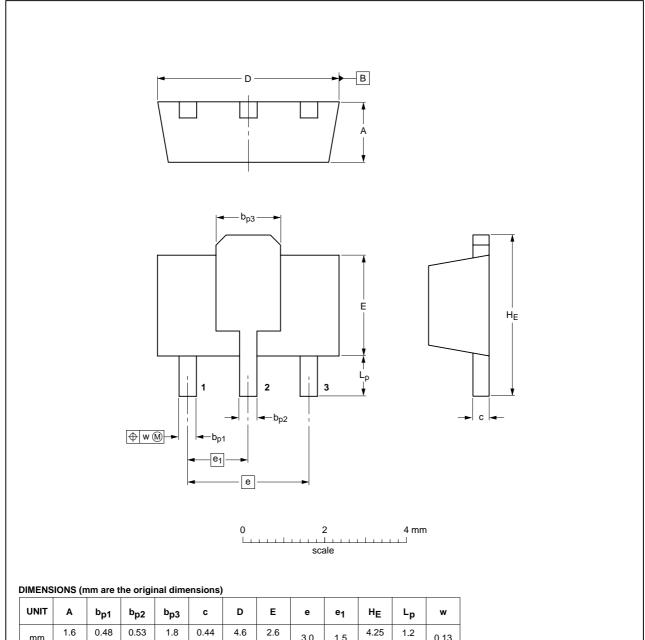
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PBSS5250X

PACKAGE OUTLINE

Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



UNIT	Α	b _{p1}	b _{p2}	b _{p3}	С	D	E	е	e ₁	HE	Lp	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.23	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	1.2 0.8	0.13

OUTLINE		REFER	EUROPEAN	ISSUE DATE					
VERSION	RSION IEC		RSION IEC JEDEC		JEITA		PROJECTION	ISSUE DATE	
SOT89		TO-243	SC-62			-04-08-03 06-03-16			

2004 Nov 04 7

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PBSS5250X

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

For additional information please visit: http://www.nxp.com
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