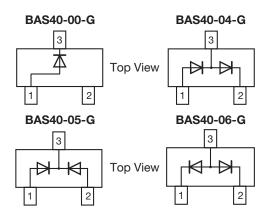


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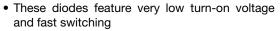
Vishay Semiconductors

# Small Signal Schottky Diodes, Single and Dual





#### **FEATURES**





 These devices are protected by a PN junction guardring against excessive voltage, such as electrostatic discharges



AEC-Q101 qualified

RoHS COMPLIANT

• Base P/N-G3 - green, commercial grade

**GREEN** (5-2008)

 Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **MECHANICAL DATA**

Case: SOT-23

Weight: approx. 8.1 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	INTERNAL CONSTRUCTION	TYPE MARKING	REMARKS	
BAS40-00-G	BAS40-00-G3-08 or BAS40-00-G3-18	Single diode	43G	Tape and reel	
BAS40-04-G	BAS40-04-G3-08 or BAS40-04-G3-18	Dual diodes serial	44G		
BAS40-05-G	BAS40-05-G3-08 or BAS40-05-G3-18	Dual diodes common cathode	45G		
BAS40-06-G	BAS40-06-G3-08 or BAS40-06-G3-18	Dual diodes common anode	46G		

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Repetitive peak reverse voltage		$V_{RRM} = V_{RWM} = V_{R}$	40	V	
Forward continuous current (1)		I <sub>F</sub>	200	mA	
Surge forward current (1)	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	600	mA	
Power dissipation (1)		P <sub>tot</sub>	200	mW	

#### Note

<sup>(1)</sup> Device on fiberglass substrate, see layout on next page.

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	500	K/W	
Junction temperature		T <sub>j</sub>	125	°C	
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C	
Operating temperature range		T <sub>op</sub>	- 55 to + 125	°C	

#### Note

<sup>(1)</sup> Device on fiberglass substrate, see layout on next page.



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# Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 10 μA (pulsed)	V <sub>(BR)</sub>	40			V
Leakage current	V <sub>R</sub> = 30 V	I <sub>R</sub>		20	100	nA
Forward voltage	I <sub>F</sub> = 1 mA	$V_{F}$			380	mV
Forward voltage (1)	I <sub>F</sub> = 40 mA	$V_{F}$			1000	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>D</sub>		4	5	pF
Reverse recovery time	$I_F = I_R = 10 \text{ mA}, i_R = 1 \text{ mA}, R_L = 100 \Omega$	t <sub>rr</sub>			5	ns

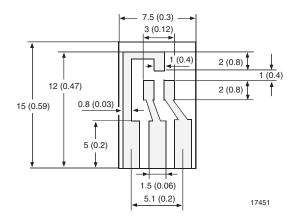
#### Note

<sup>(1)</sup> Pulse test  $t_p < 300 \mu s$ 

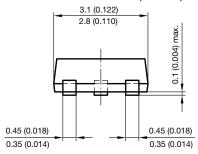
### LAYOUT FOR $R_{thJA}$ TEST

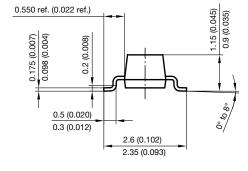
Thickness:

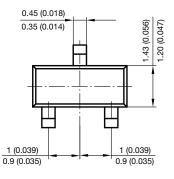
Fiberglass 1.5 mm (0.059 inches) Copper leads 0.3 mm (0.012 inches)

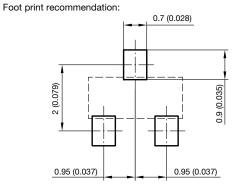


### PACKAGE DIMENSIONS in millimeters (inches): SOT-23









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## **Legal Disclaimer Notice**

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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