

CBRSDSH2-100

SURFACE MOUNT SILICON
HIGH DENSITY
2.0 AMP
SCHOTTKY BRIDGE RECTIFIER



www.centrasemi.com

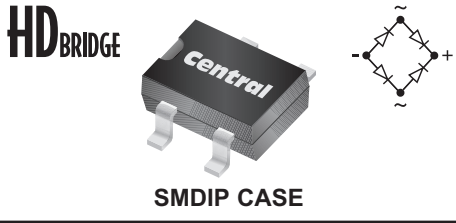
DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRSDSH2-100 is a full wave bridge rectifier mounted in a durable epoxy surface mount case, utilizing glass passivated chips.

MARKING: FULL PART NUMBER

FEATURES:

- High 2.0A Current Rating
- Low V_F Schottky Diodes (700mV MAX @ $I_F=2.0A$)



MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

Peak Repetitive Reverse Voltage

DC Blocking Voltage

RMS Reverse Voltage

Average Forward Current ($T_A=75^\circ\text{C}$)

Peak Forward Surge Current (8.3ms)

Operating Junction Temperature

Storage Temperature

SYMBOL

V_{RRM}

100

V_R

100

$V_{R(RMS)}$

70

I_O

2.0

I_{FSM}

110

T_J

-50 to +125

T_{stg}

-50 to +150

UNITS

V

V

V

A

A

$^\circ\text{C}$

$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL

TEST CONDITIONS

TYP

MAX

UNITS

I_R

$V_R=100V$

1.0

50

μA

V_F

$I_F=2.0A$

650

700

mV

C_J

$V_R=4.0V$, $f=1.0\text{MHz}$

280

500

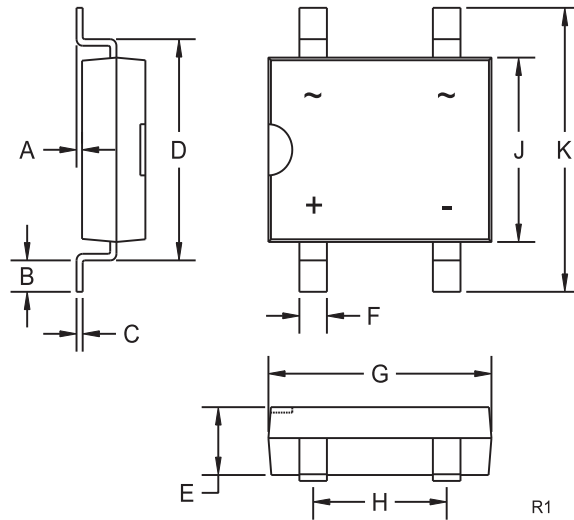
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SMDIP CASE - MECHANICAL OUTLINE



DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.004	0.008	0.10	0.20
B	0.040	0.060	1.02	1.52
C	0.009		0.23	
D	0.290	0.310	7.37	7.87
E	0.086	0.098	2.18	2.49
F	0.038	0.042	0.97	1.07
G	0.316	0.335	8.03	8.51
H	0.195	0.205	4.95	5.21
J	0.245	0.255	6.22	6.48
K	0.360	0.410	9.14	10.41

SMDIP (REV: R1)

MARKING: FULL PART NUMBER

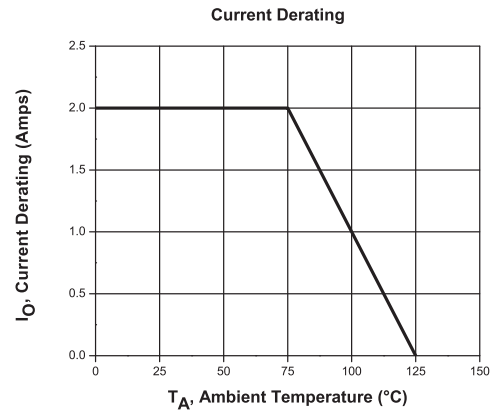
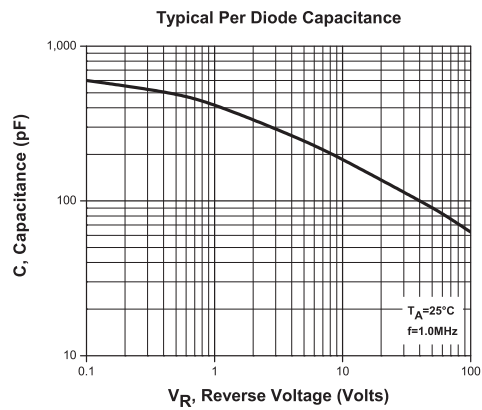
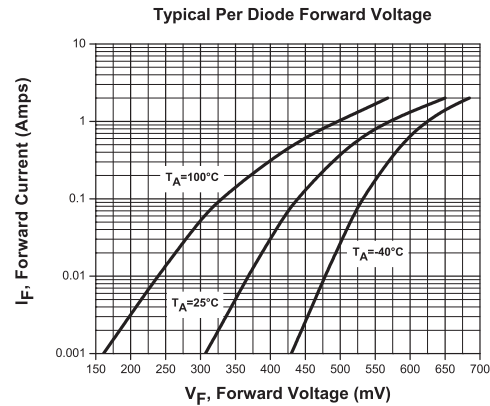
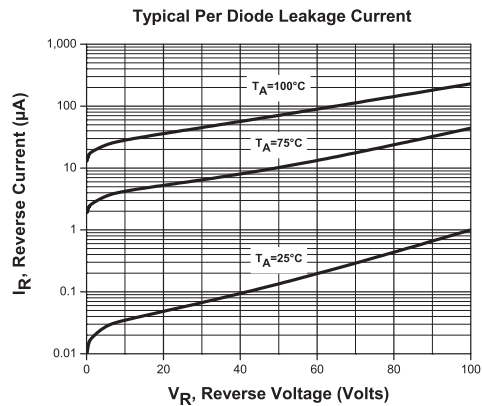
R3 (4-January 2013)

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TYPICAL ELECTRICAL CHARACTERISTICS



R3 (4-January 2013)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix " TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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