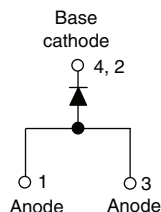


High Performance Schottky Rectifier, 10 A



D-PAK (TO-252AA)



FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

PRODUCT SUMMARY	
Package	D-PAK (TO-252AA)
$I_{F(AV)}$	10 A
V_R	45 V
V_F at I_F	0.57 V
I_{RM}	15 mA at 125 °C
T_J max.	175 °C
Diode variation	Single die
E_{AS}	20 mJ

DESCRIPTION

The VS-STPS1045B-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	10	A
V_{RRM}		45	V
I_{FSM}	$t_p = 5 \mu s$ sine	390	A
V_F	10 A _{pk} , $T_J = 125 \text{ °C}$	0.57	V
T_J	Range	-40 to +175	°C

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-STPS1045B-M3	UNITS
Maximum DC reverse voltage	V_R	45	V
Maximum working peak reverse voltage	V_{RRM}		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 151 °C, rectangular waveform		10	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	390	
		10 ms sine or 6 ms rect. pulse		75	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 3.0 A, L = 4.40 mH		20	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3.0	A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	10 A	T _J = 25 °C	0.63	V
		20 A		0.84	
		10 A	T _J = 125 °C	0.57	
		20 A		0.72	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.2	mA
		T _J = 125 °C		15	
Typical junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz), 25 °C		760	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/μs

Note(1) Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	$T_J^{(1)}, T_{Stg}$		-40 to +175	$^{\circ}\text{C}$
Maximum thermal resistance, junction to case	R_{thJC}	DC operation See fig. 4	3.0	$^{\circ}\text{C/W}$
Maximum thermal resistance, junction to ambient	R_{thJA}		50	
Approximate weight			0.3	g
			0.01	oz.
Marking device		Case style D-PAK (similar to TO-252AA)	STPS1045B	

Note(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

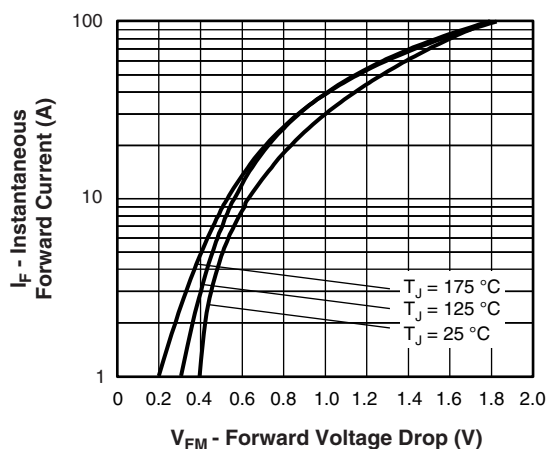


Fig. 1 - Maximum Forward Voltage Drop Characteristics

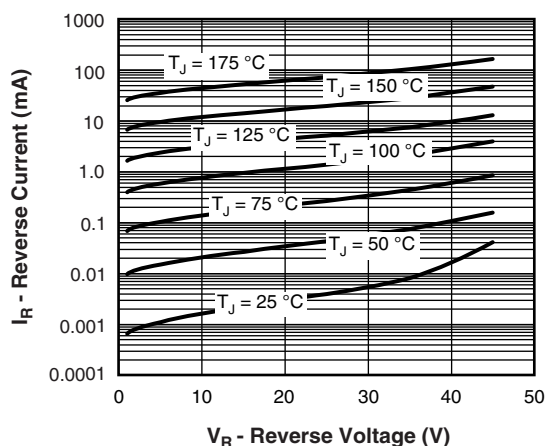


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

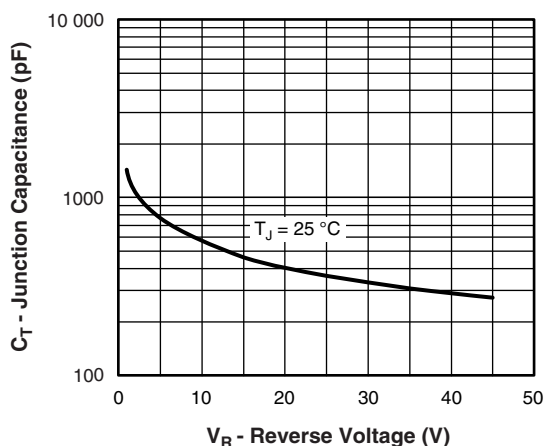
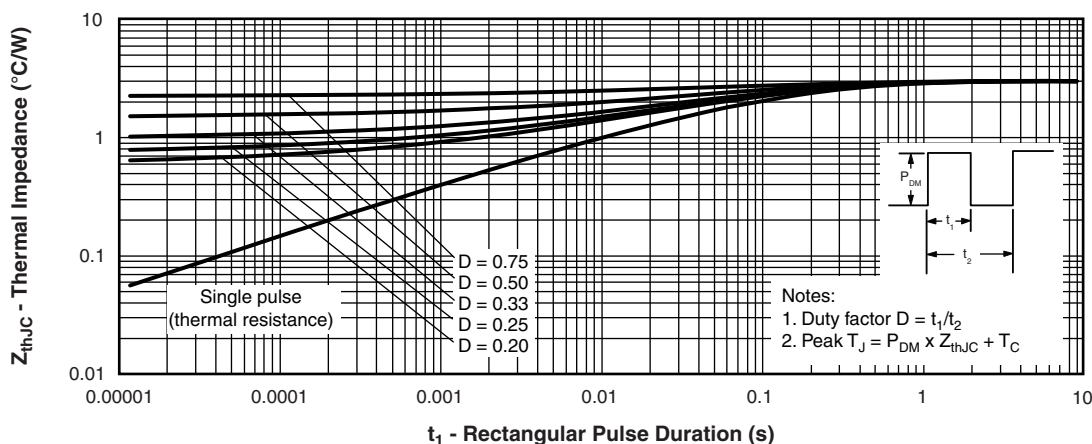


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

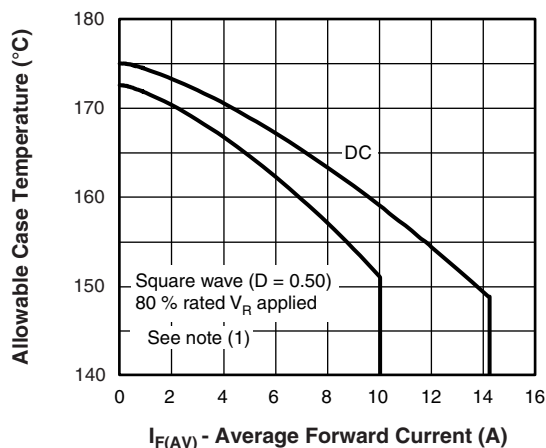


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

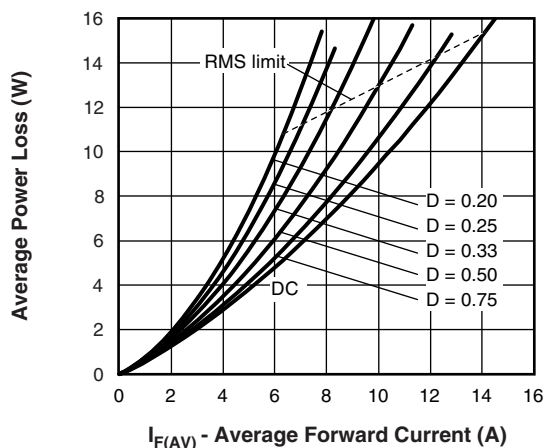


Fig. 6 - Forward Power Loss Characteristics

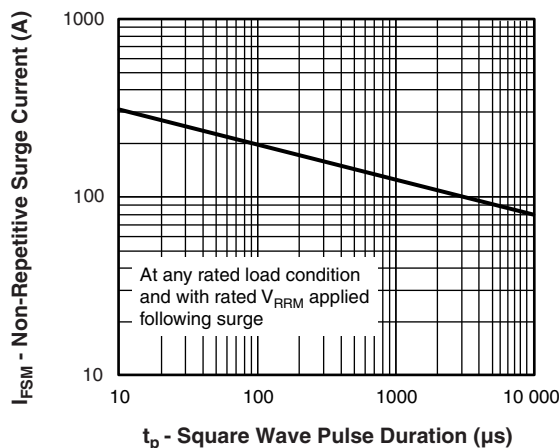


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

**ORDERING INFORMATION TABLE**

Device code	VS-	STPS	10	45	B	TRL	-M3
	①	②	③	④	⑤	⑥	⑦
①	- Vishay Semiconductors product						
②	- Schottky STPS series						
③	- Current rating (10 A)						
④	- Voltage rating (45 = 45 V)						
⑤	- B = Essential part number						
⑥	- • None = tube • TR = tape and reel • TRL = tape and reel (left oriented) • TRR = tape and reel (right oriented)						
⑦	- Environmental digit: -M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free						

ORDERING INFORMATION (Example)

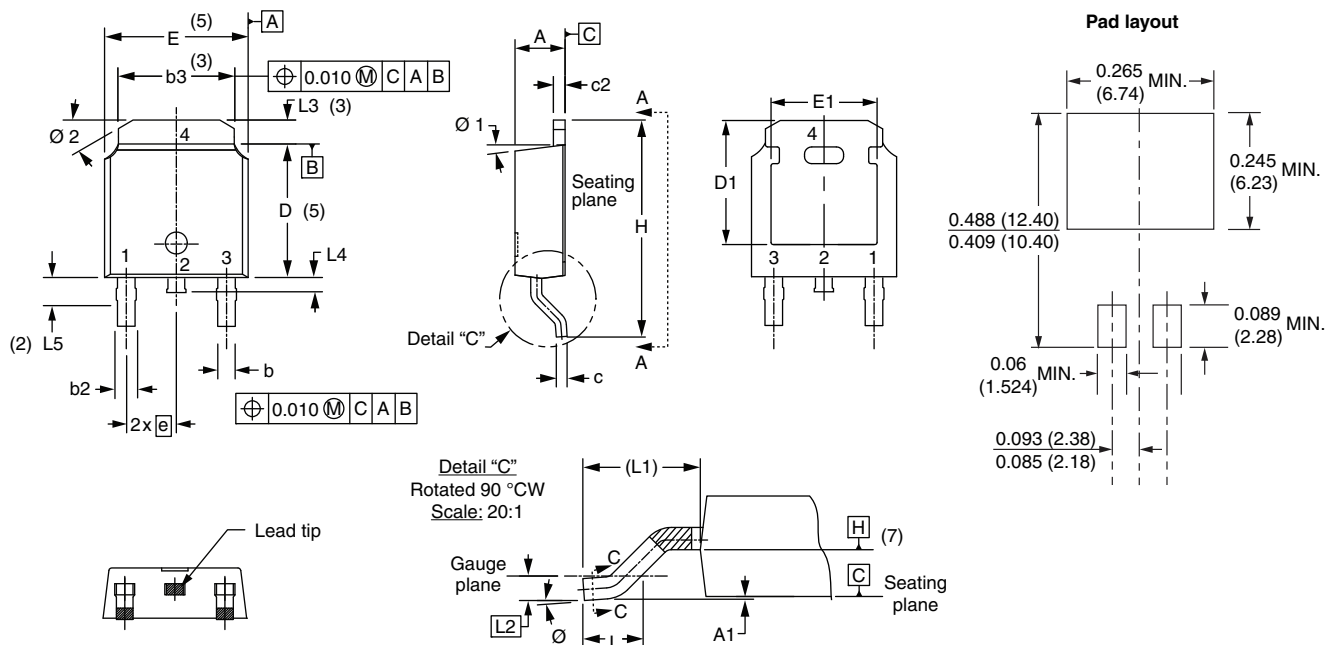
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-STPS1045B-M3	75	3000	Antistatic plastic tube
VS-STPS1045BTR-M3	2000	2000	13" diameter reel
VS-STPS1045BTRL-M3	3000	3000	13" diameter reel
VS-STPS1045BTRR-M3	3000	3000	13" diameter reel

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?95627
Part marking information	www.vishay.com/doc?95176
Packaging information	www.vishay.com/doc?95033

D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
c	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	-	3
E	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
e	2.29 BSC		0.090 BSC		
H	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108 REF.		
L2	0.51 BSC		0.020 BSC		
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

Notes

- Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.010") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC® outline TO-252AA



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