Vishay Semiconductors

## High Performance Schottky Rectifier, 240 A



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PRODUCT SUMMARY			
I <sub>F(AV)</sub> 240 A			
V <sub>R</sub>	30 V		
Package	HALF-PAK (D-67)		
Circuit	Single diode		

### FEATURES

- 150 °C T<sub>.1</sub> operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-242NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN				
I <sub>F(AV)</sub>	Rectangular waveform	240	А			
V <sub>RRM</sub>		30	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	27 000	А			
V <sub>F</sub>	220 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.45	V			
TJ	Range	-55 to 150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-242NQ030PbF	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	30	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	30	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_{C}$ = 118 °C, rectangular waveform			
Maximum peak one cycle non-repetitive surge current	1	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with	27 000	A
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	3000	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 21 A, L = 1 mH		216	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical 48		А	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
	V <sub>FM</sub> <sup>(1)</sup>	240 A	T <sub>.1</sub> = 25 °C	0.54	V
Maximum forward voltage drop		480 A	1j=25 0	0.73	
See fig. 1		240 A	T <sub>1</sub> = 125 °C	0.47	
		480 A	1j = 125 0	0.7	
Maximum reverse leakage current		T <sub>J</sub> = 25 °C	$V_{\rm B} = Rated V_{\rm B}$	20	mA
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = naleu V <sub>R</sub>	1120	
Maximum junction capacitance	CT	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		14 800	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs	

#### Note

<sup>(1)</sup> Pulse width = 500  $\mu$ s

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and s temperature range	torage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation 0.1		2044	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05	°C/W	
Approximate weight	Approximate weight			30	g	
Approximate weight				1.06	oz.	
Mounting torque	minimum			3 (26.5)	N · m	
Mounting torque maximum			Non-lubricated threads	4 (35.4)		
	minimum		Non-lubricated threads	3.4 (30)	(lbf · in)	
Terminal torque maximur				5 (44.2)		
Case style				HALF-PAI	<pre>&lt; module</pre>	

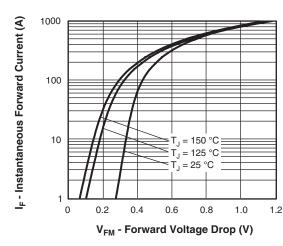


Fig. 1 - Maximum Forward Voltage Drop Characteristics

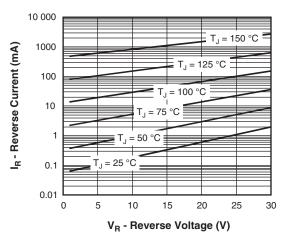


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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## VS-242NQ030PbF

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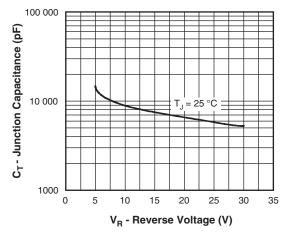


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

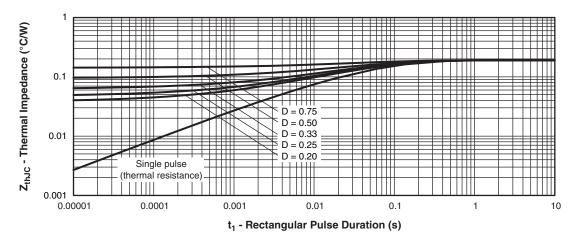
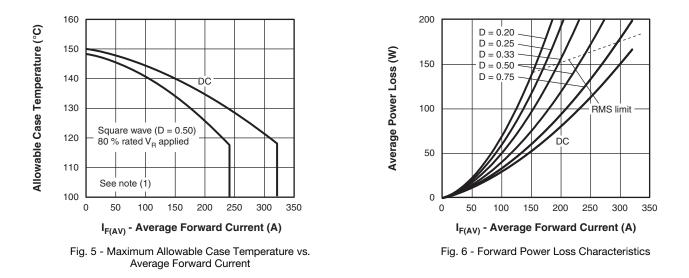


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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## VS-242NQ030PbF

## **Vishay Semiconductors**



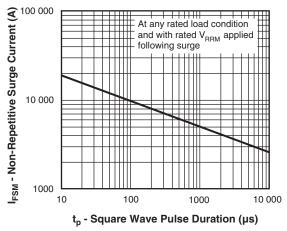


Fig. 7 - Maximum Non-Repetitive Surge Current

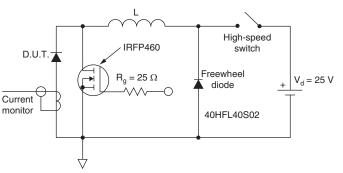


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

### **ORDERING INFORMATION TABLE**

Device code	VS-	24	2	Ν	Q	030	PbF
	1	2	3	4	5	6	7
	1     -       2     -       3     -       4     -       5     -       6     -       7     -	<ul> <li>Ave</li> <li>Pro</li> <li>N =</li> <li>Q =</li> <li>Vol</li> </ul>	hay Sen erage cu duct sili Not iso Schottl tage rat ad (Pb)-1	irrent rat con ider lated ky rectifi ing (030	ting (x 1 ntification er diode	0) n	

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95020					
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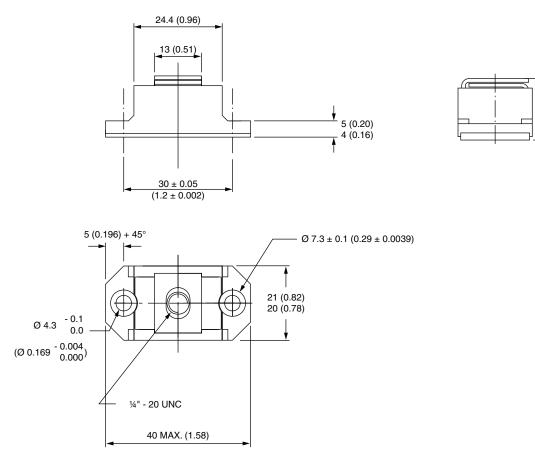
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17.5 (0.69) 16.5 (0.65)



### **DIMENSIONS** in millimeters (inches)

SHAY





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