

ATDS3534UV405B

3.45 x 3.45 mm UV LED With Ceramic Substrate



FEATURES

- High power UV-A LED
- Dimensions: 3.45 mm x 3.45 mm x 2.0 mm
- Small package with high efficiency
- Surface mount technology
- Package: 1000pcs / reel
- Soldering methods: IR reflow soldering
- Moisture sensitivity level: 1
- RoHS compliant

APPLICATIONS

- Photocatalytic Purification
- Blood and Counterfeit money detection
- UV curing in nail salon, dental, and poster printing applications
- UV Sensor Light

PACKAGE MATERIALS

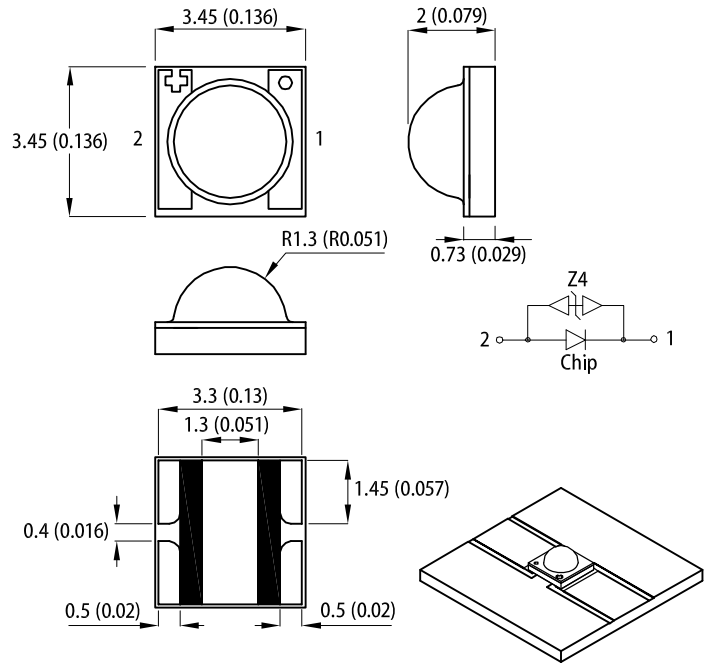
- Material as follows:
Package: Ceramics
Encapsulating resin: Silicone resin
Electrodes: Au plating

ATTENTION

Observe precautions for handling electrostatic discharge sensitive devices

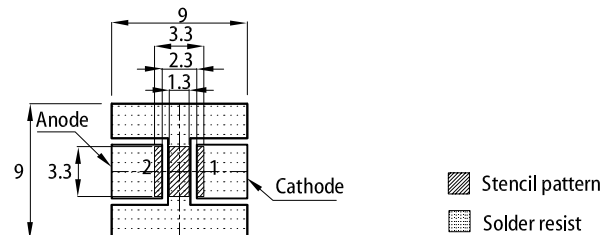


PACKAGE DIMENSIONS



RECOMMENDED SOLDERING PATTERN

(units : mm; tolerance : ± 0.1)



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.2(0.008)$ unless otherwise noted.
3. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.
4. The device has a single mounting surface. The device must be mounted according to the specifications.

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	$\Phi_e(mW)$ ^[2] @500mA*700mA				Viewing Angle ^[1]
			Code.	Min.	Max.	Typ.	2 θ 1/2
ATDS3534UV405B	Ultraviolet (InGaN)	Water Clear	C14	640	740	800	120°
			C15	740	850		
			C16	850	1000		
			-	-	-	*1100	

Notes:

1. θ 1/2 is the angle from optical centerline where the radiant intensity is 1/2 of the optical peak value.
2. *Radiant flux with asterisk is measured at 700mA; Radiant flux: $\pm 15\%$.
3. Radiant flux value is traceable to CIE127-2007 standards.

ELECTRICAL / OPTICAL CHARACTERISTICS at $T_A=25^{\circ}\text{C}$

Parameter	Symbol	Value	Unit
Wavelength at Peak Emission $I_F = 500\text{mA}$ [Min.]	λ_{peak}	400	nm
Wavelength at Peak Emission $I_F = 500\text{mA}$ [Typ.]		405	
Wavelength at Peak Emission $I_F = 500\text{mA}$ [Max.]		410	
Spectral Bandwidth at 50% Φ REL MAX $I_F = 500\text{mA}$ [Typ.]	$\Delta\lambda$	15	nm
Forward Voltage $I_F = 500\text{mA}$ [Typ.]	$V_F^{[1]}$	3.4	V
Forward Voltage $I_F = 500\text{mA}$ [Max.]		3.9	
Allowable Reverse Current [Max.]	I_R	85	mA
Temperature Coefficient of V_F $I_F = 500\text{mA}$, $-10^{\circ}\text{C} \leq T \leq 100^{\circ}\text{C}$	TC_V	-3.0	mV/ $^{\circ}\text{C}$

Notes:

1. Forward voltage: $\pm 0.1\text{V}$.

2. Wavelength value is traceable to CIE127-2007 standards.

3. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at $T_A=25^{\circ}\text{C}$

Parameter	Symbol	Value	Unit
Power Dissipation	P_D	2.8	W
Reverse Voltage	V_R	5	V
Junction Temperature	$T_J^{[1]}$	115	$^{\circ}\text{C}$
Operating Temperature	T_{op}	-40 to +100	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-40 to +115	$^{\circ}\text{C}$
DC Forward Current	$I_F^{[1]}$	700	mA
Peak Forward Current	$I_{\text{FM}}^{[2]}$	1000	mA
Electrostatic Discharge Threshold (HBM)	-	8000	V
Thermal Resistance (Junction / Ambient)	$R_{\text{th JA}}^{[1]}$	10	$^{\circ}\text{C/W}$
Thermal Resistance (Junction / Solder point)	$R_{\text{th JS}}^{[1]}$	5	$^{\circ}\text{C/W}$

Notes:

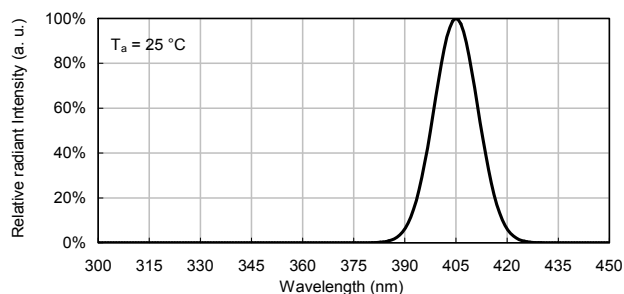
1. Results from mounting on metal core PCB, mounted on pc board-metal core PCB is recommend for lowest thermal resistance.

2. 1/10 Duty Cycle, 0.1ms Pulse Width.

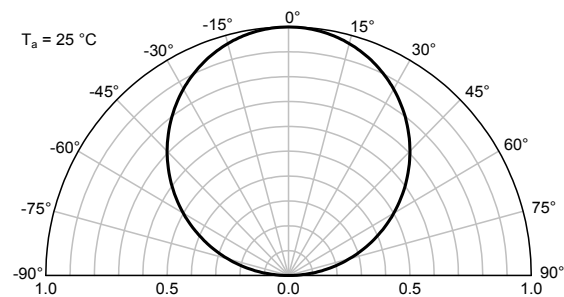
3. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

TECHNICAL DATA

RELATIVE INTENSITY vs. WAVELENGTH

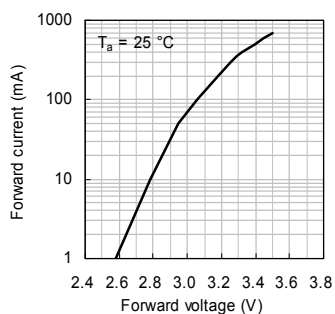


SPATIAL DISTRIBUTION

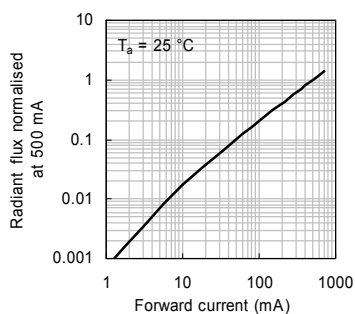


ULTRAVIOLET

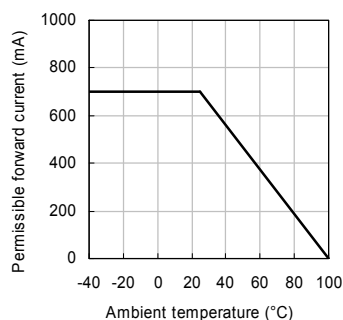
Forward Current vs. Forward Voltage



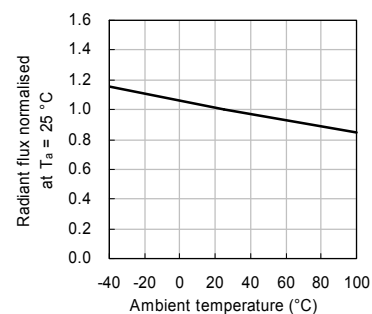
Radiant Flux vs. Forward Current



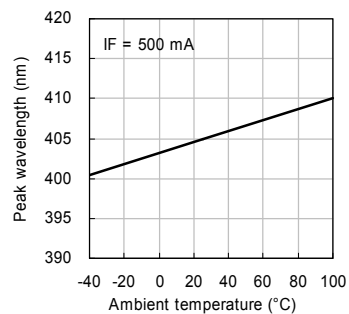
Forward Current Derating Curve



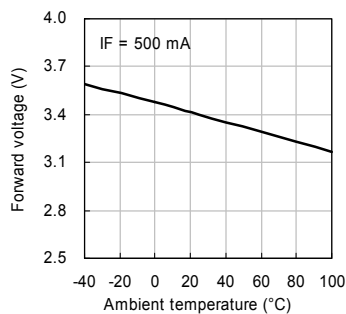
Radiant Flux vs. Ambient Temperature



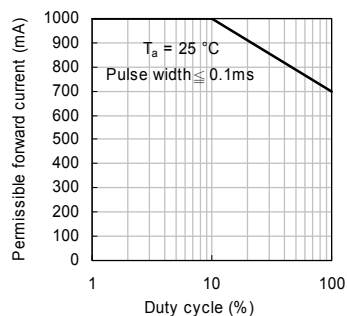
Peak Wavelength vs. Ambient Temperature



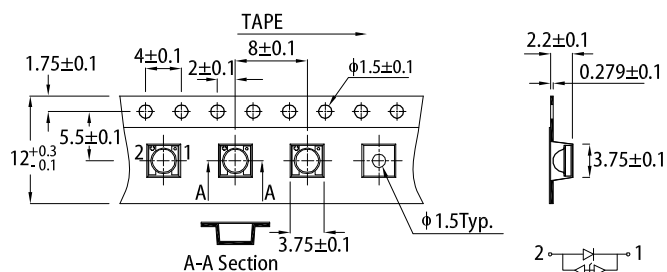
Forward Voltage vs. Ambient Temperature



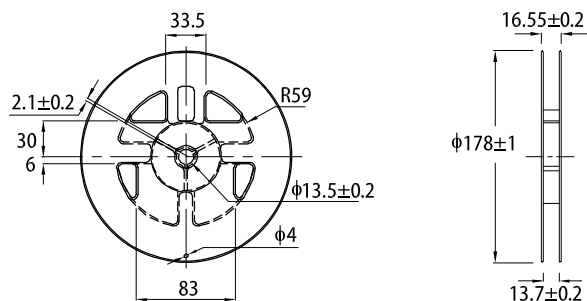
Permissible Forward Current vs. Duty Cycle



TAPE SPECIFICATIONS (units : mm)



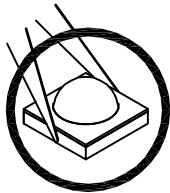
REEL DIMENSION (units : mm)



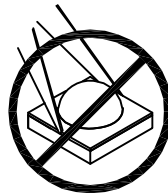
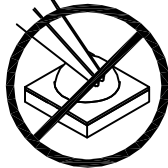
HANDLING PRECAUTIONS

Compare to epoxy encapsulant that is hard and brittle, silicone is softer and flexible. Although its characteristic significantly reduces thermal stress, it is more susceptible to damage by external mechanical force. As a result, special handling precautions need to be observed during assembly using silicone encapsulated LED products. Failure to comply might lead to damage and premature failure of the LED.

1. Handle the component along the side surfaces by using forceps or appropriate tools.



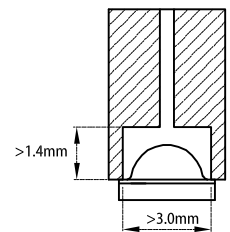
2. Do not directly touch or handle the silicone lens surface. It may damage the internal circuitry.



3. Do not stack together assembled PCBs containing exposed LEDs. Impact may scratch the silicone lens or damage the internal circuitry.



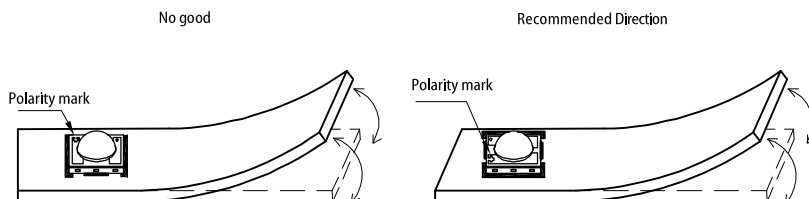
- 4-1. There should be enough space inside the nozzle to avoid contact with the dome lens during pick up.
- 4-2. The inner diameter of the SMD pickup nozzle should not exceed the size of the LED to prevent air leaks.
- 4-3. A pliable material is suggested for the nozzle tip to avoid scratching or damaging the LED surface during pickup.
- 4-4. The dimensions of the component must be accurately programmed in the pick-and-place machine to insure precise pickup and avoid damage during production.
5. As silicone encapsulation is permeable to gases, some corrosive substances such as H_2S might corrode silver plating of lead-frame. Special care should be taken if an LED with silicone encapsulation is to be used near such substances.



Designing the Position of LED on a Board

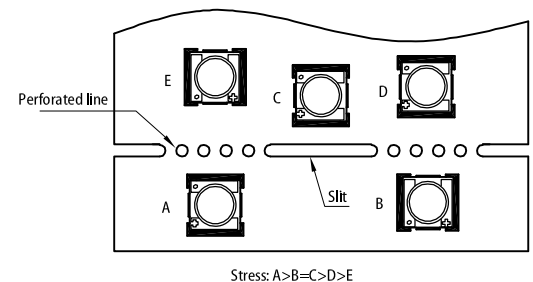
1. No twist / warp / bent / or other stress shall be applied to the board after mounting LED with solder to avoid a crack of LED package. Refer to the following recommended position and direction of LED.

2. Depending on the position and direction of LED, the mechanical stress on the LED package can be changed. Refer to the following figure.



Appropriate LED mounting is to place perpendicularly against the stress affected side.

3. Do not split board by hand. Split with exclusive special tool.
4. If an aluminum circuit board is used, a large stress by thermal shock might cause a solder crack. For this reason, it is recommended an appropriate verification should be taken before use.



JEDEC Moisture Sensitivity

Level	Floor Life		Soak Requirements			
			Standard		Accelerated Equivalent	
	Time	Conditions	Time (hours)	Conditions	Time (hours)	Conditions
1	Unlimited	$\leq 30^\circ\text{C} / 85\% \text{ RH}$	168 + 5 / - 0	$85^\circ\text{C} / 85\% \text{ RH}$	-	-

Kingbright recommends keeping the LEDs in the sealed moisture-barrier packaging until immediately prior to use. Any unused LEDs should be returned to the moisture-barrier bag and closed immediately after use.

ESD Protection During Production

Electric static discharge can result when static-sensitive products come in contact with the operator or other conductors.

The following procedures may decrease the possibility of ESD damage:

1. Minimize friction between the product and surroundings to avoid static buildup.
2. All production machinery and test instruments must be electrically grounded.
3. Operators must wear anti-static bracelets.
4. Wear anti-static suit when entering work areas with conductive machinery.
5. Set up ESD protection areas using grounded metal plating for component handling.
6. All workstations that handle IC and ESD-sensitive components must maintain an electrostatic potential of 150V or less.
7. Maintain a humidity level of 50% or higher in production areas.
8. Use anti-static packaging for transport and storage.
9. All anti-static equipment and procedures should be periodically inspected and evaluated for proper functionality.

Heat Generation

1. Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design.

The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board, as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.

2. Please determine the operating current with consideration of the ambient temperature local to the LED and refer to the plot of Permissible Forward Current vs. Ambient temperature on characteristics in this specification.

Please also take measures to remove heat from the area near the LED to improve the operational characteristics on the LED.

3. The equation ① indicates correlation between T_j and T_a , and the equation ② indicates correlation between T_j and T_s .

$$T_j = T_a + R_{th JA} * W \quad \text{..... ①}$$

$$T_j = T_s + R_{th JS} * W \quad \text{..... ②}$$

T_j = dice junction temperature: °C

T_a = ambient temperature: °C

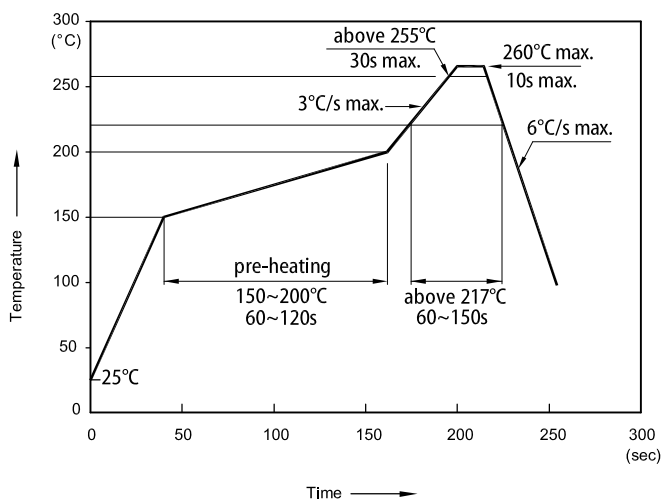
T_s = solder point temperature: °C

$R_{th JA}$ = heat resistance from dice junction temperature to ambient temperature: °C / W

$R_{th JS}$ = heat resistance from dice junction temperature to T_s measuring point: °C / W

W = inputting power ($I_F \times V_F$): W

REFLOW SOLDERING PROFILE for LEAD-FREE SMD PROCESS



Notes:

1. Don't cause stress to the LEDs while it is exposed to high temperature.
2. The maximum number of reflow soldering passes is 2 times.
3. Reflow soldering is recommended. Other soldering methods are not recommended as they might cause damage to the product.

RELIABILITY TEST ITEMS AND CONDITIONS

The reliability of products shall be satisfied with items listed below

Lot Tolerance Percent Defective (LTPD) : 10%

No.	Test Item	Standards	Test Condition	Test Times / Cycles	Number of Damaged
1	Continuous operating test	-	$T_a = 25^{\circ}\text{C} + 10/-5^{\circ}\text{C}$, RH = 55+/-20%RH I_F = maximum rated current*	1,000 h	0 / 22
2	High Temp. operating test	-	$T_a = 100^{\circ}\text{C} (+/-10^{\circ}\text{C})$ I_F = maximum rated current*	1,000 h	0 / 22
3	Low Temp. operating test	-	$T_a = -40^{\circ}\text{C} + 3/-5^{\circ}\text{C}$ I_F = maximum rated current*	1,000 h	0 / 22
4	High temp. storage test	JEITA ED-4701/200 201	$T_a = 100^{\circ}\text{C} (+/-10^{\circ}\text{C})$ T_a = maximum rated storage temperature	1,000 h	0 / 22
5	Low temp. storage test	JEITA ED-4701/200 202	$T_a = -40^{\circ}\text{C} + 3/-5^{\circ}\text{C}$	1,000 h	0 / 22
6	High temp. & humidity storage test	JEITA ED-4701/100 103	$T_a = 60^{\circ}\text{C} + 5/-3^{\circ}\text{C}$, RH = 90+5/-10%RH	1,000 h	0 / 22
7	High temp. & humidity operating test	-	$T_a = 60^{\circ}\text{C} + 5/-3^{\circ}\text{C}$, RH = 90+5/-10%RH I_F = maximum rated current*	1,000 h	0 / 22
8	Resistance to Soldering Heat (Reflow Soldering)	JEITA ED-4701/300 301	$T_{sld} = 260^{\circ}\text{C}$, 10sec	2 times	0 / 22
9	Solderability (Reflow Soldering)	JEITA ED-4701/303 303A	$T_{sld} = 245^{\circ}\text{C} + 5/-1^{\circ}\text{C}$, 5+/-1sec	1 time over 95%	0 / 22
10	Temperature Cycle operating test	-	$-40^{\circ}\text{C}(30\text{min}) \sim 25^{\circ}\text{C}(5\text{min}) \sim 100^{\circ}\text{C}(30\text{min}) \sim 25^{\circ}\text{C}(5\text{min})$ I_F = derated current at 100°C	10 cycles	0 / 22
11	Temperature Cycle	JEITA ED-4701/100 105	$-40^{\circ}\text{C}(30\text{min}) \sim 25^{\circ}\text{C}(5\text{min}) \sim 100^{\circ}\text{C}(30\text{min}) \sim 25^{\circ}\text{C}(5\text{min})$	100 cycles	0 / 22
12	Thermal shock test	MIL-STD-202G	$T_a = -40^{\circ}\text{C}(15\text{min}) \sim 100^{\circ}\text{C}(15\text{min})$	500 cycles	0 / 22
13	Electric Static Discharge (ESD)	JEITA ED-4701/300 304	$C = 100\text{pF}$, $R = 1.5\text{K}\Omega$ $V = 8000\text{V}$	3 times Negative / Positive	0 / 22
14	Vibration test	JEITA ED-4701/400 403	100 ~ 2000 ~ 100HZ Sweep 4min. 200m/s ² 3directions, 4cycles	48 min.	0 / 22

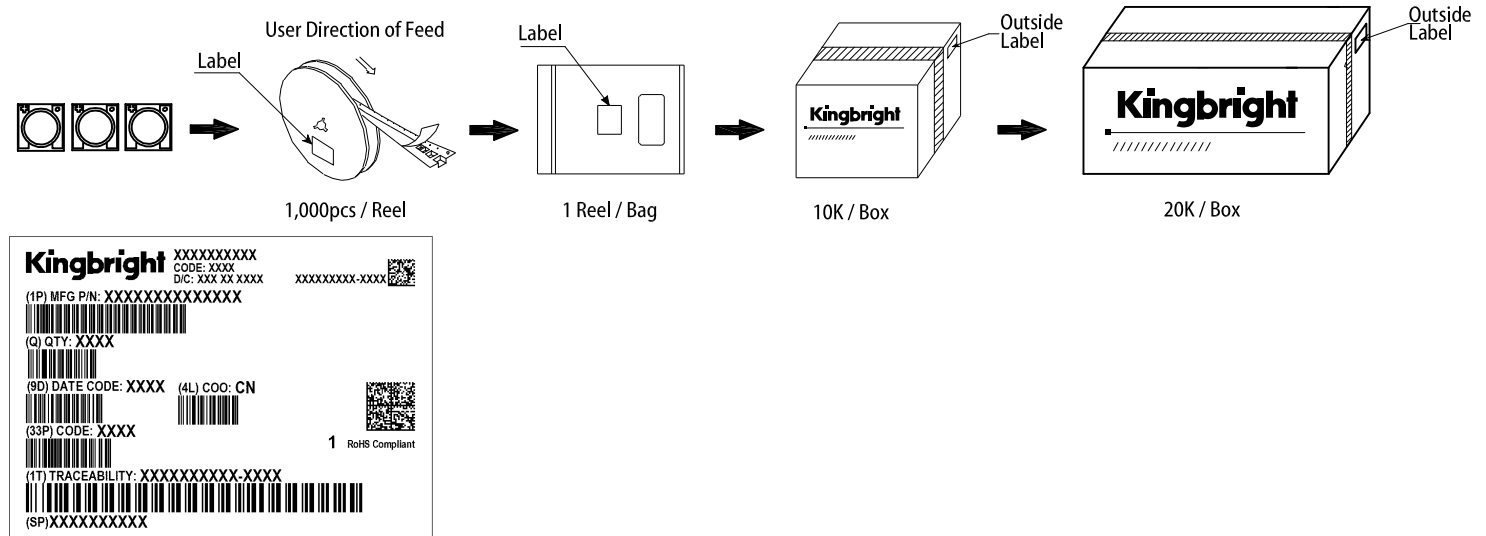
Note: Refer to forward current vs. derating curve diagram.

Criteria For Judging Damage

Item	Symbol	Test Conditions	Criteria for Judgement	
			Min.	Max.
Forward Voltage	V_F	$I_F = 500\text{mA}$	-	Initial Level x 1.1
Radiant Flux	Φ_e	$I_F = 500\text{mA}$	Initial Level x 0.7	-

Note: The test is performed after the board is cooled down to the room temperature.

PACKING & LABEL SPECIFICATIONS



Packaging

- 1.The LEDs are packed in cardboard boxes after taping.
- 2.The label on the minimum packing unit shows: Part Number, Lot Number, Ranking, Quantity.
- 3.In order to protect the LEDs from mechanical shock, we pack them in cardboard boxes for transportation.
- 4.The LEDs may be damaged if the boxes are dropped or receive a strong impact against them, so precautions must be taken to prevent any damage.
- 5.The boxes are not water resistant and therefore must be kept away from water and moisture.
- 6.When the LEDs are transported, we recommend that you use the same packing methods as Kingbright's.

PRECAUTIONARY NOTES

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
3. When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
4. The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening liabilities, such as automotive or medical usage, please consult with Kingbright representative for further assistance.
5. The contents and information of this document may not be reproduced or re-transmitted without permission by Kingbright.
6. All design applications should refer to Kingbright application notes available at <https://www.KingbrightUSA.com/ApplicationNotes>