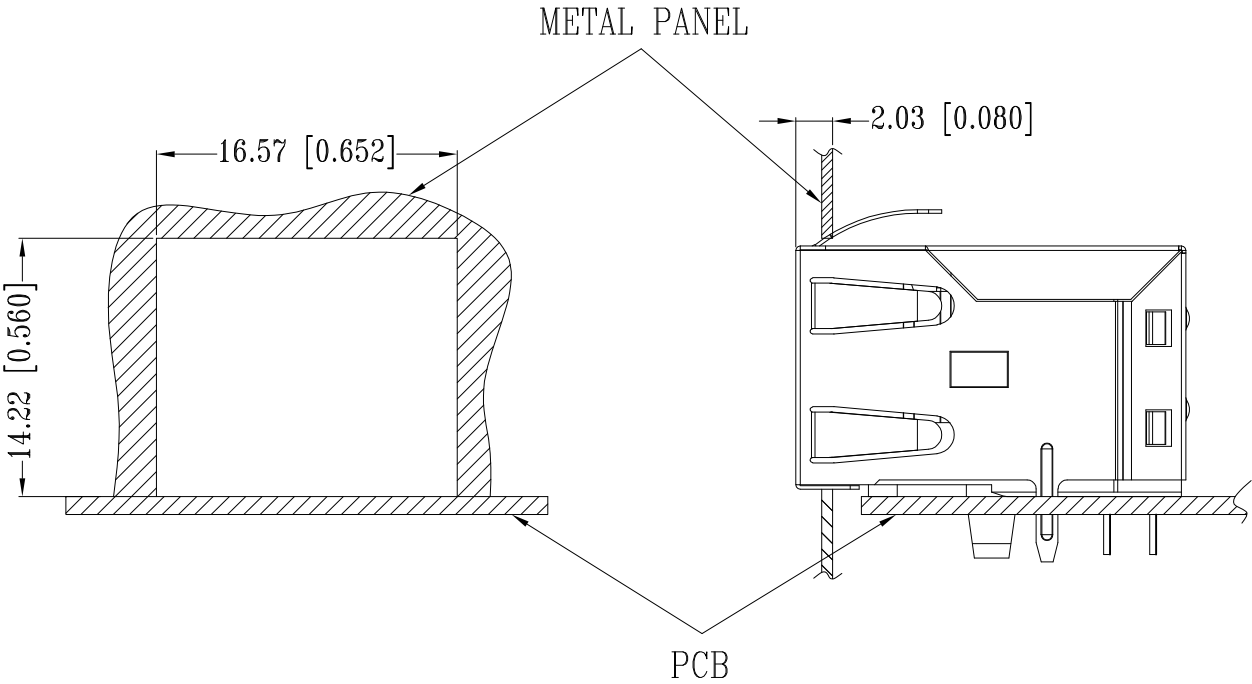
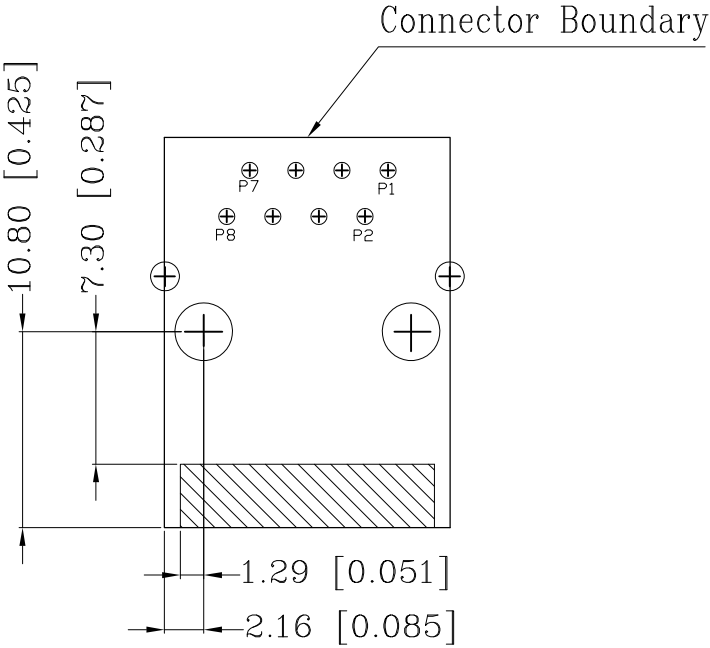
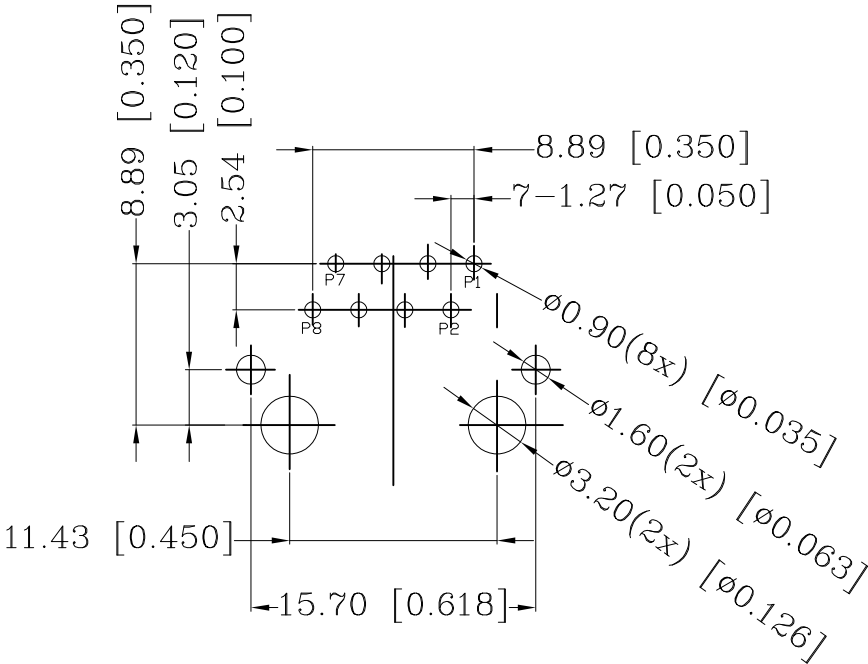
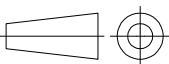


REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

SUGGESTED PCB LAYOUT

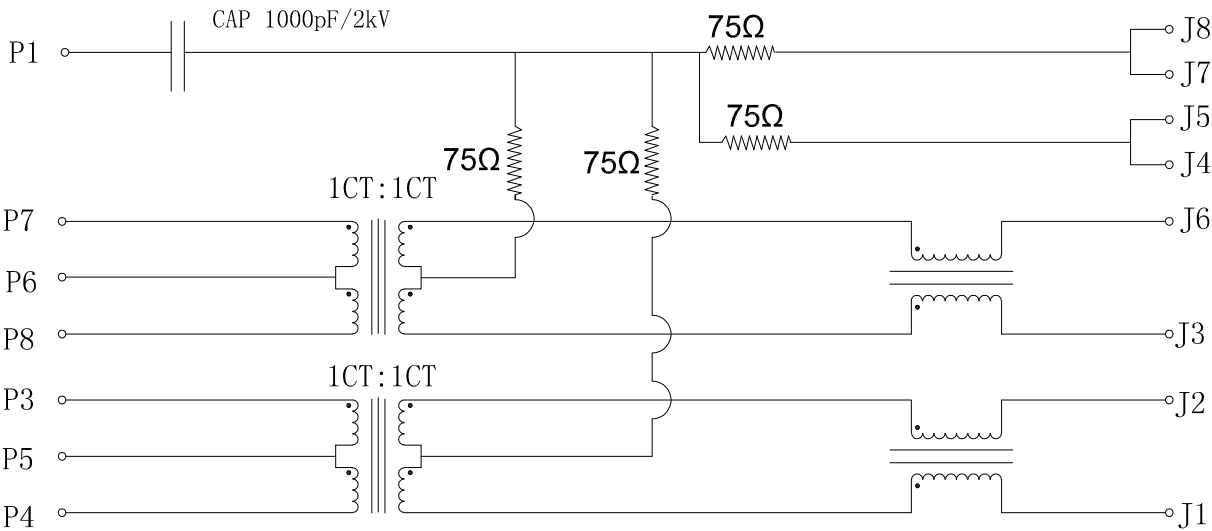


UNIT: mm / inch
TOLERANCES: ± 0.10 / 0.004

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN GANESH C M 16AUG2016	 TE Connectivity				
		CHK FRANZ MUELLER 16AUG2016					
DIMENSIONS: mm [INCHES]	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD MARTIN SZELAG 16AUG2016	NAME RJ45 JACK INT.MAG. 10/100 1x1 INV.				
	0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± - 	PRODUCT SPEC 108-94552	-				
		APPLICATION SPEC 114-94447	-				
MATERIAL 	FINISH 	WEIGHT	SIZE A3	CAGE CODE 00779	DRAWING NO C-2301994-1	RESTRICTED TO -	
		CUSTOMER DRAWING					
			SCALE 3:1	SHEET 2 OF 3		REV B2	

REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

ELECTRICAL CIRCUIT DIAGRAM



- 1.0 Turn Ratio @100kHz: (P4~P3):(J1~J2) = 1:1±2%
(P8~P7):(J3~J6) = 1:1±2%
- 2.0 Primary Inductance: 350μH MIN. @100kHz, 0.1V 8mA DC BIAS
- 3.0 DC Resistance: 1.2 OHMS MAX.
- 4.0 Insertion Loss: 1-100MHz -1.2dB MAX.
- 5.0 Return Loss: 1-30MHz -16dB MIN.
30-60MHz -12dB MIN.
60-80MHz -10dB MIN.
- 6.0 CROSS TALK: 1-100MHz -30dB MIN.
- 7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN.
- 8.0 Isolation: PHY Side to Line Side: 2250VDC

2301994-1
WWYY-xxxx

CONTROL CODE

DATE CODE

"0213"

YY-YEAR

WW-WEEK

FCC

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN GANESH C M 16AUG2016	TE Connectivity		
		CHK FRANZ MUELLER 16AUG2016			
DIMENSIONS: mm [INCHES]		APVD MARTIN SZELAG 16AUG2016	NAME RJ45 JACK INT.MAG. 10/100 1x1 INV.		
		PRODUCT SPEC 108-94552			
		APPLICATION SPEC 114-94447			
MATERIAL 1		WEIGHT	SIZE A3	CAGE CODE 00779	DRAWING NO C-2301994-1
		FINISH 1	RESTRICTED TO -		
CUSTOMER DRAWING			SCALE 3:1	SHEET 3 of 3	REV B2