

Vishay Semiconductors

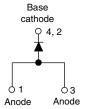
COMPLIANT

HALOGEN

FREE

Schottky Rectifier, 3.5 A





PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	3.5 A			
V_{R}	60 V			
V _F at I _F	See Electrical table			
I _{RM}	30 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			
E _{AS}	6 mJ			

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Small foot print, surface mountable
- · High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>



The VS-30WQ06FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	3.5	А	
V _{RRM}		60	V	
I _{FSM}	t _p = 5 μs sine	490	A	
V _F	3 A _{pk} , T _J = 125 °C	0.53	V	
T_{J}		- 40 to 150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-30WQ06FNHM3	UNITS
Maximum DC reverse voltage	V_R	60	V
Maximum working peak reverse voltage	V_{RWM}	60	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 133 °C, rectangular waveform		3.5	
Maximum peak one cycle non-repetitive surge current	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	490	Α
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	70	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		1.0	А



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V (1)	3 A	T _J = 25 °C	0.61	V
Maximum forward voltage drop		6 A		0.76	
See fig. 1	V _{FM} ⁽¹⁾	3 A	T _J = 125 °C	0.53	
		6 A		0.65	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2	mA
See fig. 2	IRM (**/	T _J = 125 °C		30	IIIA
Threshold voltage	V _{F(TO)}	$T_{J} = T_{J}$ maximum		0.38	V
Forward slope resistance	r _t			34.31	mΩ
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		145	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

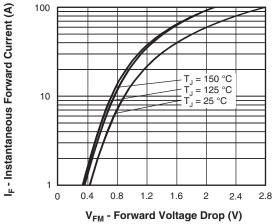
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	4.7	°C/W
Approximate weight			0.3	g
Approximate weight			0.01	oz.
Marking device		Case style D-PAK	30WQ0	6FNH

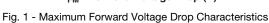
Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

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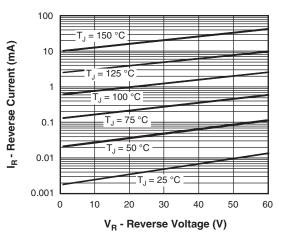


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

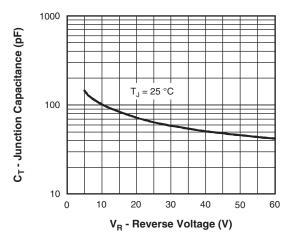


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

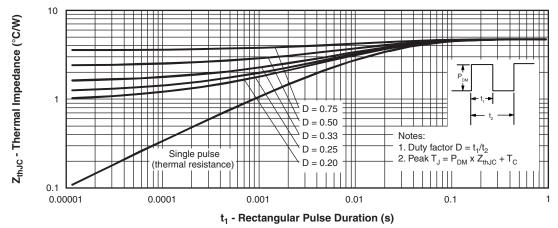
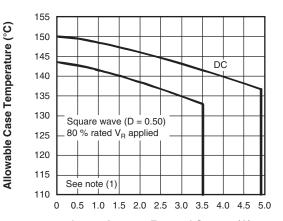


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



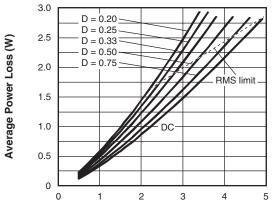
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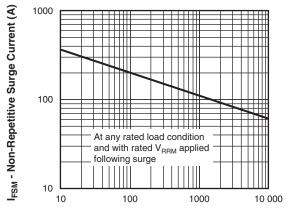
 $I_{F(AV)}$ - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

Note

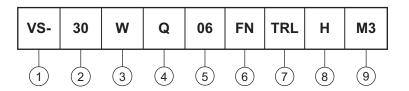
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (3.5 A)

Package identifier:

W = D-PAK

4 - Schottky "Q" series

- Voltage rating (06 = 60 V)

6 - FN = TO-252AA (D-PAK)

7 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

8 - H = AEC-Q101 qualified

9 - Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-30WQ06FNHM3	75	3000	Antistatic plastic tube		
VS-30WQ06FNTRHM3	2000	2000	13" diameter reel		
VS-30WQ06FNTRRHM3	3000	3000	13" diameter reel		
VS-30WQ06FNTRLHM3	3000	3000	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95519				
Part marking information <u>www.vishay.com/doc?95518</u>				
Packaging information	www.vishay.com/doc?95033			



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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