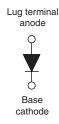


# **High Performance Schottky Rectifier, 120 A**

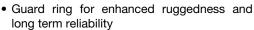




PRODUCT SUMMARY				
I <sub>F(AV)</sub>	120 A			
$V_{R}$	100 V			
Package	HALF-PAK (D-67)			
Circuit	Single diode			

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation



- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





#### **DESCRIPTION**

The VS-123NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I <sub>F(AV)</sub>	Rectangular waveform	120	Α		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	12 800	Α		
V <sub>F</sub>	120 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.73	V		
T <sub>J</sub>	Range	-55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-123NQ100PbF	UNITS	
Maximum DC reverse voltage	$V_{R}$	100		
Maximum working peak reverse voltage	V <sub>RWM</sub>	100		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 133 °C, rectangular waveform		120	А
Maximum peak one cycle non-repetitive surge current	l==	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	12 800	А
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	1800	^
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 5.5 A, L = 1 mH		15	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	120 A	T <sub>J</sub> = 25 °C	0.91	V
		240 A		1.26	
		120 A	- T <sub>J</sub> = 125 °C	0.73	
		240 A		0.9	
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	3	mA
See fig. 2		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	40	
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		2650	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

<sup>&</sup>lt;sup>(1)</sup> Pulse width =  $500 \mu s$ 

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora	age temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	0.38	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
				1.06	OZ.	
	minimum			3 (26.5)		
Mounting torque	maximum		Non-lubricated threads	4 (35.4)	N · m (lbf · in)	
Terminal torque	minimum			3.4 (30)		
	maximum			5 (44.2)		
Case style				HALF-PAK module		

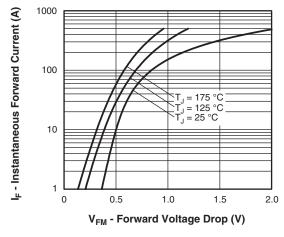


Fig. 1 - Maximum Forward Voltage Drop Characteristics

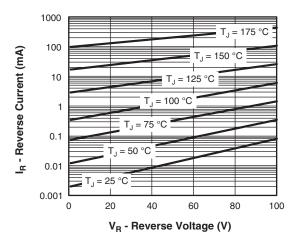


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



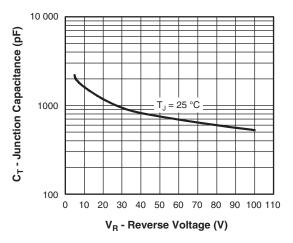


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

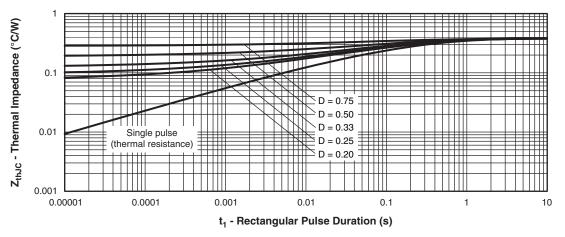


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

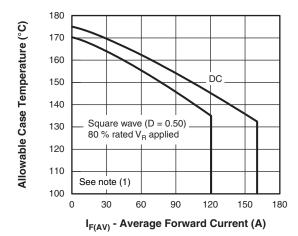


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

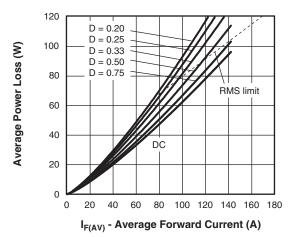
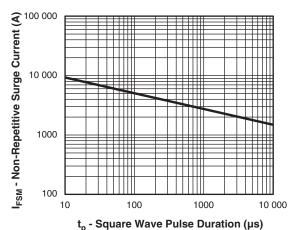


Fig. 6 - Forward Power Loss Characteristics



tp Oquate Wave Fullet Buration (po)

Fig. 7 - Maximum Non-Repetitive Surge Current

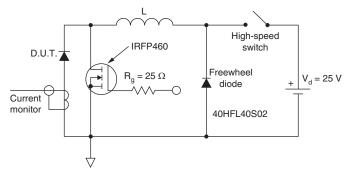


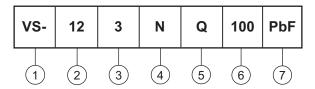
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 

#### **ORDERING INFORMATION TABLE**

#### Device code



- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- 3 Product silicon identification
- 4 N = Not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (100 = 100 V)
- 7 Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			



### **D-67 HALF-PAK**

### **DIMENSIONS** in millimeters (inches)









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Vishay

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Revision: 02-Oct-12 Document Number: 91000