

# NTGS3441, NVGS3441

## Power MOSFET

1 Amp, 20 Volts, P-Channel TSOP-6



ON Semiconductor®

<http://onsemi.com>

**1 AMPERE**  
**20 VOLTS**  
**R<sub>DS(on)</sub> = 90 mΩ**

### Features

- Ultra Low R<sub>DS(on)</sub>
- Higher Efficiency Extending Battery Life
- Miniature TSOP-6 Surface Mount Package
- NV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

### Applications

- Power Management in Portable and Battery-Powered Products, i.e.: Cellular and Cordless Telephones, and PCMCIA Cards

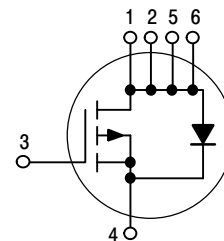
### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-to-Source Voltage – Continuous	V <sub>GS</sub>	± 8.0	V
Thermal Resistance Junction-to-Ambient (Note 1)	R <sub>θJA</sub>	244	°C/W
Total Power Dissipation @ T <sub>A</sub> = 25°C	P <sub>d</sub>	0.5	W
Drain Current – Continuous @ T <sub>A</sub> = 25°C	I <sub>D</sub>	-1.65	A
– Pulsed Drain Current (T <sub>p</sub> < 10 μS)	I <sub>DM</sub>	-10	A
Thermal Resistance Junction-to-Ambient (Note 2)	R <sub>θJA</sub>	128	°C/W
Total Power Dissipation @ T <sub>A</sub> = 25°C	P <sub>d</sub>	1.0	W
Drain Current – Continuous @ T <sub>A</sub> = 25°C	I <sub>D</sub>	-2.35	A
– Pulsed Drain Current (T <sub>p</sub> < 10 μS)	I <sub>DM</sub>	-14	A
Thermal Resistance Junction-to-Ambient (Note 3)	R <sub>θJA</sub>	62.5	°C/W
Total Power Dissipation @ T <sub>A</sub> = 25°C	P <sub>d</sub>	2.0	W
Drain Current – Continuous @ T <sub>A</sub> = 25°C	I <sub>D</sub>	-3.3	A
– Pulsed Drain Current (T <sub>p</sub> < 10 μS)	I <sub>DM</sub>	-20	A
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C
Maximum Lead Temperature for Soldering Purposes for 10 Seconds	T <sub>L</sub>	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Minimum FR-4 or G-10 PCB, operating to steady state.
2. Mounted onto a 2" square FR-4 board (1 in sq, 2 oz. Cu. 0.06" thick single sided), operating to steady state.
3. Mounted onto a 2" square FR-4 board (1 in sq, 2 oz. Cu. 0.06" thick single sided), t < 5.0 seconds.

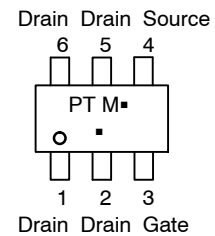
### P-Channel



### MARKING DIAGRAM & PIN ASSIGNMENT



TSOP-6  
CASE 318G  
STYLE 1



PT = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

### ORDERING INFORMATION

Device	Package	Shipping†
NTGS3441T1G	TSOP-6 (Pb-Free)	3000 / Tape & Reel
NVGS3441T1G	TSOP-6 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NTGS3441, NVGS3441

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted) (Notes 4 & 5)

Characteristic	Symbol	Min	Typ	Max	Unit	
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = -10 μA)	V <sub>(BR)DSS</sub>	-20	-	-	Vdc	
Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0 Vdc, V <sub>DS</sub> = -20 Vdc, T <sub>J</sub> = 25°C) (V <sub>GS</sub> = 0 Vdc, V <sub>DS</sub> = -20 Vdc, T <sub>J</sub> = 70°C)	I <sub>DSS</sub>	-	-	-1.0 -5.0	μAdc	
Gate-Body Leakage Current (V <sub>GS</sub> = -8.0 Vdc, V <sub>DS</sub> = 0 Vdc)	I <sub>GSS</sub>	-	-	-100	nAdc	
Gate-Body Leakage Current (V <sub>GS</sub> = +8.0 Vdc, V <sub>DS</sub> = 0 Vdc)	I <sub>GSS</sub>	-	-	100	nAdc	
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μAdc)	V <sub>GS(th)</sub>	-0.45	-1.05	-1.50	Vdc	
Static Drain-Source On-State Resistance (V <sub>GS</sub> = -4.5 Vdc, I <sub>D</sub> = -3.3 Adc) (V <sub>GS</sub> = -2.5 Vdc, I <sub>D</sub> = -2.9 Adc)	R <sub>DS(on)</sub>	-	0.069 0.117	0.090 0.135	Ω	
Forward Transconductance (V <sub>DS</sub> = -10 Vdc, I <sub>D</sub> = -3.3 Adc)	g <sub>FS</sub>	-	6.8	-	Mhos	
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	(V <sub>DS</sub> = -5.0 Vdc, V <sub>GS</sub> = 0 Vdc, f = 1.0 MHz)	C <sub>ISS</sub>	-	480	-	pF
Output Capacitance		C <sub>OSS</sub>	-	265	-	pF
Reverse Transfer Capacitance		C <sub>RSS</sub>	-	100	-	pF
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	(V <sub>DD</sub> = -20 Vdc, I <sub>D</sub> = -1.6 Adc, V <sub>GS</sub> = -4.5 Vdc, R <sub>g</sub> = 6.0 Ω)	t <sub>d(on)</sub>	-	13	25	ns
Rise Time		t <sub>r</sub>	-	23.5	45	ns
Turn-Off Delay Time		t <sub>d(off)</sub>	-	27	50	ns
Fall Time		t <sub>f</sub>	-	24	45	ns
Total Gate Charge	(V <sub>DS</sub> = -10 Vdc, V <sub>GS</sub> = -4.5 Vdc, I <sub>D</sub> = -3.3 Adc)	Q <sub>tot</sub>	-	6.2	14	nC
Gate-Source Charge		Q <sub>gs</sub>	-	1.3	-	nC
Gate-Drain Charge		Q <sub>gd</sub>	-	2.5	-	nC
<b>BODY-DRAIN DIODE RATINGS</b>						
Diode Forward On-Voltage	(I <sub>S</sub> = -1.6 Adc, V <sub>GS</sub> = 0 Vdc)	V <sub>SD</sub>	-	-0.88	-1.2	Vdc
Diode Forward On-Voltage	(I <sub>S</sub> = -3.3 Adc, V <sub>GS</sub> = 0 Vdc)	V <sub>SD</sub>	-	-0.98	-	Vdc
Reverse Recovery Time	(I <sub>S</sub> = -1.6 Adc, dI <sub>S</sub> /dt = 100 A/μs)	t <sub>rr</sub>	-	30	60	ns

4. Indicates Pulse Test: P.W. = 300 μsec max, Duty Cycle = 2%.

5. Handling precautions to protect against electrostatic discharge are mandatory.

TYPICAL ELECTRICAL CHARACTERISTICS

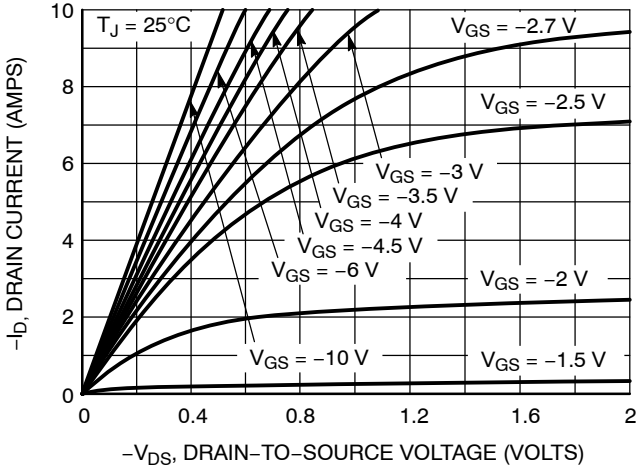


Figure 1. On-Region Characteristics

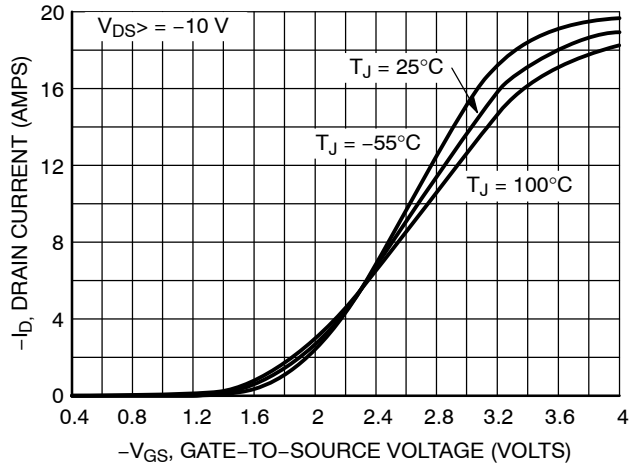


Figure 2. Transfer Characteristics

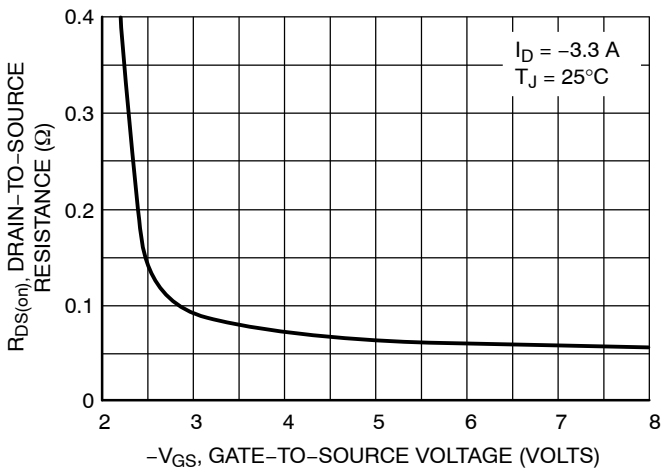


Figure 3. On-Resistance vs. Gate-to-Source Voltage

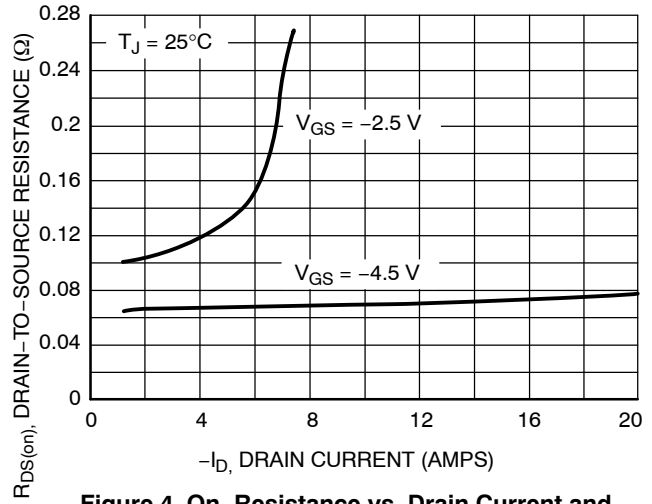


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

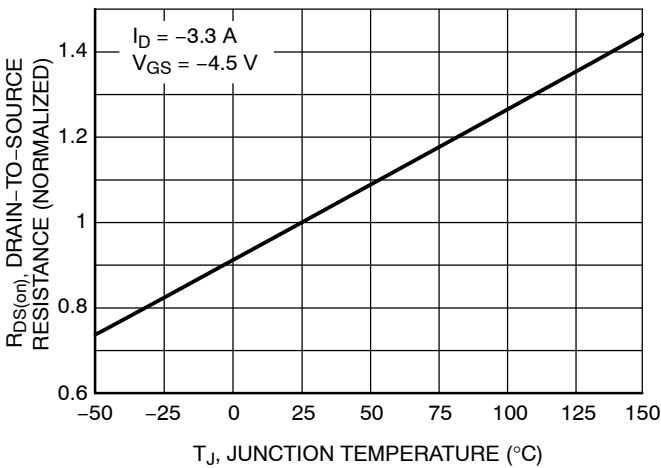


Figure 5. On-Resistance Variation with Temperature

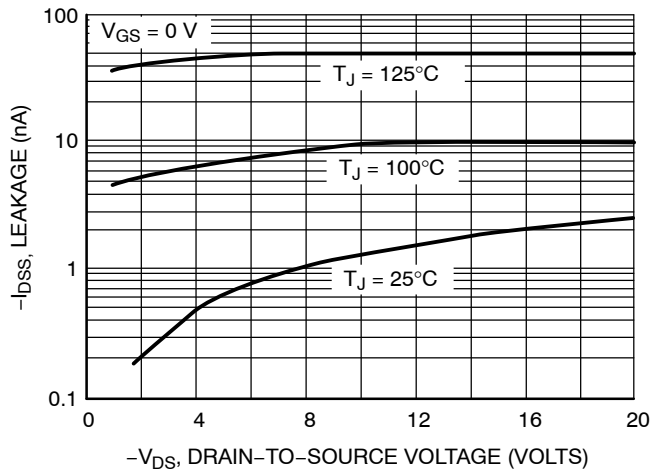


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL ELECTRICAL CHARACTERISTICS

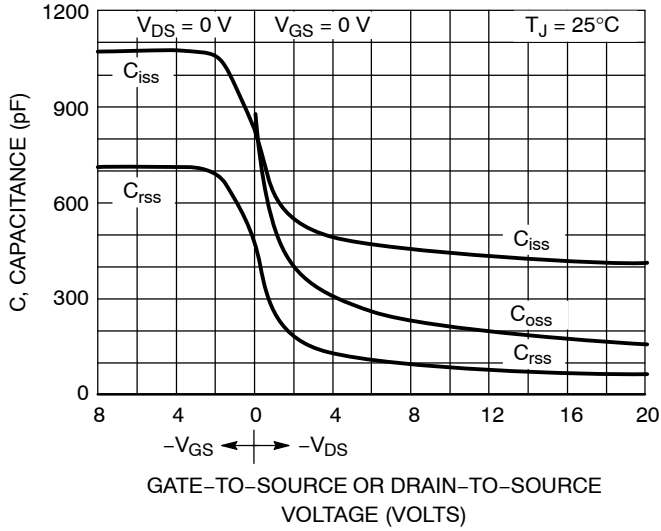


Figure 7. Capacitance Variation

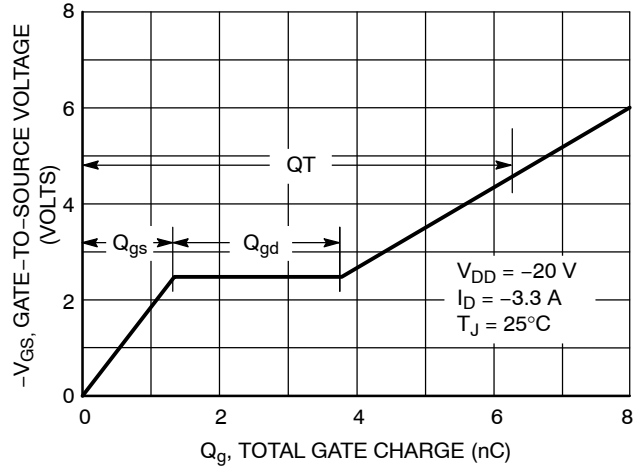


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

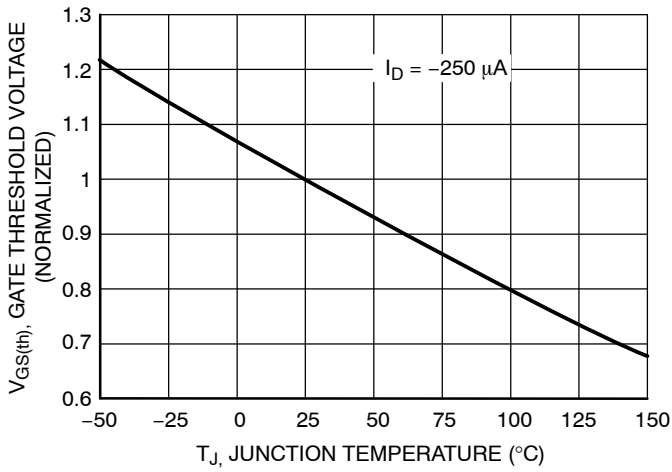


Figure 9. Gate Threshold Voltage Variation with Temperature

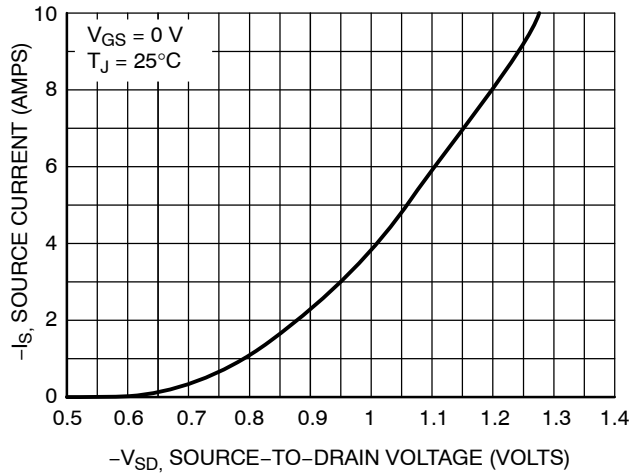


Figure 10. Diode Forward Voltage vs. Current

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## TYPICAL ELECTRICAL CHARACTERISTICS

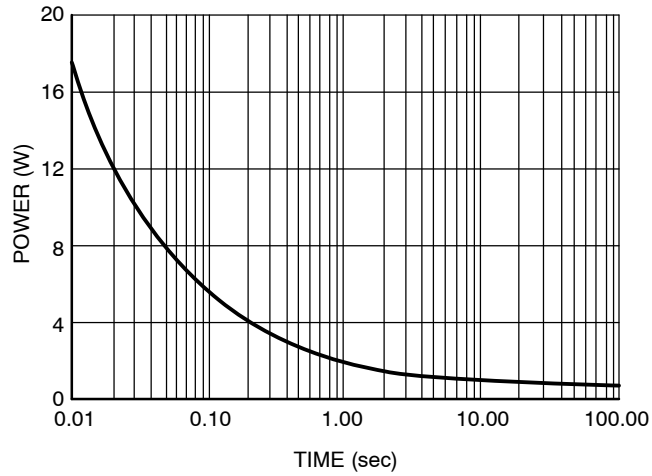


Figure 11. Single Pulse Power

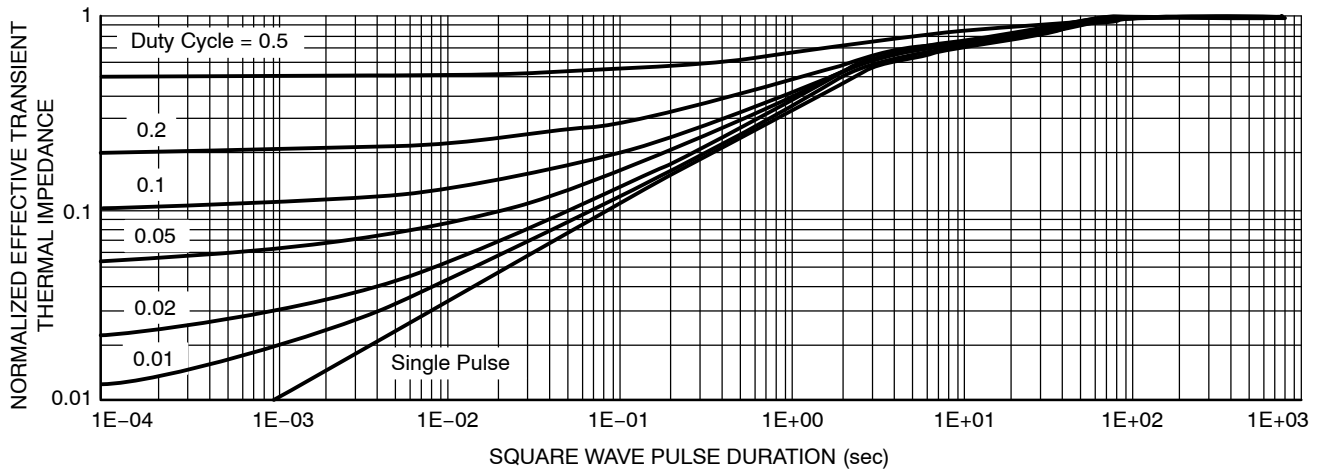
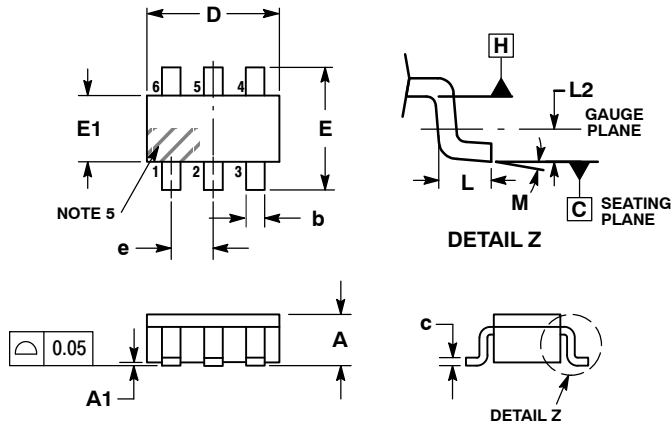


Figure 12. Normalized Thermal Transient Impedance, Junction-to-Ambient

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## PACKAGE DIMENSIONS

TSOP-6  
CASE 318G-02  
ISSUE V



NOTES:

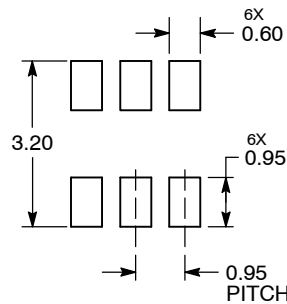
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSIONS D AND E1 ARE DETERMINED AT DATUM H.
5. PIN ONE INDICATOR MUST BE LOCATED IN THE INDICATED ZONE.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.90	1.00	1.10
A1	0.01	0.06	0.10
b	0.25	0.38	0.50
c	0.10	0.18	0.26
D	2.90	3.00	3.10
E	2.50	2.75	3.00
E1	1.30	1.50	1.70
e	0.85	0.95	1.05
L	0.20	0.40	0.60
L2	0.25 BSC		
M	0°	-	10°

STYLE 1:

1. DRAIN
2. DRAIN
3. GATE
4. SOURCE
5. DRAIN
6. DRAIN

### RECOMMENDED SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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