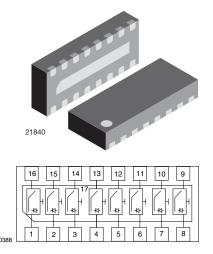
**Vishay Semiconductors** 

# 8-Channel EMI-Filter with ESD-Protection



#### **MARKING** (example only)



 $\begin{array}{l} \text{Dot} = \text{pin 1 marking} \\ \text{Y} = \text{type code (see table below)} \\ \text{XX} = \text{date code} \end{array}$ 

### **FEATURES**

- Ultra compact LLP3313-17L package
- Low package profile of 0.6 mm
- 8-channel EMI-filter
- Low leakage current
- Line resistance  $R_S = 100 \Omega$
- Typical cut off frequency  $f_{3dB} = 130 \text{ MHz}$
- ESD-protection acc. IEC 61000-4-2 ± 18 kV contact discharge
  - $\pm$  25 kV air discharge
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

ORDERING INFORMATION					
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
VEMI85AB-HGK	VEMI85AB-HGK-GS08	3000	15 000		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VEMI85AB-HGK	LLP3313-17L	9U	7.4 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	All I/O pin to pin 17; acc. IEC 61000-4-5; $t_p = 8/20 \ \mu s$ ; single shot	I <sub>PPM</sub>	4	А	
ESD immunity	Contact discharge acc. IEC61000-4-2; 10 pulses	V	± 18	kV	
	Air discharge acc. IEC61000-4-2; 10 pulses	V <sub>ESD</sub>	± 25	κv	
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	

\*\* Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902







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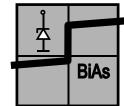
### 8-Channel EMI-Filter with ESD-Protection

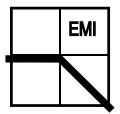


### **APPLICATION NOTE**

With the VEMI85AB-HGK 8 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behaviour is <u>Bi</u>directional and <u>Asymmetric</u> (BiAs).

L1 <sub>IN</sub>	L1 <sub>OUT</sub>
L2 <sub>IN</sub>	L2 <sub>OUT</sub>
L3 <sub>IN</sub>	L3 <sub>OUT</sub>
L4 <sub>IN</sub>	L4 <sub>OUT</sub>
L5 <sub>IN</sub>	L5 <sub>OUT</sub>
L6 <sub>IN</sub>	L6 <sub>OUT</sub>
L7 <sub>IN</sub>	L7 <sub>OUT</sub>
L8 <sub>IN</sub>	L8 <sub>OUT</sub>
Ground	





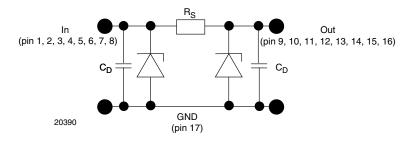
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The 8 independent EMI-filter are placed between

pin 1 and pin 16, pin 2 and pin 15, pin 3 and pin 14, pin 4 and pin 13, pin 5 and pin 12, pin 6 and pin 11, pin 7 and pin 10 and pin 8 and pin 9.

They all are connected to a common ground pin 17 on the backside of the package.

The circuit diagram of one EMI-filter-channel shows two identical Z-diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level ( $V_{BR}$ ) and the diode capacitance ( $C_D$ ). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance  $R_S$  between input and output the device works as a low pass filter. Low frequency signals ( $f < f_{3dB}$ ) pass the filter while high frequency signals ( $f > f_{3dB}$ ) will be shorted to ground through the diode capacitances  $C_D$ .



Each filter is symmetrical so that both ports can be used as input or output.



# **VEMI85AB-HGK**

## 8-Channel EMI-Filter with ESD-Protection

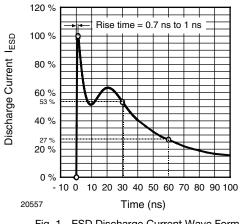
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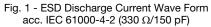
ELECTRICAL CHARACTERISTICS VEMI85AB-HGK						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of channels which can be protected	N <sub>channel</sub>	-	-	8	channel
Reverse stand off voltage	at I <sub>R</sub> = 1 μA	V <sub>RWM</sub>	5	-	-	V
Reverse current	at $V_R = V_{RWM}$	I <sub>R</sub>	-	0.25	1	μA
Reverse break down voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6	6.8	-	V
Pos. clamping voltage	at I <sub>PP</sub> = 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	-	-	7	V
	at $I_{PP} = I_{PPM} = 4$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	-	-	8	V
Neg. clamping voltage	at I <sub>PP</sub> = - 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	- 1.4	-	-	V
	at $I_{PP} = I_{PPM} = -4$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	- 1.6	-	-	V
Input capacitance	at $V_R = 0 V$ ; f = 1 MHz	C <sub>IN</sub>	-	40	45	pF
	at V <sub>R</sub> = 2.5 V; f = 1 MHz	C <sub>IN</sub>	-	24	28	pF
ESD-clamping voltage	at ± 18 kV ESD-pulse acc. IEC 61000-4-2	V <sub>CESD</sub>	-	7.5	-	V
Line resistance	Measured between input and output; $I_S = 10 \text{ mA}$	R <sub>S</sub>	90	100	110	Ω
Cut-off frequency	$V_{IN}$ = 0 V; measured in a 50 $\Omega$ system	f <sub>3dB</sub>	-	130	-	MHz

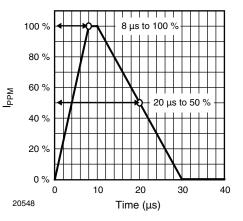
Note

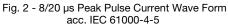
• Ratings at 25 °C, ambient temperature unless otherwise specified. All inputs (pin 1, 2, 3, 4, 5, 6, 7 and 8) to ground (pin 17)

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)









# VEMI85AB-HGK

# **Vishay Semiconductors**

8-Channel EMI-Filter with ESD-Protection



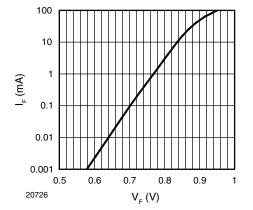


Fig. 3 - Typical Forward Current  $I_{\text{F}}$  vs. Forward Voltage  $V_{\text{F}}$ 

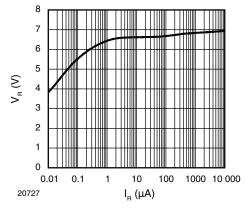
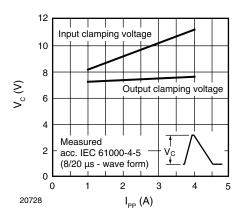
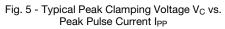


Fig. 4 - Typical Reverse Voltage  $V_{R}$  vs. Reverse Current  $I_{R}$ 





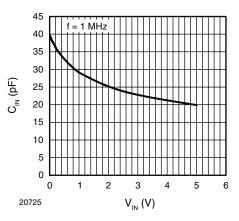


Fig. 6 - Typical Input Capacitance  $C_{\text{IN}}$  vs. Input Voltage  $V_{\text{IN}}$ 

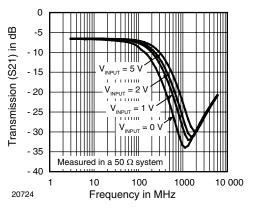


Fig. 7 - Typical Small Signal Transmission (S21) at ~ Z\_{O} = 50 ~ \Omega

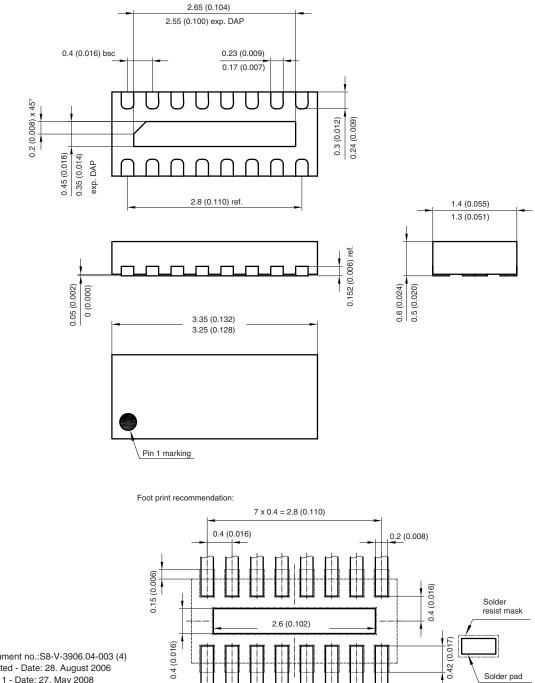




## 8-Channel EMI-Filter with **ESD-Protection**

Vishay Semiconductors

### PACKAGE DIMENSIONS in millimeters (inches): LLP3313-17L



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Solder pad



Vishay

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