



The Delphi NC20 Series, 12V input, single output, non-isolated point of load DC/DC converters are the latest offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. The NC20 series operates from a 12V nominal input, provides up to 20A of power in a vertical or horizontal mounted through-hole package and the output can be resistor- or voltage-trimmed from 0.9Vdc to 5.0Vdc. It provides a very cost effective point of load solution. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions.



- High Efficiency:
 - 91% @ 12Vin, 5V/20A out
- Size: 30.5x27.9x11.4mm
 - (1.20"×1.10"×0.45") -- Vertical
 - 30.5x27.9x12.9mm
 - (1.20"×1.10"×0.51") -- Horizontal
- Voltage and resistor-based trim
- No minimum load required
- Output voltage programmable from
 0.9Vdc to 5.0Vdc via external resistors
- Fixed frequency operation
- Input UVLO, output OCP, SCP
- Power good output signal
- Remote ON/OFF (default: positive)
- ISO 9001, TL 9000, ISO 14001, QS 9000, OHSAS 18001 certified manufacturing facility
- UL/cUL 60950-1 (US & Canada)
 Recognized

OPTIONS

- Vertical or horizontal mounted
- Negative On/Off logic

APPLICATIONS

- DataCom
- Distributed power architectures
- Servers and workstations
- LAN / WAN applications
- Data processing applications



TECHNICAL SPECIFICATIONS

(Ambient Temperature=25°C, minimum airflow=200LFM, V_{in} =12Vdc, nominal Vout unless otherwise specified.)

PARAMETER	NOTES and CONDITIONS	NC12S0A0V/H20			
		Min.	Тур.	Max.	Units
ABSOLUTE MAXIMUM RATINGS					
Input Voltage				14	Vdc
Operating Temperature (Vertical)	With appropriate air flow and derating, see Fig. 33	0		130	°C
Operating Temperature (Horizontal)	With appropriate air flow and derating, see Fig. 39	-40		125	°C
Storage Temperature		-40		125	°C
Input/Output Isolation Voltage	Non-isolated		NA		V
INPUT CHARACTERISTICS					
Operating Input Voltage		10.2	12	13.8	V
Input Under-Voltage Lockout					
Turn-On Voltage Threshold			9.0		V
Turn-Off Voltage Threshold			7.5		V
Lockout Hysteresis Voltage			1.5		V
Maximum Input Current	100% Load, 10.2Vin, 5Vout			10.65	Α
No-Load Input Current	Vin=12V, Vout=0.9V		68		mA
Off Converter Input Current	Remote OFF		8		mA
Input Reflected-Ripple Current	Refer to Figure 31.		180		mA
Input Voltage Ripple Rejection	120 Hz		45		dB
OUTPUT CHARACTERISTICS					
Output Voltage Adjustment Range		0.9		5.0	V
Output Voltage Set Point	With a 1.0% trim resistors	-3.0		+3.0	%
Output Voltage Regulation					
Over Load	lo=lo,min to lo,max (excluding soldered pins)	-1.3		+1.3	%
Over Line	Vin=Vin,min to Vin,max	-0.2		+0.2	%
Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth				
Peak-to-Peak	Full Load, 100nF ceramic, 10µF tantalum			50	mV
RMS	Full Load, 100nF ceramic, 10µF tantalum			20	mV
Output Current Range		0		20	Α
Output Voltage Over-shoot at Start-up	Vin=12V, Turn ON			1	%
Output Voltage Under-shoot at Power-Off	Vin=12V, Turn OFF			100	mV
Output DC Current-Limit Inception	Hiccup mode	21			Α
Output Short-Circuit Current	Hiccup mode				
DYNAMIC CHARACTERISTICS					
Out Dynamic Load Response	12Vin, 10µF Tan & 1µF Ceramic load cap, 10A/µs				
Positive Step Change in Output Current	50% lo,max to 75% lo,max		100		mV
Negative Step Change in Output Current	75% lo,max to 50% lo,max		100		mV
Setting Time to 10% of Peak Devitation	Settling to be within regulation band (Vo +/- 3%)			200	μs
Turn-On Transient					
Start-Up Time, From On/Off Control	From Enable high to 10% of Vo			10	ms
Start-Up Time, by input power	From Vin=12V to 10% of Vo			10	ms
Minimum Output Capacitance	EX: Al-Polymer PSA series (6.3V/680μF, ESR 7 mΩ max)	680			
Maximum Output Startup Capacitive Load	Full load			11560	μF
Minimum Input Capacitance	Ex: OSCON (16V/270μF, ESR 18 mΩ max.)	270			μF
EFFICIENCY					
Vo=0.9V	Vin=12V, Io=20A		75		%
Vo=1.2V	Vin=12V, Io=20A		78		%
Vo=1.8V	Vin=12V, Io=20A		84		%
Vo=2.5V	Vin=12V, Io=20A		86		%
Vo=3.3V	Vin=12V, Io=20A		88		%
Vo=5.0V	Vin=12V, Io=20A		91		%
FEATURE CHARACTERISTICS					
Switching Frequency	Fixed		200		KHz
ON/OFF Control	Positive logic (internally pulled high)				
Logic High	Module On (or leave the pin open)	2.4		5.5	V
Logic Low	Module Off	0		0.8	V
GENERAL SPECIFICATIONS				0.0	v
MTBF	Telcordia SR-332 Issue1 Method1 Case3 at 50°C		1.28		M hours

ELECTRICAL CHARACTERISTICS CURVES

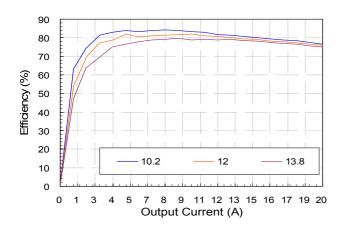


Figure 1: Converter efficiency vs. output current (0.9V output voltage)

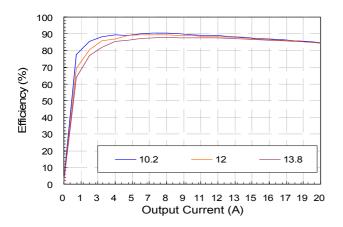


Figure 3: Converter efficiency vs. output current (1.8V output voltage)

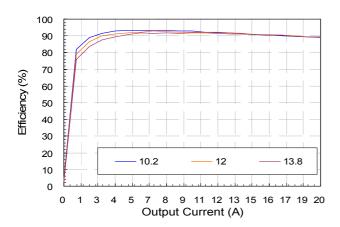


Figure 5: Converter efficiency vs. output current (3.3V output voltage)

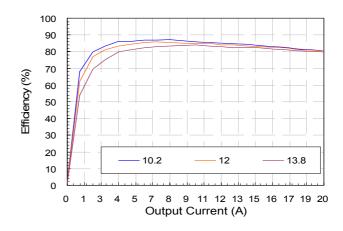


Figure 2: Converter efficiency vs. output current (1.2V output voltage)

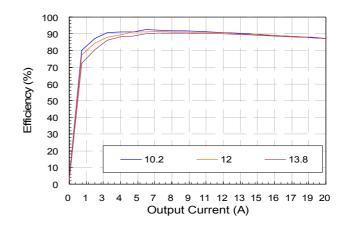


Figure 4: Converter efficiency vs. output current (2.5V output voltage)

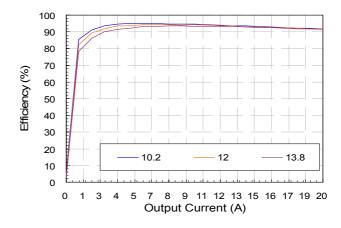


Figure 6: Converter efficiency vs. output current (5.0V output voltage)

ELECTRICAL CHARACTERISTICS CURVES (CON.)

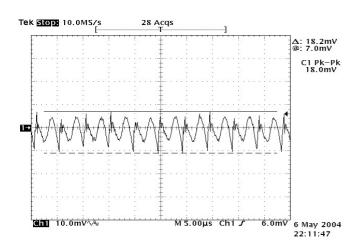


Figure 7: Output ripple & noise at 12Vin, 0.9V/20A out

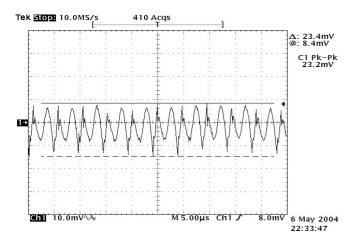


Figure 9: Output ripple & noise at 12Vin, 1.8V/20A out

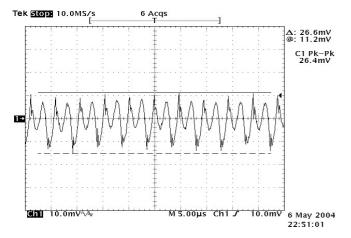


Figure 11: Output ripple & noise at 12Vin, 3.3V/20A out

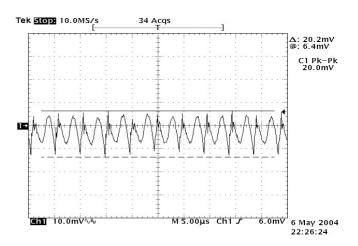


Figure 8: Output ripple & noise at 12Vin, 1.2V/20A out

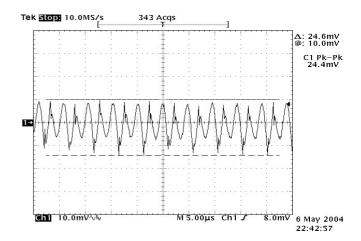


Figure 10: Output ripple & noise at 12Vin, 2.5V/20A out

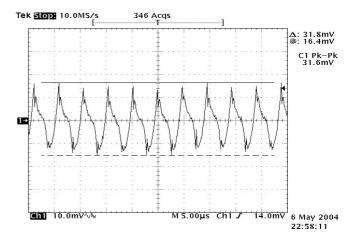


Figure 12: Output ripple & noise at 12Vin, 5.0V/20A out

ELECTRICAL CHARACTERISTICS CURVES (CON.)

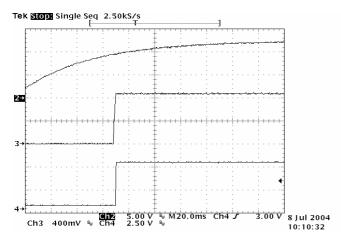


Figure 13: Turn on delay time at 12Vin, 0.9V/20A out Ch2:Vin Ch3:Vout Ch4:PWRGD

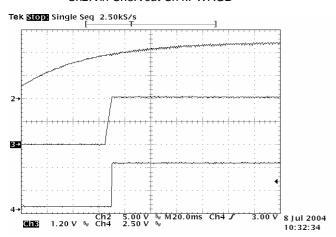


Figure 15: Turn on delay time at 12Vin, 2.5V/20A out Ch2:Vin Ch3:Vout Ch4:PWRGD

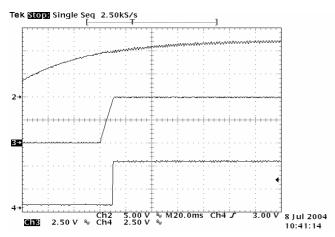


Figure 17: Turn on delay time at 12Vin, 5.0V/20A out Ch2:Vin Ch3:Vout Ch4:PWRGD

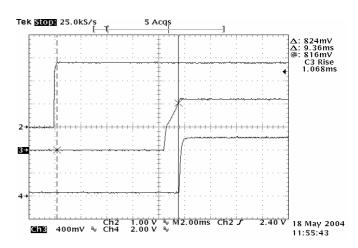


Figure 14: Turn on delay time Remote On/Off, 0.9V/20A out Ch2:ENABLE Ch3:Vout Ch4:PWRGD

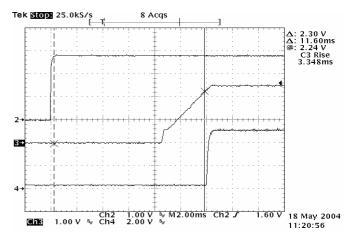


Figure 16: Turn on delay time at Remote On/Off, 2.5V/20A out Ch2: ENABLE Ch3: Vout Ch4:PWRGD

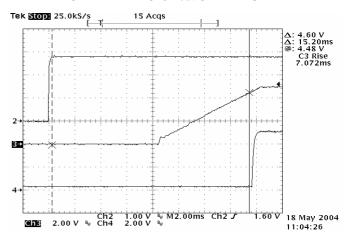


Figure 18: Turn on delay time at Remote On/Off, 5.0V/20A out Ch2: ENABLE Ch3:Vout Ch4:PWRGD

ELECTRICAL CHARACTERISTICS CURVES (CON.)

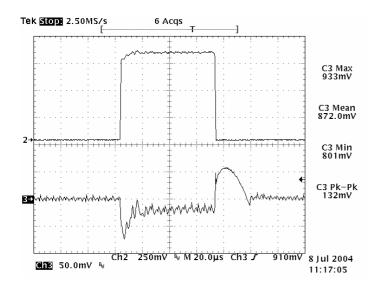


Figure 19: Typical transient response to step load change at 10A/μS from 50% to 75% and 75% to 50% of lo_max at 12 Vin, 0.9 V out

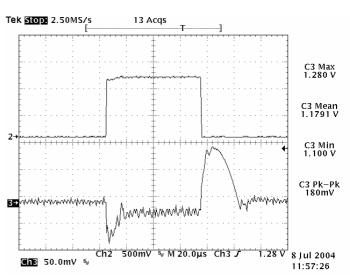


Figure 20: Typical transient response to step load change at 10A/μS from 50% to 75% and 75% to 50% of lo_max at 12Vin, 1.2V out

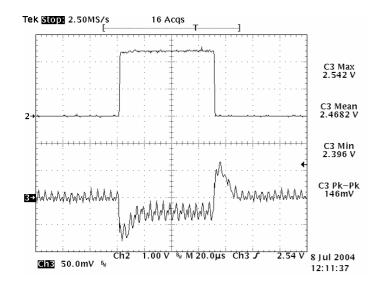


Figure 21: Typical transient response to step load change at 10A/µS from 50% to 75% and 75% to 50% of lo_max at 12Vin, 2.5V out

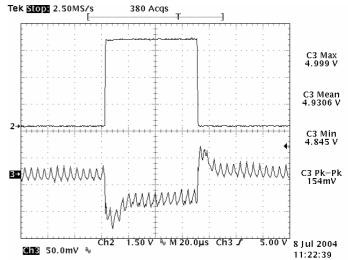


Figure 22: Typical transient response to step load change at 10A/µS from 50% to 75% and 75% to 50% of lo_max at 12Vin, 5.0V out

DESIGN CONSIDERATIONS

The NC20A is a single phase and voltage mode controlled Buck topology. Block diagram of the converter is shown in Figure 23. The output can be trimmed in the range of 0.9Vdc to 5.0Vdc by a resistor from Trim pin to Ground.

The converter can be turned ON/OFF by remote control. Positive on/off (ENABLE pin) logic implies that the converter DC output is enabled when this signal is driven high (greater than 2.4V) or floating and disabled when the signal is driven low (below 0.8V). Negative on/off logic is optional and could also be ordered.

The converter provides an open collector signal called Power Good. The power good signal is pulled low when output is not within ±10% of Vout or Enable is OFF.

The converter can protect itself by entering hiccup mode against over current and short circuit condition. Also, the converter will shut down when an over voltage protection is detected.

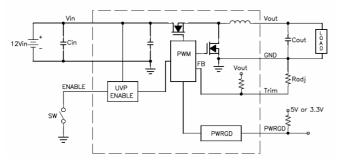


Figure 23: Block Diagram

Safety Considerations

It is recommended that the user to provide a very fast-acting type fuse in the input line for safety. The output voltage set-point and the output current in the application could define the current rating of the fuse.

FEATURES DESCRIPTIONS

ENABLE (On/Off)

The ENABLE (on/off) input allows external circuitry to put the NC converter into a low power dissipation (sleep) mode. Positive (active-high) ENABLE is available as standard.

Positive ENABLE (active-high) units of the NC series are turned on if the ENABLE pin is high or floating. Pulling the pin low will turn off the unit. With the active high function, the output is guaranteed to turn on if the ENABLE pin is driven above 2.4V. The output will turn off if the ENABLE pin voltage is pulled below .8V.

The ENABLE input can be driven in a variety of ways as shown in Figures 24, 25 and 26. If the ENABLE signal comes from the primary side of the circuit, the ENABLE can be driven through either a bipolar signal transistor (Fig. 24) or a logic gate (Fig. 25). If the enable signal comes from the secondary side, then an opto-coupler or other isolation devices must be used to bring the signal across the voltage isolation (please see Figure 26).

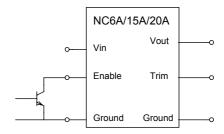


Figure 24: Enable Input drive circuit for NC series

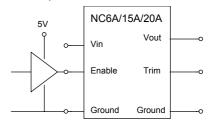


Figure 25: Enable input drive circuit using logic gate.

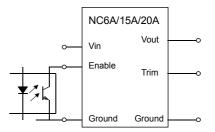


Figure 26: Enable input drive circuit example with isolation.

FEATURES DESCRIPTIONS

Input Under-Voltage Lockout

The input under-voltage lockout prevents the converter from being damaged while operating when the input voltage is too low. The lockout occurs between 7.0V to 8.0V.

Over-Current and Short-Circuit Protection

The NC series modules have non-latching over-current and short-circuit protection circuitry. When over current condition occurs, the module goes into the non-latching hiccup mode. When the over-current condition is removed, the module will resume normal operation.

An over current condition is detected by measuring the voltage drop across the high-side MOSFET. The voltage drop across the MOSFET is also a function of the MOSFET's Rds(on). Rds(on) is affected by temperature, therefore ambient temperature will affect the current limit inception point. Please see the electrical characteristics for details of the OCP function.

The detection of the Rds(on) of the high side MOSFET also acts as an over temperature protection since high temperature will cause the Rds(on) of the MOSFET to increase, eventually triggering over-current protection.

Output Voltage Programming

The output voltage of the NC series is trimmable by connecting an external resistor between the trim pin and output ground as shown Figure 27 and the typical trim resistor values are shown in Figure 28. The output can also be set by an external voltage connected to trim pin as shown in Figure 29.

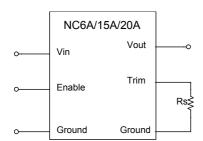


Figure 27: Trimming Output Voltage

The NC15A/20A module has a trim range of 0.9V to 5.0V. The trim resistor equation for the NC6A/NC15A/NC20A is :

$$Rs(\Omega) = \frac{1170}{Vout - 0.9}$$

Vout is the output voltage setpoint Rs is the resistance between Trim and Ground Rs values should not be less than 280Ω

Output Voltage	Rs (Ω)
+0.9 V	OPEN
+1.2 V	3.92K
+1.5 V	1.96K
+1.8 V	1.3K
+2.5 V	732
+3.3 V	487
+5.0 V	287

Figure 28: Typical trim resistor values

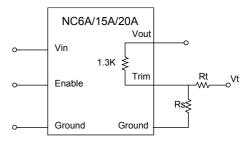


Figure 29: Output voltage trim with voltage source

To use voltage trim, the trim equation for the NC6A/NC15A/ NC20A is (please refer to Fig. 29) :

$$Rt(k\Omega) = \frac{Rs(1.3Vt - 1.17)}{1.17 - Rs(Vout - 0.9)}$$

Vout is the desired output voltage Vt is the external trim voltage Rs is the resistance between Trim and Ground (in $K\Omega$) Rt is the resistor to be defined with the trim voltage (in $K\Omega$)

Below is an example about using this voltage trim equation :

Example:

If Vt = 1.25V, desired Vout = 2.5V and Rs = $0.715K\Omega$

$$Rt(K\Omega) = \frac{Rs(1.3Vt - 1.17)}{1.17 - Rs(Vout - 0.9)} = 12.51K\Omega$$

FEATURES DESCRIPTIONS

Power Good

The converter provides an open collector signal called Power Good. This output pin uses positive logic and is open collector. This power good output is able to sink 5mA and set high when the output is within $\pm 10\%$ of output set point.

The power good signal is pulled low when output is not within ±10% of Vout or Enable is OFF.

Current Sink Capability

The NC series converters are able to sink current as well as function as a current source. It is able to sink the full output current at any output voltage up to and including 2.5V. This feature allows the NC series fit into any voltage termination application.

Voltage Margining Adjustment

Output voltage margin adjusting can be implemented in the NC modules by connecting a resistor, Rmargin-up, from the Trim pin to the Ground for margining up the output voltage. Also, the output voltage can be adjusted lower by connecting a resistor, Rmargin-down, from the Trim pin to the voltage source Vt. Figure 30 shows the circuit configuration for output voltage margining adjustment.

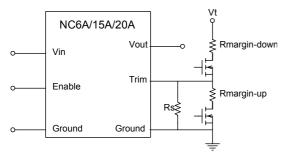


Figure 30: Circuit configuration for output voltage margining

Paralleling

NC06/NC15/NC20 converters do not have built-in current sharing (paralleling) ability. Hence, paralleling of multiple NC06/NC15/NC20 converters is not recommended.

Output Capacitance

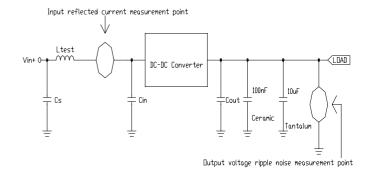
There is no output capacitor on the NC series modules. Hence, an external output capacitor is required for stable operation. For NC20 modules, an external 6.3V/680µF low ESR capacitor (for example, OSCON Al-Polymer PSA series) is required for stable operation.

It is important to places these low ESR capacitors as close to the load as possible in order to get improved dynamic response and better voltage regulation, especially when the load current is large. Several of these low ESR capacitors could be used together to further lower the ESR.

Please refer to individual datasheet for the maximum allowed start-up load capacitance for each NC series as it is varied between series.

Reflected Ripple Current and Output Ripple and Noise Measurement

The measurement set-up outlined in Figure 31 has been used for both input reflected/ terminal ripple current and output voltage ripple and noise measurements on NC series converters.



Cs=270µF*1, Ltest=1.4µH, Cin=270µF*1, Cout=680µF *1

Figure 31: Input reflected ripple/ capacitor ripple current and output voltage ripple and noise measurement setup for NC20

THERMAL CONSIDERATION

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

Thermal Testing Setup

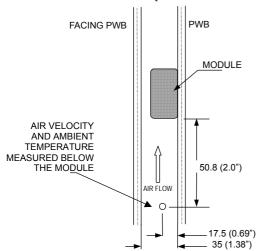
Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The height of this fan duct is constantly kept at 25.4mm (1").

Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.

THERMAL CURVES (NC12S0A0V20)



Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 32: Wind tunnel test setup

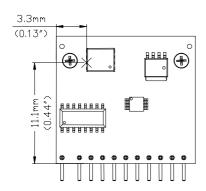


Figure 33: Temperature measurement location
* The allowed maximum hot spot temperature is defined at 130

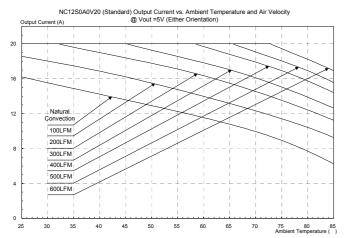


Figure 34: Output current vs. ambient temperature and air vellaty 100/626-2014 (Eith2002614) (Batton)

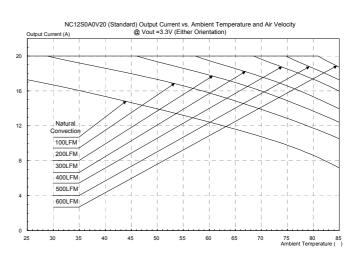


Figure 35: Output current vs. ambient temperature and air velocity @ Vout=3.3V(Either Orientation)

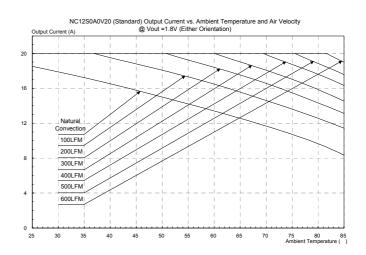


Figure 36: Output current vs. ambient temperature and air velocity @ Vout=1.8V(Either Orientation)

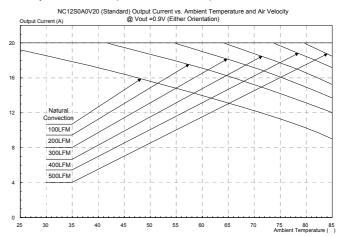
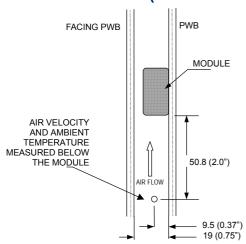


Figure 37: Output current vs. ambient temperature and air velocity @ Vout=0.9V(Either Orientation)

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THERMAL CURVES (NC12S0A0H20)



Note: Wind Tunnel Test Setup Figure Dimensions are in

millimeters and (Inches)

Figure 38: Wind tunnel test setup

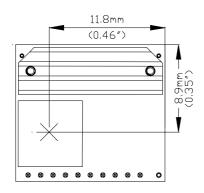


Figure 39: Temperature measurement location

* The allowed maximum hot spot temperature is defined at 125

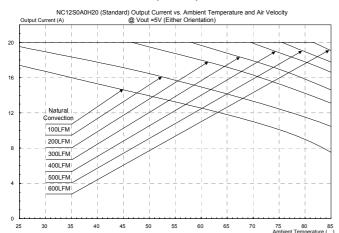


Figure 40: Output current vs. ambient temperature and air velocity @Vout=5V(Either Orientation)



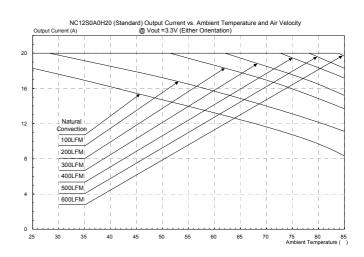


Figure 41: Output current vs. ambient temperature and air velocity @ Vout=3.3V(Either Orientation)

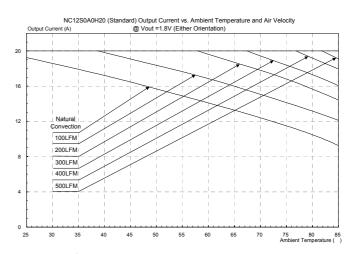


Figure 42: Output current vs. ambient temperature and air velocity @ Vout=1.8V(Either Orientation)

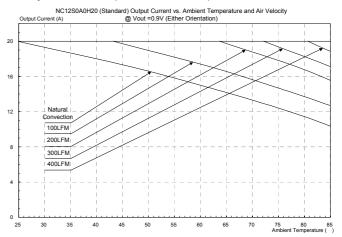
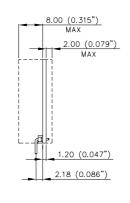
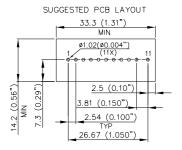


Figure 43: Output current vs. ambient temperature and air velocity @ Vout=0.9V(Either Orientation)

MECHANICAL DRAWING

VERTICAL

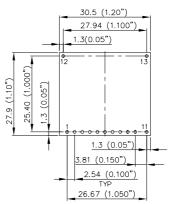


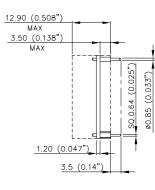


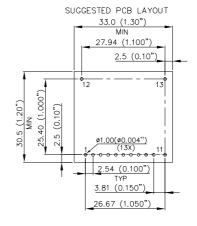
PIN ASSIGNMENT

NOTES:
DIMENSIONS ARE IN MILLIMETERS AND (INCHS)
TOLERANCE: X.X mm±0.5 mm(X.XX in.±0.02 in.)
X.XX mm±0.25 mm(X.XXX in.±0.010 in.)

HORIZONTAL







PIN ASSIGNMENT						
PIN#	FUNCTION					
1	Vout					
2	Vout					
3	Vout					
4	Trim					
5	Enable					
6	Power Good					
7	Ground					
8	Ground					
9	Reserved					
10	Vin					
11	Vin					
12	Mechanical Support					
13	Mechanical Support					

PART NUMBERING SYSTEM

NC	12	S	0A0	V	20	Р	N	F	Α	
Product	Input	Number of	Output	Mounting	Output	ON/OFF	Pin		Ontion Code	
Series	Voltage	outputs	Voltage	Mounting	Current	Logic	Length		Option Code	
NC-	12-	S- Single	0A0-	H- Horizontal	20- 20A	P- Positive	N- 0.14"	F- RoHS 6/6	A- Standard	
Non-isolated	10.2~13.8V	output	programmable	V- Vertical		N- Negative	R- 0.118"	(Lead Free)	Function	
Converter										

MODEL LIST

Model Name	Packaging	Input Voltage	Output Voltage	Output Current	Efficiency 12Vin @ 100% load
NC12S0A0V20PNFA	Vertical	10.2 ~ 13.8Vdc	0.9 V ~ 5.0Vdc	20A	91% (5.0V)
NC12S0A0H20PNFA	Horizontal	10.2 ~ 13.8Vdc	0.9 V ~ 5.0Vdc	20A	91% (5.0V)

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WARRANTY

Delta offers a two (2) year limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

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