

CMLD6263DO

SURFACE MOUNT SILICON
DUAL OPPISING
HIGH VOLTAGE
SCHOTTKY DIODE

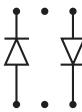


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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLD6263DO incorporates two galvanically isolated, high voltage, low VF silicon diodes with an opposing Anode/Cathode configuration, in a space saving SOT-563 surface mount package. These diodes are designed for fast switching applications requiring a low forward voltage drop.

MARKING CODE: 630



SOT-563 CASE

FEATURES:

- Dual Opposing (DO) Schottky Diodes
- High Voltage (70V)
- Low Forward Voltage
- Galvanically Isolated

MAXIMUM RATINGS: (TA=25°C)

	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V _{RRM}	70	V
Continuous Forward Current	I _F	15	mA
Peak Forward Surge Current, tp=1.0s	I _{FSM}	50	mA
Power Dissipation	P _D	250	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ _{JA}	500	°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (TA=25°C unless otherwise noted)

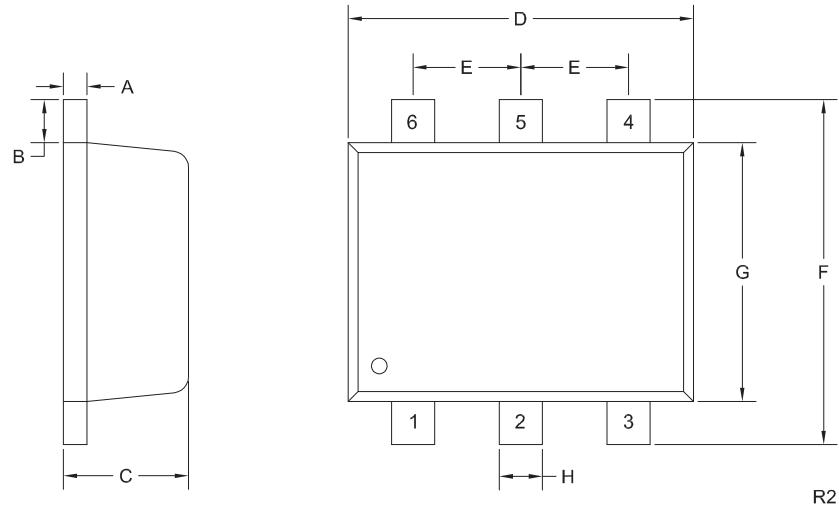
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _R	V _R =50V		98	200	nA
BV _R	I _R =10μA	70			V
V _F	I _F =1.0mA		395	410	mV
C _J	V _R =0, f=1.0MHz			2.0	pF
t _{rr}	I _R =I _F =10mA, I _{rr} =1.0mA, R _L =100Ω			5.0	ns

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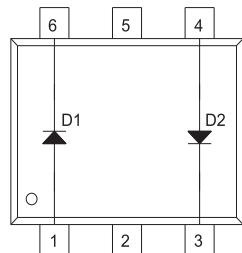
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Central
Semiconductor Corp.™

SOT-563 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



LEAD CODE:

- 1) Anode D1
- 2) NC
- 3) Cathode D2
- 4) Anode D2
- 5) NC
- 6) Cathode D1

MARKING CODE: 63O

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.0027	0.007	0.07	0.18
B	0.008		0.20	
C	0.017	0.024	0.45	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.059	0.067	1.50	1.70
G	0.043	0.051	1.10	1.30
H	0.006	0.012	0.15	0.30

SOT-563 (REV: R2)

R5 (15-June 2015)

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SERVICES

- Bonded Inventory
- Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM™)
- Bare Die Available for Hybrid Applications

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R5 (15-June 2015)

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