

**Technical Data Sheet** 

SPDT Ramses SMA 26.5GHz Latching 12Vdc Positive common Diodes Pins Terminals

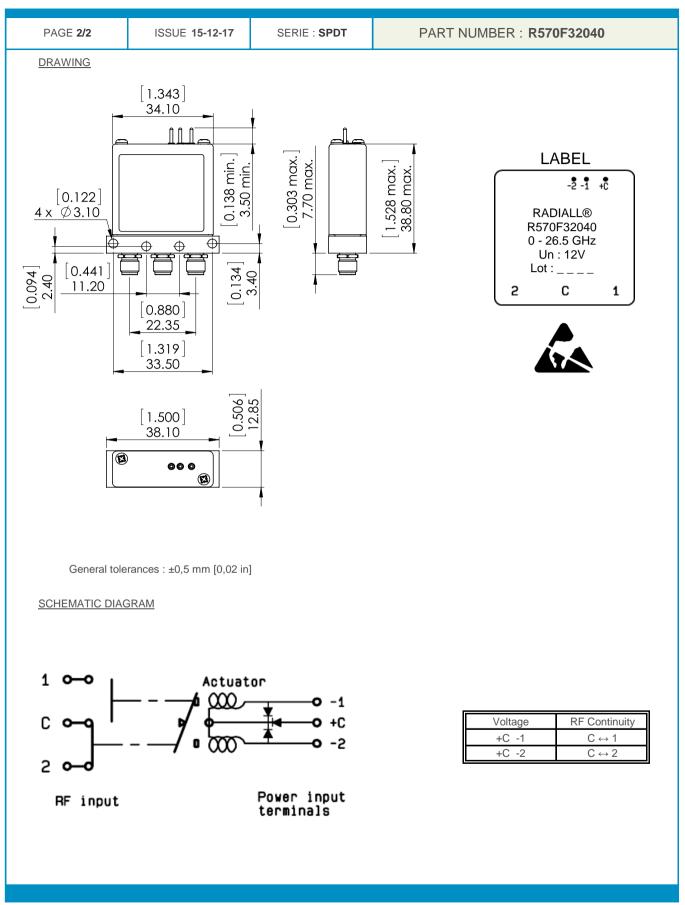
	CHARACTERIS			:	0 - 26.5 GHz	:		
	Impedance	: 50 Ohms						
	Frequency (	GHz)	DC - 3	3 - 8	8 - 12.4	12.4 - 18	18 - 26.5	
	VSWR max		1.10	1.20	1.20	1.40	1.50	
	Insertion loss		0.15 dB	0.20 dB	0.25 dB	0.35 dB	0.50 dB	
	Isolation min Average pow		80 dB 240 W	75 dB 150 W	65 dB 120 W	60 dB 100 W	55 dB 40 W	
					<u> </u>			
	CTRICAL CHA		100	:	LATCHING			
	Nominal curr				210 mA			
Actuator voltage (Vcc)				: 12V (10.2 to 13V) / POSITIVE COMMON				
	Terminals			:	solder pins	(250°C max.	/ 30 sec.)	
	Life Switching Tin Construction Weight			:	10 million c < 10 ms Splashproo < 45 g	-		
			RISTICS					
<u>ENV</u>	IRONMENTAL	<u>CHARACTI</u>						
<u>ENV</u>	<u>TRONMENTAL</u>			:	-40°C to +85	5°C		
<u>ENV</u>		mperature ra	nge		-40°C to +8 -55°C to +8			ROHS
<u>ENV</u>	Operating te	mperature ra perature ranç	nge je				(	
	Operating te Storage tem Average pow At 25° C ±10	mperature rang perature rang ver at 25°C p 1%)	nge je				(	
(*	Operating te Storage tem Average pow	mperature rang perature rang ver at 25°C p 1%)	nge je				(	ROHS COMPLIAN

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