Dual High-Voltage TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.46$  V at  $I_F = 5$  A

## FEATURES

- Trench MOS Schottky technology generation 2
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
   Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, inductrial, and automotive application.

### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

AEC-Q101 gualified

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test **Polarity:** as marked

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<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V30DM120	UNIT	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	120	V	
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub> <sup>(1)</sup>	30	A	
	I <sub>F(AV)</sub> <sup>(2)</sup>	6		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	250	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +175	°C	

#### Notes

<sup>(1)</sup> With infinite heatsink

<sup>(2)</sup> With recommended pad size, 2 oz FR4 PCB

DESIGN SUPPORT	TOOLS AVAILABLE



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	30 A		
V <sub>RRM</sub>	120 V		
I <sub>FSM</sub>	250 A		
$V_F$ at $I_F$ = 30 A ( $T_A$ = 125 °C)	0.73 V		
T <sub>J</sub> max.	175 °C		
Package	SMPD (TO-263AC)		
Circuit configuration	Single		



eSMP<sup>®</sup> Series

www.vishay.com



# V30DM120-M3, V30DM120HM3

Vishay General Semiconductor

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RoHS COMPLIANT

HALOGEN

FREE



# V30DM120-M3, V30DM120HM3

## Vishay General Semiconductor

ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.55	-	- V
	I <sub>F</sub> = 15 A			0.73	-	
	I <sub>F</sub> = 30 A			0.98	1.06	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.46	-	
	I <sub>F</sub> = 15 A			0.61	-	
	I <sub>F</sub> = 30 A			0.73	0.81	
Reverse current at rated $V_R$ per diode	V <sub>B</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	0.01	-	- mA
	v <sub>R</sub> = 90 v	T <sub>A</sub> = 125 °C		4	-	
	V <sub>B</sub> = 120 V	T <sub>A</sub> = 25 °C		-	1	IIIA
	v <sub>R</sub> = 120 v	T <sub>A</sub> = 125 °C		8	20	

### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	METER SYMBOL V30DM120		UNIT	
Typical thermal resistance	$R_{\theta JC}$	1.2	°C/W	
	R <sub>0JA</sub> (1)(2)	48		

### Notes

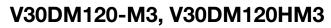
<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$  - junction-to-mount

<sup>(2)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V30DM120-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel	
V30DM120HM3/I (1)	0.55	l	2000/reel	13" diameter plastic tape and reel	

Note

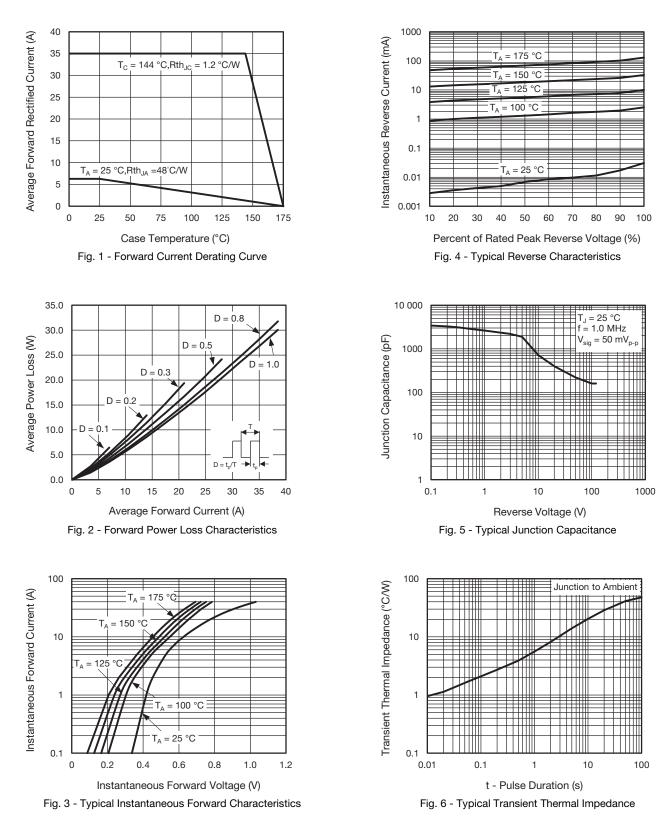
(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

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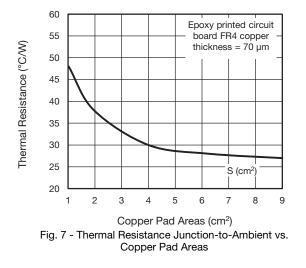
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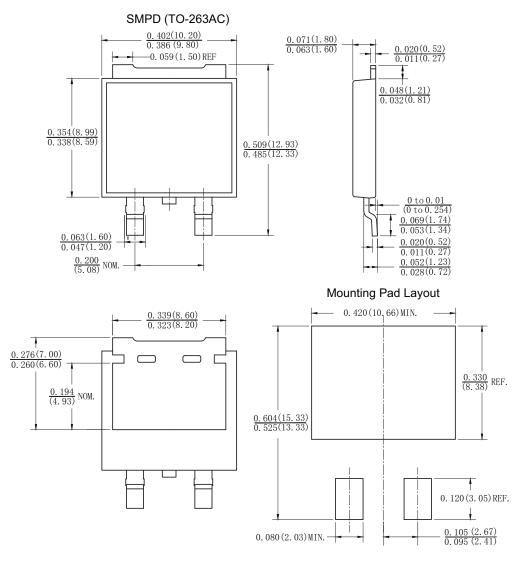


# V30DM120-M3, V30DM120HM3

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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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