



Grove - Base Shield for IOIO-OTG User Manual

Release date: 2015/9/22

Version: 1.0

Wiki: [http://www.seeedstudio.com/wiki/Grove -](http://www.seeedstudio.com/wiki/Grove_-_Base_Shield_for_IOIO-OTG)

[_Base Shield for IOIO-OTG](http://www.seeedstudio.com/wiki/Grove_-_Base_Shield_for_IOIO-OTG)

Bazaar: <http://www.seeedstudio.com/depot/Grove-Base-Shield-for-IOIOOTG-p-1613.html>

Document Revision History

Revision	Date	Author	Description
1.0	Sep 22, 2015	Jiankai.li	Create file

Contents

Document Revision History	2
1. Introduction	2
2. Interface	3
3. Resources	4

Disclaimer

For physical injuries and possessions loss caused by those reasons which are not related to product quality, such as operating without following manual guide, natural disasters or force majeure, we take no responsibility for that.

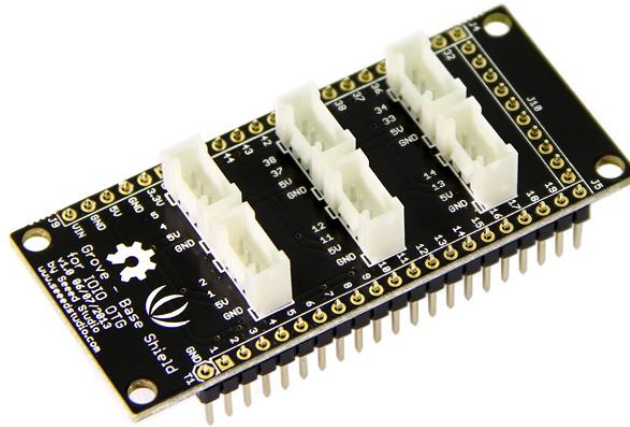
Under the supervision of Seeed Technology Inc., this manual has been compiled and published which covered the latest product description and specification. The content of this manual is subject to change without notice.

Copyright

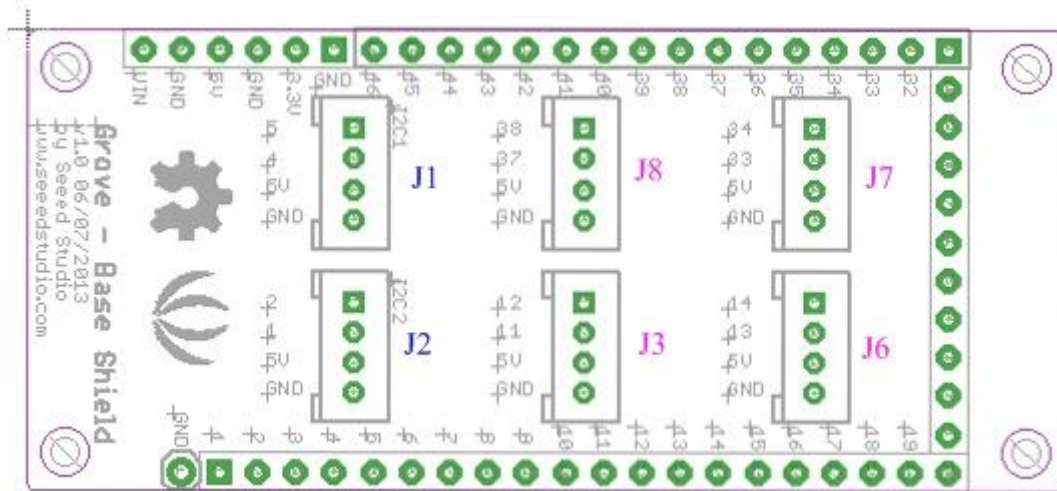
The design of this product (including software) and its accessories is under tutelage of laws. Any action to violate relevant right of our product will be penalized through law. Please consciously observe relevant local laws in the use of this product.

1. Introduction

IOIO is a board specially designed to work with your Android device. And this Grove - Base Shield for IOIO is an expansion board for IOIO to work with abundant Grove resources. There are 6 ready Grove sockets on the board covering functions like ADC and I2C. And in this way, all Grove Modules are accessible for the IOIO board. Wanna make some projects with IOIO and need some sensors or display? This Grove - Base Shield for IOIO can bring you this convenience.



2. Interface



- **J1,J2:** can be used for TWI.
- **J3,J6,J7,J8:** can be used inputs and outputs, include PWM, UART.

The pins describe mapping to the Grove - Base Shield for IOIO-OTG board as show below:

Grove Interface	IOIO Pin	A/D	I ² C	PPSi	PPSo	PIC Pin	PIC function
J2	1		DA1	Y	Y	31	SDA2/RP10/GD4/CN17/RF4
	2		CL1	Y	Y	32	SCL2/RP17/GD5/CN18/RF5
J1	4		DA0	Y	Y	43	DPLN/SDA1/RP4/GD8/CN54/RD9
	5		CL0	Y	Y	44	SCL1/RP3/GD6/CN55/RD10
J3	11			Y	Y	50	DPH/RP23/CN51/RD2
	12			Y	Y	51	RP22/GEN/CN52/RD3
J6	13			Y	Y	52	RP25/GCLK/CN13/RD4
	14			Y	Y	53	RP20/GPWR/CN14/RD5
J7	33	Y		Y	Y	50	DPH/RP23/CN51/RD2
	34	Y		Y	Y	51	RP22/GEN/CN52/RD3
J8	37	Y		Y	Y	17	PGEC2/AN6/RP6/CN24/RB6
	38	Y		Y	Y	18	PGED2/AN7/RP7/RCV/CN25/RB7

3. Resources

- [Grove - Base Shield for IOIO-OTG Eagle File](#)