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July 2002 Revised March 2004

### NC7SVU04

# TinyLogic® ULP-A Unbuffered Inverter

### **General Description**

The NC7SVU04 is a single unbuffered inverter from Fairchild's Ultra Low Power-A (ULP-A) series of TinyLogic®. ULP-A is ideal for applications that require extreme high speed, high drive and low power. This product is designed for a wide low voltage operating range (0.9V to 3.6V V<sub>CC</sub>) and applications that require more drive and speed than the TinyLogic ULP series, but still offer best in class low power operation.

The NC7SVU04 is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve high-speed operation while maintaining low CMOS power dissipation.

### **Features**

- 0.9V to 3.6V V<sub>CC</sub> supply operation
- 3.6V overvoltage tolerant I/O's at V<sub>CC</sub> from 0.9V to 3.6V
- Extremely High Speed tpD

1.5 ns typ for 2.7V to 3.6V  $V_{\rm CC}$ 

1.8 ns typ for 2.3V to 2.7V  $\rm V_{\rm CC}$ 

1.9 ns typ for 1.65V to 1.95V  $\ensuremath{\text{V}_{\text{CC}}}$ 

3.2 ns typ for 1.4V to 1.6V  $\rm V_{\rm CC}$ 

5.9 ns typ for 1.1V to 1.3V  $V_{CC}$ 

12.0 ns typ for 0.9V  $V_{\rm CC}$ 

- Power-Off high impedance inputs and outputs
- High Static Drive (I<sub>OH</sub>/I<sub>OL</sub>)

±24 mA @ 3.00V V<sub>CC</sub>

±18 mA @ 2.30V V<sub>CC</sub>

±6 mA @ 1.65V V<sub>CC</sub>

±4 mA @ 1.4V V<sub>CC</sub>

 $\pm 2$  mA @ 1.1V V<sub>CC</sub>

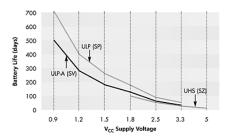
±20 μA @ 0.9V V<sub>CC</sub>

- Uses patented Quiet Series<sup>™</sup> noise/EMI reduction circuitry
- Ultra small MicroPak™ leadfree package
- Ultra low dynamic power

### **Ordering Code:**

Order Number	ber Package Product Code Number Top Mark		Package Description	Supplied As	
NC7SVU04P5X	MAA05A	VU4	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel	
NC7SVU04L6X	MAC06A	N4	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel	

### Battery Life vs. V<sub>CC</sub> Supply Voltage



TinyLogic ULP and ULP-A with up to 50% less power consumption can extend your battery life significantly.

Battery Life = (V<sub>battery</sub> \*I<sub>battery</sub>\*.9)/(P<sub>device</sub>)/24hrs/day

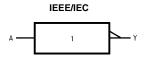
Where,  $P_{device} = (I_{CC} * V_{CC}) + (C_{PD} + C_L) * V_{CC}^2 * f$ 

Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with  $C_L=15\,\mathrm{pF}$  load

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# **Logic Symbol**



# **Pin Descriptions**

Pin Names	Description
A	Input
Y	Output
NC	No Connect

### **Function Table**

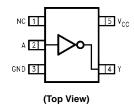
 $Y = \overline{A}$ 

Inputs	Output
Α	Y
L	Н
Н	L

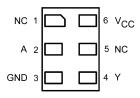
H = HIGH Logic Level L = LOW Logic Level

## **Connection Diagrams**

Pin Assignment for SC70



Pad Assignments for MicroPak



(Top Thru View)

### **Absolute Maximum Ratings**(Note 1)

# Note 1) Recommended Operating Conditions (Note 3)

Supply Voltage (V <sub>CC</sub> )	-0.5V to +4.6V	
DC Input Voltage (V <sub>IN</sub> )	-0.5V to +4.6V	
DC Output Voltage (V <sub>OUT</sub> )		
HIGH or LOW State (Note 2)	$-0.5V$ to $V_{CC}$ +0.5V	
$V_{CC} = 0V$	-0.5V to +4.6V	
DC Input Diode Current ( $I_{IK}$ ) $V_{IN}$ < 0V	±50 mA	
DC Output Diode Current (I <sub>OK</sub> )		
V <sub>OUT</sub> < 0V	−50 mA	
V> V	.50 m∆	

 $-65^{\circ}C$  to  $+150^{\circ}C$ 

Storage Temperature Range ( $T_{STG}$ )

Supply Voltage 0.9V to 3.6V Input Voltage (V<sub>IN</sub>) 0V to 3.6V

Output Voltage (V<sub>OUT</sub>)

 $\begin{aligned} & \text{V}_{\text{CC}} = \text{0.0V} & \text{0V to 3.6V} \\ & \text{HIGH or LOW State} & \text{0V to V}_{\text{CC}} \\ & \text{Output Current in I}_{\text{OH}} \text{I}_{\text{OL}} \end{aligned}$ 

 $\begin{array}{lll} {\rm V_{CC}} = 3.0 {\rm V} \; {\rm to} \; 3.6 {\rm V} & & \pm 24 \; {\rm mA} \\ {\rm V_{CC}} = 2.3 {\rm V} \; {\rm to} \; 2.7 {\rm V} & & \pm 18 \; {\rm mA} \\ {\rm V_{CC}} = 1.65 {\rm V} \; {\rm to} \; 1.95 {\rm V} & & \pm 6 \; {\rm mA} \\ \end{array}$ 

 $V_{CC} = 1.4 V \text{ to } 1.6 V$   $\pm 4 \text{ mA}$   $V_{CC} = 1.1 V \text{ to } 1.3 V$   $\pm 2 \text{ mA}$   $V_{CC} = 0.9 V$   $\pm 20 \,\mu\text{A}$ 

Free Air Operating Temperature (T<sub>A</sub>)  $-40^{\circ}\text{C}$  to +85°C

Minimum Input Edge Rate (Δt/ΔV)

 $V_{IN} = 0.8V$  to 2.0V,  $V_{CC} = 3.0V$  10 ns/V

Note 1: Absolute Maximum Ratings: are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2:  $I_{O}$  Absolute Maximum Rating must be observed.

Note 3: Unused inputs must be held HIGH or LOW. They may not float.

### **DC Electrical Characteristics**

Symbol	Parameter	V <sub>CC</sub>	T <sub>A</sub> = -	⊦25°C	T <sub>A</sub> = -40°0	C to +85°C	Units	Conditi	one
Symbol	raiametei	(V)	Min	Max	Min	Max	Units	Conditi	Ulis
V <sub>IH</sub>	HIGH Level	0.90	0.8 x V <sub>CC</sub>		0.8 x V <sub>CC</sub>				
	Input Voltage	$1.10 \le V_{CC} \le 1.30$	0.8 x V <sub>CC</sub>		0.8 x V <sub>CC</sub>				
		$1.40 \le V_{CC} \le 1.60$	0.8 x V <sub>CC</sub>		0.8 x V <sub>CC</sub>		V		
		$1.65 \le V_{CC} \le 1.95$	0.8 x V <sub>CC</sub>		0.8 x V <sub>CC</sub>		V		
		$2.30 \le V_{CC} < 2.70$	0.8 x V <sub>CC</sub>		0.8 x V <sub>CC</sub>				
		$2.70 \leq V_{CC} \leq 3.60$	0.8 x V <sub>CC</sub>		0.8 x V <sub>CC</sub>				
V <sub>IL</sub>	LOW Level	0.90		0.2 x V <sub>CC</sub>		0.2 x V <sub>CC</sub>			
	Input Voltage	$1.10 \le V_{CC} \le 1.30$		$0.2 \times V_{\rm CC}$		$0.2 \times V_{\rm CC}$			
		$1.40 \le V_{CC} \le 1.60$		$0.2 \times V_{\rm CC}$		$0.2 \times V_{\rm CC}$	V		
		$1.65 \le V_{CC} \le 1.95$		$0.2 \times V_{\rm CC}$		$0.2 \times V_{\rm CC}$	V		
		$2.30 \le V_{CC} < 2.70$		$0.2 \times V_{\rm CC}$		$0.2 \times V_{\rm CC}$			
		$2.70 \leq V_{CC} \leq 3.60$		$0.2 \times V_{\rm CC}$		$0.2 \times V_{\rm CC}$			
V <sub>OH</sub>	HIGH Level	0.90	V <sub>CC</sub> - 0.2		V <sub>CC</sub> - 0.2			$I_{OH} = -20 \mu A$	
	Output Voltage	$1.10 \le V_{CC} \le 1.30$	V <sub>CC</sub> - 0.2		V <sub>CC</sub> - 0.2				7
		$1.40 \le V_{CC} \le 1.60$	$V_{CC}-0.3$		$V_{CC} - 0.3$			$I_{OH} = -100 \mu A$ $V_{IN} = 3$	V – V
		$1.65 \le V_{CC} \le 1.95$	$V_{CC} - 0.3$		V <sub>CC</sub> - 0.3				VIN - VIH
		$2.30 \le V_{CC} < 2.70$	$V_{CC} - 0.3$		V <sub>CC</sub> - 0.3				
		$2.70 \leq V_{CC} \leq 3.60$	$V_{CC}-0.3$		$V_{CC} - 0.3$				
		$1.10 \le V_{CC} \le 1.30$	0.75 x V <sub>CC</sub>		0.75 x V <sub>CC</sub>			$I_{OH} = -2 \text{ mA}$	
		$1.40 \le V_{CC} \le 1.60$	0.75 x V <sub>CC</sub>		0.75 x V <sub>CC</sub>		V	$I_{OH} = -4 \text{ mA}$	
		$1.65 \le V_{CC} \le 1.95$	1.25		1.25			I <sub>OH</sub> = -6 mA	
		$2.30 \le V_{CC} < 2.70$	2.0		2.0			IOH - OTHER	
		$2.30 \le V_{CC} < 2.70$	1.8		1.8			I <sub>OH</sub> = -12 mA	$V_{IN} = GND$
		$2.70 \leq V_{CC} \leq 3.60$	2.2		2.2			OH - 12117	
		$2.30 \le V_{CC} < 2.70$	1.7		1.7			I <sub>OH</sub> = -18 mA	
		$2.70 \leq V_{CC} \leq 3.60$	2.4		2.4				
		$2.70 \leq V_{CC} \leq 3.60$	2.2		2.2	•		$I_{OH} = -24 \text{ mA}$	
	-								

# DC Electrical Characteristics (Continued)

Symbol	Parameter	V <sub>cc</sub>	T <sub>A</sub> = +25	5°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	
Syllibol	raiametei	(V)	Min	Max	Min	Max	Onics	Condition	113
V <sub>OL</sub>	LOW Level	0.90		0.1		0.1		$I_{OL} = 20 \mu A$	
	Output Voltage	$1.10 \le V_{CC} \le 1.30$		0.1		0.1			
		$1.40 \leq V_{CC} \leq 1.60$		0.2		0.2			$V_{IN} = V_{IL}$
		$1.65 \leq V_{CC} \leq 1.95$		0.2		0.2		$I_{OL} = 100 \ \mu A$	VIN = VIL
		$2.30 \leq V_{CC} < 2.70$		0.2		0.2			
		$2.70 \leq V_{CC} \leq 3.60$		0.2		0.2			
		$1.10 \le V_{CC} \le 1.30$	0.:	25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	V	I <sub>OL</sub> = 2 mA	
		$1.40 \le V_{CC} \le 1.60$	0.:	25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	V	I <sub>OL</sub> = 4 mA	
		$1.65 \le V_{CC} \le 1.95$		0.3		0.3		I <sub>OL</sub> = 6 mA	
		$2.30 \le V_{CC} < 2.70$		0.4		0.4		I <sub>OI</sub> = 12 mA	
		$2.70 \leq V_{CC} \leq 3.60$		0.4		0.4		10L - 12 IIIA	
		$2.30 \le V_{CC} < 2.70$		0.6		0.6		I <sub>OI</sub> = 18 mA	$V_{IN} = V_{CC}$
		$2.70 \leq V_{CC} \leq 3.60$		0.4		0.4		10L = 10 IIIA	
		$2.70 \leq V_{CC} \leq 3.60$		0.55		0.55		I <sub>OL</sub> = 24 mA	
I <sub>IN</sub>	Input Leakage Current	0.90 to 3.60		±0.1		±0.5	μΑ	$0 \le V_I \le 3.6V$	
I <sub>CC</sub>	Quiescent Supply Current	0.90 to 3.60		0.9		0.9	μА	$V_I = V_{CC}$ or GND	
		0.90 to 3.60				±0.9	μΑ	$V_{CC} \le V_1 \le 3.6V$	

### **AC Electrical Characteristics**

Symbol	Parameter	V <sub>CC</sub>	T <sub>A</sub> = +25°C		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	Figure	
Syllibol	rarameter	(V)	Min	Тур	Max	Min	Max	Units	Conditions	Number
t <sub>PHL</sub>	Propagation Delay	0.90		12					$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	
t <sub>PLH</sub>		$1.10 \le V_{CC} \le 1.30$	2.0	5.9	10.0	1.0	14.4		$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$	
		$1.40 \leq V_{CC} \leq 1.60$	1.0	3.2	6.1	0.9	7.0	ns		Figures
		$1.65 \le V_{CC} \le 1.95$	1.0	1.9	5.2	0.7	6.2	115	C <sub>L</sub> = 30 pF	1, 2
		$2.30 \leq V_{CC} < 2.70$	8.0	1.8	3.7	0.6	4.4		$R_L = 1 k\Omega$	
		$2.70 \leq V_{CC} \leq 3.60$	0.7	1.5	3.3	0.5	3.8			
C <sub>IN</sub>	Input Capacitance	0		2.0				pF		
C <sub>OUT</sub>	Output Capacitance	0		4.5				pF		
C <sub>PD</sub>	Power Dissipation Capacitance	0.90 to 3.60		10				pF	V <sub>I</sub> = 0V or V <sub>CC</sub> f = 10 MHz	

# **AC Loading and Waveforms**

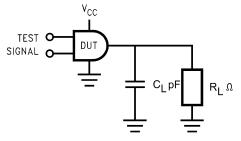


FIGURE 1. AC Test Circuit

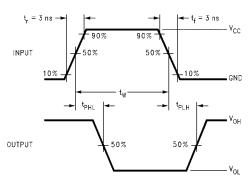


FIGURE 2. Waveform for Inverting and Non-Inverting Functions

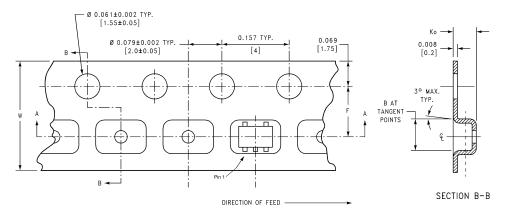
	Symbol	V <sub>CC</sub>									
Gymbol	$3.3V \pm 0.3V$	$\textbf{2.5V} \pm \textbf{0.2V}$	$1.8V \pm 0.15V$	1.5V ± 0.10V	$1.2V \pm 0.10V$	0.9V					
	V <sub>mi</sub>	1.5V	V <sub>CC</sub> /2								
Г	V <sub>mo</sub>	1.5V	V <sub>CC</sub> /2								

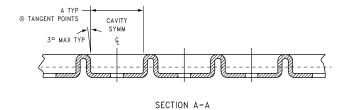
# **Tape and Reel Specification**

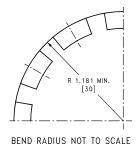
### TAPE FORMAT for SC70

Package	Tape	Number	Cavity	Cover Tape
Designator	Section	Cavities	Status	Status
	Leader (Start End)	125 (typ)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

### TAPE DIMENSIONS inches (millimeters)







Pack	age		Та	pe	-	Number	Cavity	Cover Tape
Desig			Sec	tion		Cavities	Status	Status
			Leader (S			125 (typ)	Empty	Sealed
L6	X		Car			5000	Filled	Sealed
			Trailer (H	lub End)		75 (typ)	Empty	Sealed
8.00 +0.0.	2.00	4.00	5° MAX	4.00	01.50 ±0.05	B ■ B ■ B ■ B ■ B ■ B ■ B ■ B ■ B ■ B ■	3.50±0.05	SECTION B-B SCALE:10X
A .	IENSION	IS inches	(millimete	rs)		TAPE SLOT	B c	W <sub>1</sub>
ape ize	A 7.0	B 0.059	C 0.512	D 0.795	N 2.165		TAIL X ALE: 3X  W2  0.567	₩3 W1 + 0.078/-0.0
mm	(177.8)	(1.50)	(13.00)	(20.20)	(55.00)	(8.40 + 1.50/-0.00)	(14.40)	(W1 + 2.00/–1.0

# Physical Dimensions inches (millimeters) unless otherwise noted 0.65 0.65 0.200±0.020 0.200±0.020 0.25 0.25 0.25 0.25 0.25 0.25 0.25 0.25 0.30 0.45 0.425 NOMINAL

### NOTES:

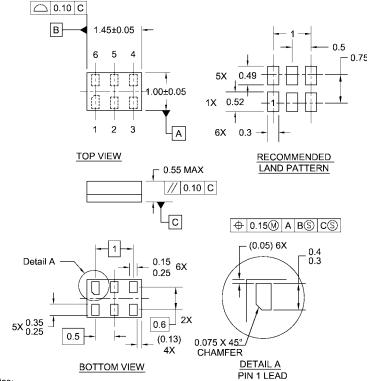
- A. CONFORMS TO EIAJ REGISTERED OUTLINE DRAWING SC88A.
- B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.
- C. DIMENSIONS ARE IN MILLIMETERS.

MAA05ARevC

DETAIL A

5-Lead SC70, EIAJ SC-88a, 1.25mm Wide Package Number MAA05A

### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



- Notes:
- 1. JEDEC PACKAGE REGISTRATION IS ANTICIPATED 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06ARevB

6-Lead MicroPak, 1.0mm Wide Package Number MAC06A

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