LIQUID CRYSTAL DISPLAY MODULE

MODEL: NMTG-F24160BFWHSEB-15

Customer's No.:

Acceptance

Microtips Technology Inc.
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Approved and Checked by

Approved by	Checked by		Made by
微 端 2006/5/24 陳宏誠	微 端 2006/5/24 連俊傑	微 端 2006/5/24 連俊傑	微 端 2006/5/24 郭美伶

Microtips Technology Inc.

Revise Records

Rev.	Date	Contents	Written	Approved
A	2006/5/24	Initial Release	Kenix Kuo	Danny Lian

Special Notes

Note1.	The LCD module is compliant with RoHS
Note2.	
Note3.	
Note4.	
Note5.	

Contents

The Microtips Customized LCD module, model: NMTG-F24160BFWHSEB-15 is compliant with RoHS

1. General Specifications

Operating Temperature	:	Min. -20°C ～ Max. 70°C
Storage Temperature	:	Min. -30°C ～ Max. 80°C
Dot Pixels	:	240 (W) x 160 (H) dots
Dot Size	:	0.23 (W) x 0.23 (H) mm
Dot Pitch	:	0.24 (W) x 0.24 (H) mm
Viewing Area	:	61.6 (W) x 42.5 (H) mm
Outline Dimensions	:	74.6* (W) x 54.1 (H) x 4.0 max. (D) mm
		* Not concerning EL pin and FPCB
Weight	:	N/A
LCD Type	:	FSTN/ Positive mode/ Transflective
Viewing Direction	:	6:00
Data Transfer	:	4-bit parallel data transfer
Backlight	:	EL backlight (Blue-Green)
Drawings	:	As attached drawings

2. Electrical Specifications

2.1 Absolute Maximum Ratings

V_{SS} = 0V

Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage (Logic)	V_{DD} - V_{SS}	–	-0.3	7.0	V
Supply Voltage (LCD Drive)	V_0 - V_{SS}	–	-0.3	30.0	V
Input Voltage	V_I	–	- 0.3	V_{DD} + 0.3	V

2.2 DC Characteristics

Ta = 25°C, V_{SS} = 0V

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units	Note
Supply Voltage (Logic)	V_{DD} - V_{SS}	–	2.5	3.3	5.5	V	
Supply Voltage (LCD Drive)	V_0 -V_{SS}	–	15.0	–	30.0	V	
	V_O -V_{SS}	Shown in 3.1				V	
High Level Input Voltage	V_{IH}	–	0.8V_{DD}	–	–	V	
Low Level Input Voltage	V_{IL}	–	–	–	0.2V_{DD}	V	
High Level Output Voltage	V_{OH}	I_{OH} = -0.4mA	V_{DD}-0.4	–	–	V	
Low Level Output Voltage	V_{OH}	I_{OH} = +0.4mA	–	–	0.4	V	
Standby Current	I_{STB}	–	–	–	50	μA	1
Supply Current (1) Non-selection	I_{DD1}	–	–	–	2.0	mA	2
Supply Current (2) Selection	I_{DD2}	–	–	–	8.0	mA	3
Supply Current (3)	I_0				1.0	mA	4
Frame	f_F	Duty = 50%	65	70	75	Hz	

NOTES:

1. V_{DD} = +5.0 V, V0 = +30.0 V, V_I = V_{SS}.
2. V_{DD} = +5.0 V, V0 = +30.0 V, f_{XCK} = 8 MHz, no-load, EI = V_{DD}. The input data is turned over by data taking clock (4-bit parallel input mode).
3. V_{DD} = +5.0 V, V0 = +30.0 V, f_{XCK} = 8 MHz, no-load, EI = V_{SS}. The input data is turned over by data taking clock (4-bit parallel input mode).
4. V_{DD} = +5.0 V, V0 = +30.0 V, f_{XCK} = 8MHz, f_{LP} = 19.2 kHz, f_{FR} = 80 Hz, no-load. The input data is turned over by data taking clock (4-bit parallel input mode).

2.3 Signal Timing Diagram

Segment Mode (V_{SS}=0V, V_{DD}= 4.5~5.5V, V0=15 to 30 V, and Ta=-20 to +85°C)

Parameter	Symbol	Min.	Max.	Units	Condition
Shift clock period*	t_{WLP}	71	--	ns	tr, tf <=10ns
Shift clock "H" pulse width	t_{WCKH}	23	--	ns	
Shift clock "L" pulse width	t_{WCKL}	23	--	ns	
Data setup time	t_{DS}	10	--	ns	
Data hold time	t_{DH}	20	--	ns	
Latch pulse "H" pulse width	t_{WLPH}	23	--	ns	
Latch pulse rise to shift clock rise time	t_{LS}	51	--	ns	
Latch pulse fall to shift clock fall time	t_{LH}	51	--	ns	
Input signal rise time**	tr	--	50	ns	
Input signal fall time**	tf	--	50	ns	

Note :

*Take the cascade connection into consideration.

**(t_{WLP} -t_{WCKH} -t_{WCKL})/2 is the maximum in the case of high speed operation.

Segment Mode (V_{SS}=0V, V_{DD}= 2.5~4.5V, V0=15 to 30 V, and Ta=-20 to +85°C)

Parameter	Symbol	Min.	Max.	Units	Condition
Shift clock period*	t_{WLP}	125	--	ns	tr, tf <=11ns
Shift clock "H" pulse width	t_{WCKH}	51	--	ns	
Shift clock "L" pulse width	t_{WCKL}	51	--	ns	
Data setup time	t_{DS}	30	--	ns	
Data hold time	t_{DH}	40	--	ns	
Latch pulse "H" pulse width	t_{WLPH}	51	--	ns	
Latch pulse rise to shift clock rise time	t_{LS}	51	--	ns	
Latch pulse fall to shift clock fall time	t_{LH}	51	--	ns	
Input signal rise time**	tr	--	50	ns	
Input signal fall time**	tf	--	50	ns	

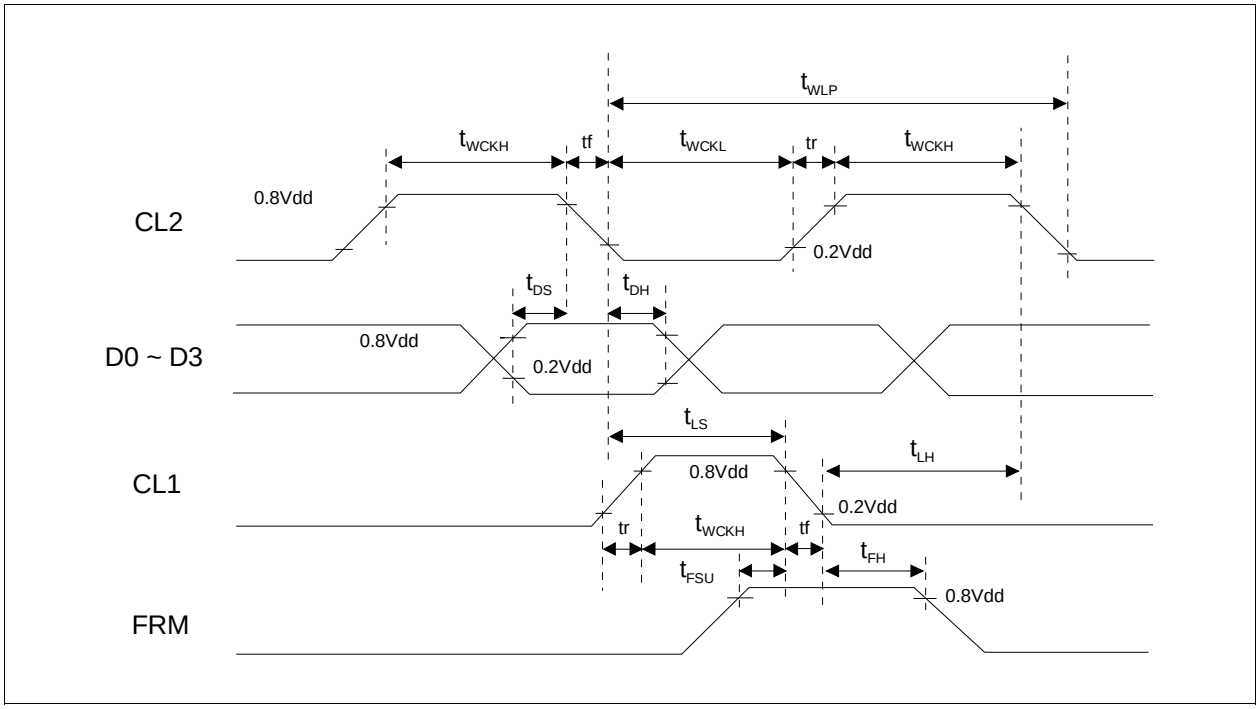
Note :

*Take the cascade connection into consideration.

**(t_{WLP} -t_{WCKH} -t_{WCKL})/2 is the maximum in the case of high speed operation.

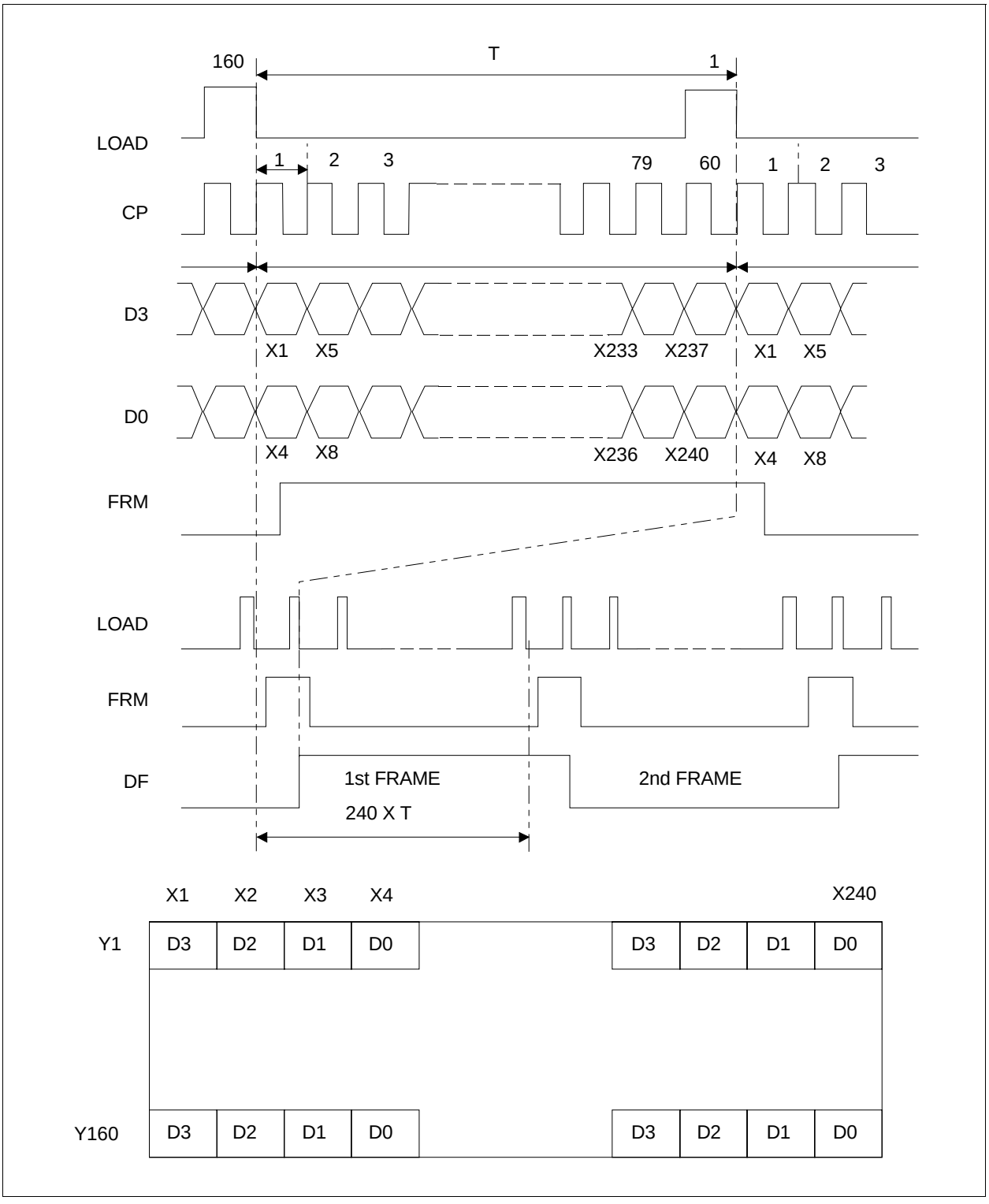
Common Mode (V_{SS}=0V, V_{DD}= 2.5~5.5V, V0=15 to 30 V, and Ta=-20 to +85°C)

Parameter	Symbol	Min.	Max.	Units	Condition
Shift clock period	t_{WLP}	250	--	ns	tr, tf <=20ns
Shift clock "H" pulse width	t_{WCKH}	15	--	ns	V_{DD} = 5.0V±10%
		30	--	ns	V_{DD} = 2.5~4.5V
Data setup time	t_{DS}	30	--	ns	
Data hold time	t_{DH}	50	--	ns	
Input signal rise time**	tr	--	50	ns	
Input signal fall time**	tf	--	50	ns	



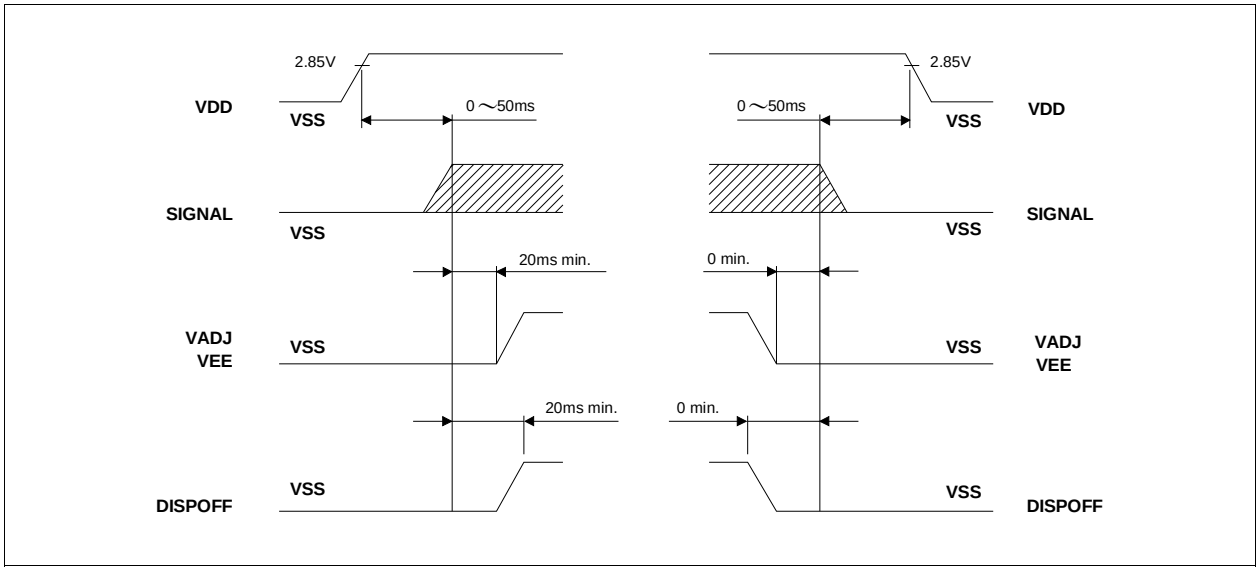
Signal Timing(1)

2.4 Timing Chart & Comparison of Display and Data



Signal Timing (2)

2.5 Power Supply ON/OFF Sequence



The missing pixels may occur when the LCM is driver beyond above power interface timing sequence.

2.6 Lighting Specifications

2.6.1 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating Voltage	V_{MAX}	Ta= 25°C	40	–	120	V_{RMS}
Frequency	F_{MAX}		50	–	1000	Hz

2.6.2 Operating Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating Voltage	V_{AC}	Ta= 25°C	--	100	–	V_{RMS}
Frequency	f_O		--	400	--	Hz
Luminosity*	L	Ta= 25°C V_{AC} =100 V_{RMS} f_O=400Hz	40	–	–	cd/m²
Current Consumption	I		–	–	0.2	mA/cm²
C.I.E.of 1931	X		0.15	0.18	0.21	--
	Y		0.35	0.38	0.41	--
Lifetime	–		--	2000	--	HRs
Color	–		Blue Green			

*The luminosity is measured from the surface of EL only.

** Time to half luminance.

3. Optical Specifications

3.1 LCD Driving Voltage Recommended

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
LCD Driving Voltage Note 1	V_O -V_{SS}	Ta = -20 °C	–	21.11	–	V
		Ta = 25 °C	17.89	18.49	19.09	V
		Ta = 70 °C	–	16.28	–	V

Note 1 : Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2 Optical Characteristics

Ta=25 °C, 1/160 Duty, 1/13 Bias, (Note 4), θ = 0°, ϕ = –°

Parameter		Symbol	Conditions	Min.	Typ.	Max.	Units
Contrast Ratio Note 1		C	θ = 0°, ϕ = 0°	–	3.0	–	–
Viewing Angle. CR≧2 (Shown in 3.3)		Front-Back	θ_f- θ_b, ϕ = 0°	+38	to	-29	deg.
		Left-Right	θ_l- θ_r, ϕ = 0°	+30	to	-32	deg.
Response Time	Rise Note 2	T_{ON}	Ta = -20 °C	–	2010	4020	msec
	Decay Note 3	T_{OFF}		–	8200	16400	msec
	Rise Note 2	T_{ON}	Ta = 25 °C	–	140	280	msec
	Decay Note 3	T_{OFF}		–	255	510	msec
	Rise Note 2	T_{ON}	Ta = 70 °C	–	75		msec
	Decay Note 3	T_{OFF}		–	100		msec

Note 1 : Contrast ratio is defined as follows.
CR = L_{OFF} / L_{ON}
L_{ON} : Luminance of the ON segments, L_{OFF} : Luminance of the OFF segments

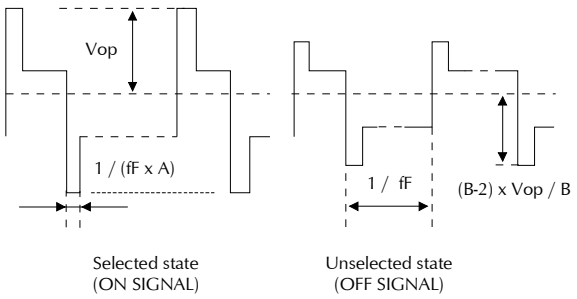
Note 2 : The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.

Note 3 : The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

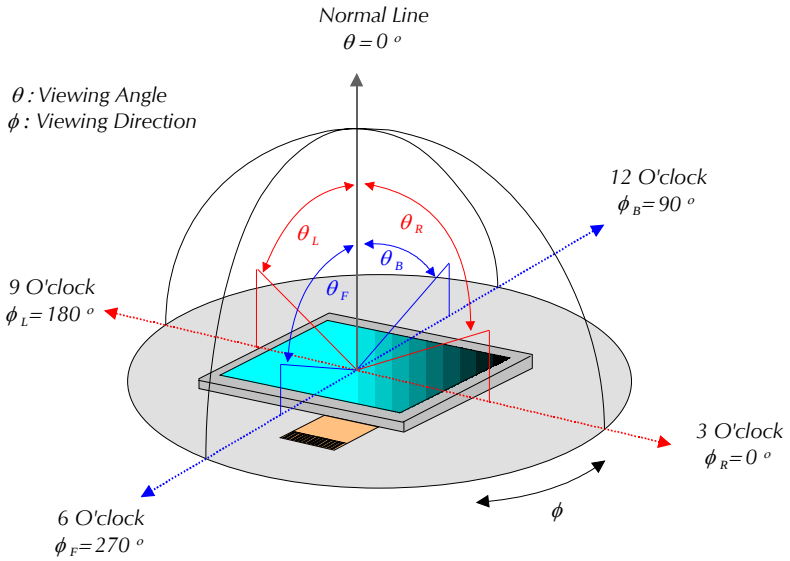
Note 4 : Definition of Driving Voltage V_D. Assuming that the typical driving waveforms shown below are applied to the LCD Panel at /A Duty - 1/B Bias (A : Duty Number, B : Bias Number). Driving voltage V_D is defined s follows: V_D = (Vth1+Vth2) / 2

Vth1 : The voltage VO-P that should provide 50% of the saturation level in the luminance at the segment which the ON signal is applied to.

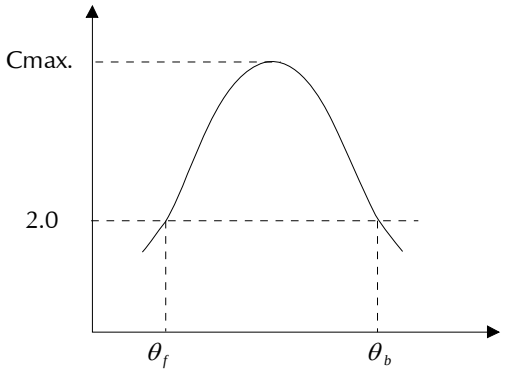
Vth2 : The voltage VO-P that should provide 50% of the saturation level in the luminance at the segment which the OFF signal is applied to.



3.3 Definition of Viewing Angle and Optimum Viewing Area



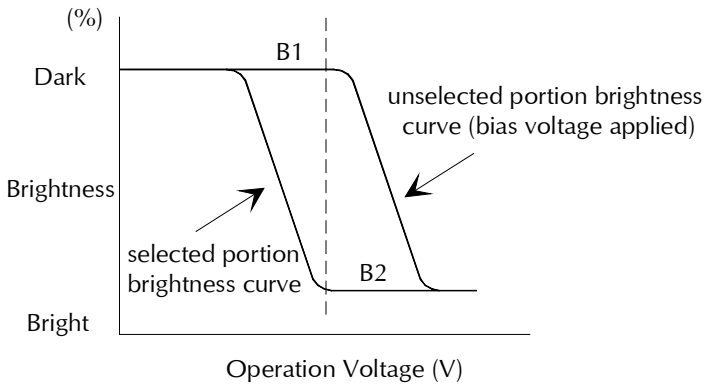
3.4 Definition of Viewing Angle θ_f and θ_b



Viewing angles θ (ϕ fixed)

Optimum viewing angle with the naked eye and viewing angle θ at Cmax. Above are not always the same.

3.5 Definition of Contrast C, *C= Brightness of selected dot (B1)/ Brightness of unselected dot (B2)*



4. I/O Terminal

4.1 Pin Assignment

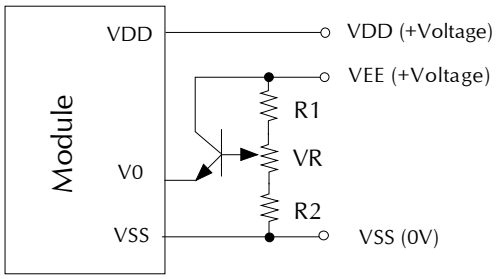
No.	Symbol	Level	Function
1	V5	–	Power supply for LCD drive
2	V2	–	Power supply for LCD drive
3	V_{EE}	–	Power supply for LCD drive
4	V_{DD}	–	Power supply for logic system
5	FLM	I/O	Input/output for chip select or data of shift register
6	F_GND	–	Frame Ground.
7	Load	I	Latch pulse input/shift clock input for shift register
8	V_{SS}	–	Ground pin connects to 0V
9	DF	I	AV converting signal input for LCD drive waveform
10	DISP	I	Control input pin for output deselect level
11	CP	I	Display data shift clock input for segment mode
12	V4	–	Power supply for LCD drive
13	V3	–	Power supply for LCD drive
14	DB3	I	Display Data
15	DB2	I	Display Data
16	DB1	I	Display Data
17	DB0	I	Display Data
18	N/C	–	No connection

EL pin out

No.	Symbol	Level	Function
1	EL 1	–	Power supply for EL B/L
2	EL 2	–	Power supply for EL B/L

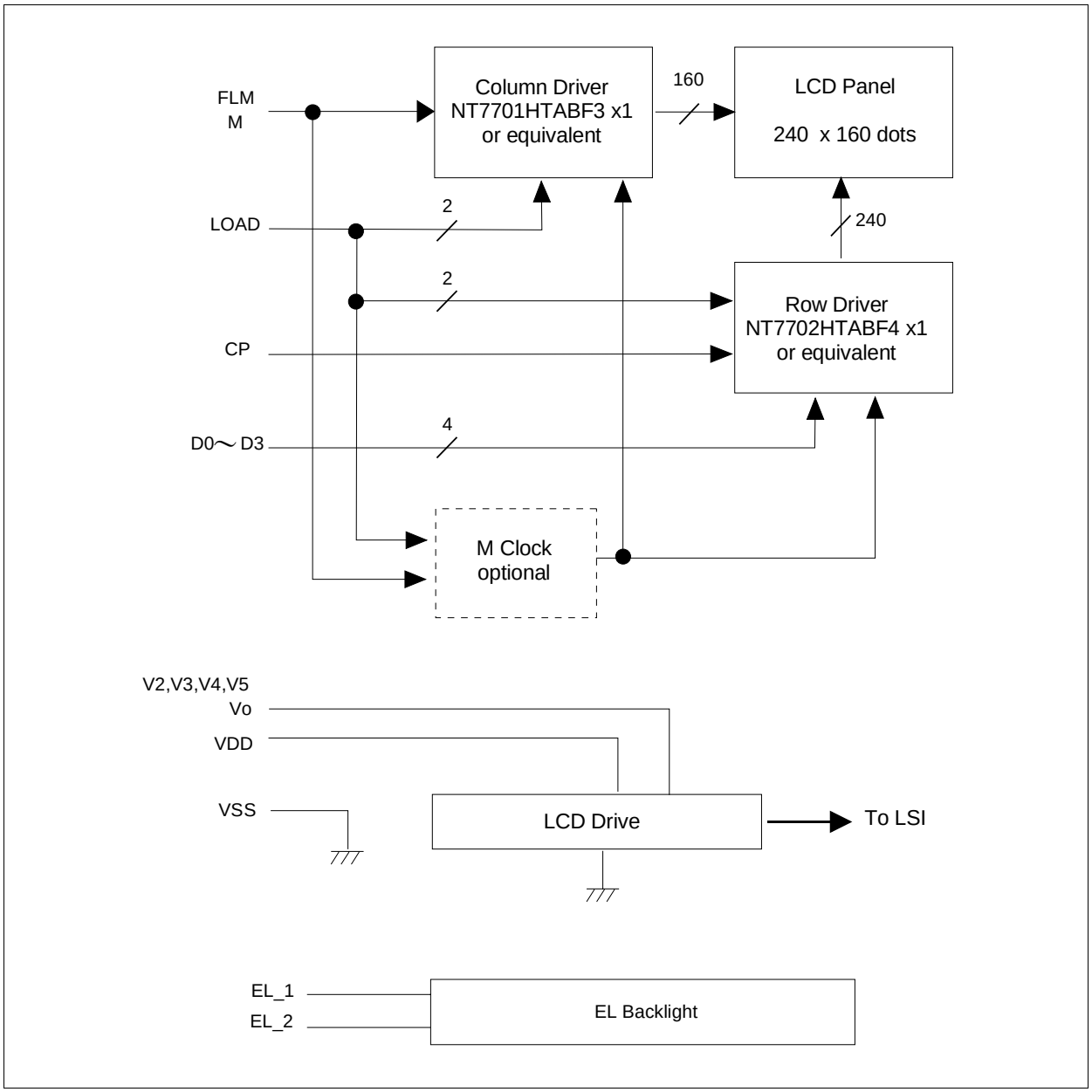
4.2 Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.



R1+R2+VR=10 ～ 20K
Tr = 2SA1202 or equivalent

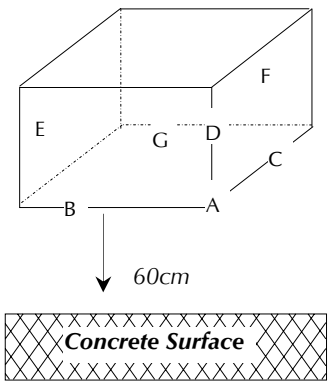
4.3 Block Diagram



5. Reliability Test

5.1 Test Item

No change on display and in operation under the following test condition.

No.	Test Item	Description	Condition	Note
1.	High Temperature (Operation)	Durability test under long time high temperature with electrical stress (voltage, current)	70°C ± 2°C 96hrs	
2.	High Temperature (Storage)	Durability test under long time high temperature storage	80°C ± 2°C 96hrs	4
3.	Low Temperature (Operation)	Durability test under long time low temperature with electrical stress (voltage, current)	-20°C ± 2°C, 96hrs	3
4.	Low Temperature (Storage)	Durability test under long time low temperature storage	-30°C ± 2°C, 96hrs	3, 4
5.	Damp Proof Test	Durability test under long time high temperature and high humidity	40°C± 2°C, 80% RH 96hrs	3 , 4
6.	Vibration Test	Total fixed amplitude: 1.5mm Vibration frequency: 10～55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes	- -	5
7.	Drop Test	To be measured after dropping from 60cm high on the concrete surface in packing state.  *Dropping method corner dropping* *A corner: once* *Edge dropping* *B, C, D edge: once* *Face dropping* *E, F, G face: once*		

Note 1: Unless otherwise specified, tests will be conducted under the following condition,
Temperature : 25°C ± 2°C
Humidity : 65% ± 5%
Note 2: Unless otherwise specified, tests will be not conducted under functioning state.
Note 3: No dew condensation to be observed.
Note 4: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.
Note 5: Vibration test will be conducted to the product itself without putting it in a container.

5.2 Judgment Standard

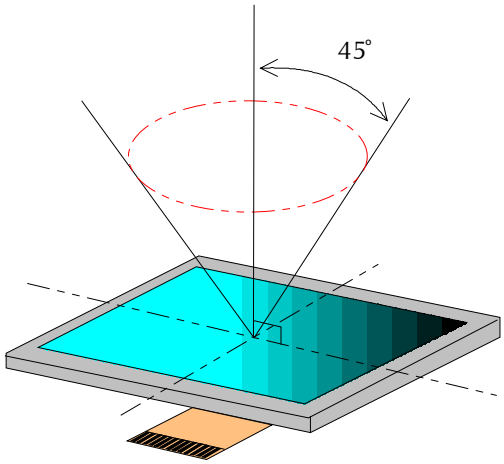
Failure Mode	Test Item							Judgment Standard
	1	2	3	4	5	6	7	
Orientation	*	*	*	*	*			No remarkable degradation of appearance under bias/ non-bias condition
Current Value (IAC)	*	*	*	*	*			No remarkable increase
Contrast	*		*	*	*			No remarkable poor contrast
Domain	*	*	*	*	*			Less than 20% of all dots have reverse tilt of more than on third of one dot area.
Bubble (Inside Cell)	*	*	*	*	*	*		As per "Appearance Standard" (Note. In-cluding one which disappear after 25°C 2H)
Polarizer	*				*	*		As per "Appearance Standard" no remarkable appearance change
Glass Damage							*	As per "Appearance Standard"

Note.1. * is strong linkage between Failure Mode and Test Item.
2. Number of Test Item should be referred to former page.
3. Judgment and Standard value should be fixed by other inspection standard and criteria samples.

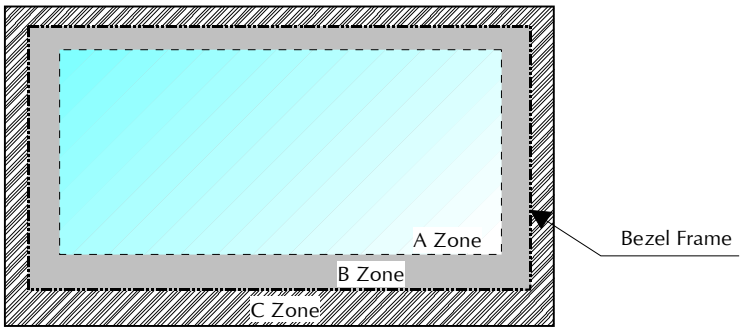
6. Appearance Standards

6.1 Inspection Conditions

The LCD shall be inspected under 40W white fluorescent light. The distance between the eyes and the sample shall be more than 30cm. All directions for inspecting the sample should be within 45° against perpendicular line.



6.2 Definition of Applicable Zones

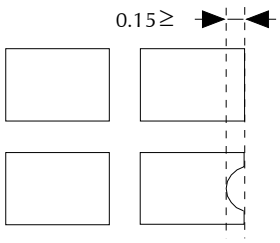
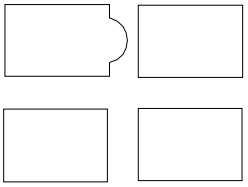
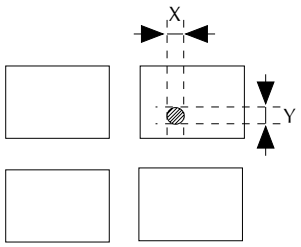
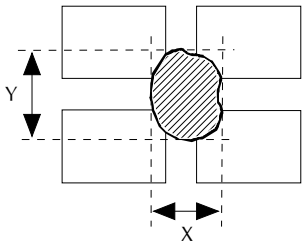


A Zone : Active display area
B Zone : Area from outside of "A Zone" to validity viewing area
C Zone : Rest parts
A Zone + B Zone = Validity viewing area

6.3 Standards

<table>
<tr><th>No.</th><th>Parameter</th><th>Criteria</th></tr>
<tr><td>1.</td><td>Black and White Spots, Foreign Substances</td><td>
(1) Round Shape
<table>
<tr><th rowspan="2">Zone
Dimension (mm)</th><th colspan="3">Acceptable Number</th></tr>
<tr><th>A</th><th>B</th><th>C</th></tr>
<tr><td>D ≤ 0.2</td><td>*</td><td>*</td><td>*</td></tr>
<tr><td>0.2 < D ≤ 0.3</td><td>3</td><td>5</td><td>*</td></tr>
<tr><td>0.3 < D ≤ 0.4</td><td>2</td><td>3</td><td>*</td></tr>
<tr><td>0.4 < D ≤ 0.5</td><td>0</td><td>1</td><td>*</td></tr>
<tr><td>0.5 < D</td><td>0</td><td>0</td><td>*</td></tr>
</table>
D = (Long + Short)/2 *: Disregard
(2) Line Shape
<table>
<tr><th rowspan="2">Zone
X (mm)</th><th rowspan="2">Zone
Y (mm)</th><th colspan="3">Acceptable Number</th></tr>
<tr><th>A</th><th>B</th><th>C</th></tr>
<tr><td>--</td><td>0.03 ≥ W</td><td>*</td><td>*</td><td>*</td></tr>
<tr><td>2.0 ≥ L</td><td>0.05 ≥ W</td><td>3</td><td>3</td><td>*</td></tr>
<tr><td>1.0 ≥ L</td><td>0.1 ≥ W</td><td>3</td><td>3</td><td>*</td></tr>
<tr><td>--</td><td>0.1 < W</td><td colspan="3">In the same way (1)</td></tr>
</table>
X : Length Y: Width *: Disregard
Total defects shall not exceed 5.
</td></tr>
<tr><td>2.</td><td>Air Bubbles (between glass & polarizer)</td><td>
<table>
<tr><th rowspan="2">Zone
Dimension (mm)</th><th colspan="3">Acceptable Number</th></tr>
<tr><th>A</th><th>B</th><th>C</th></tr>
<tr><td>D ≤ 0.3</td><td>*</td><td>*</td><td>*</td></tr>
<tr><td>0.3 < D ≤ 0.4</td><td>3</td><td>*</td><td>*</td></tr>
<tr><td>0.4 < D ≤ 0.6</td><td>2</td><td>3</td><td>*</td></tr>
<tr><td>0.6 < D</td><td>0</td><td>0</td><td>*</td></tr>
</table>
*: Disregard
Total defects shall not exceed 3.
</td></tr>
</table>

To be continued.....

No.	Parameter	Criteria
3.	The Shape of Dot	(1) Dot Shape (with Dent)  As per the sketch of left hand. (2) Dot Shape (with Projection)  Should not be connected to next dot. (3) Pin Hole  (X+Y)/2 ≤ 0.2mm (Less than 0.1mm is no counted.) (4) Deformation  (X+Y)/2 ≤ 0.2mm Total acceptable number: 1/dot, 5/cell (Defect number of (4): 1pc.)
4.	Polarizer Scratches	Not to be conspicuous defects.
5.	Polarizer Dirts	I f the stains are removed easily from LCDP surface, the module is not defective.
6.	Complex Foreign Substance Defects	Black spots, line shaped foreign substance or air bubbles between glass & polarizer should be 5pcs maximum in total.
7.	Distance between different Foreign Substance defects	D ≤ 0.2 : 20mm or more 0.2 < D : 40mm or more

7. Handling and Precautions

The Following precautions will guide you in handling our product correctly.

1 Liquid crystal display devices

1.1 The liquid crystal display device panel used in the liquid crystal display module is made of plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care.
1.2 The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.

2 Care of the liquid crystal display module against static electricity discharge.

2.1 When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect work tables against the hazards of electrical shock.
2.2 Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
2.3 Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.

3 When the LCD module alone must be stored for long periods of time:

3.1 Protect the modules from high temperature and humidity.
3.2 Keep the modules out of direct sunlight or direct exposure to ultra-violet rays.
3.3 Protect the modules from excessive external forces.

4 Use the module with a power supply that is equipped with an over current protector circuit, since the module is not provided with this protective feature.

5 Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.

6 Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.

8. Warranty:

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

1 We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.

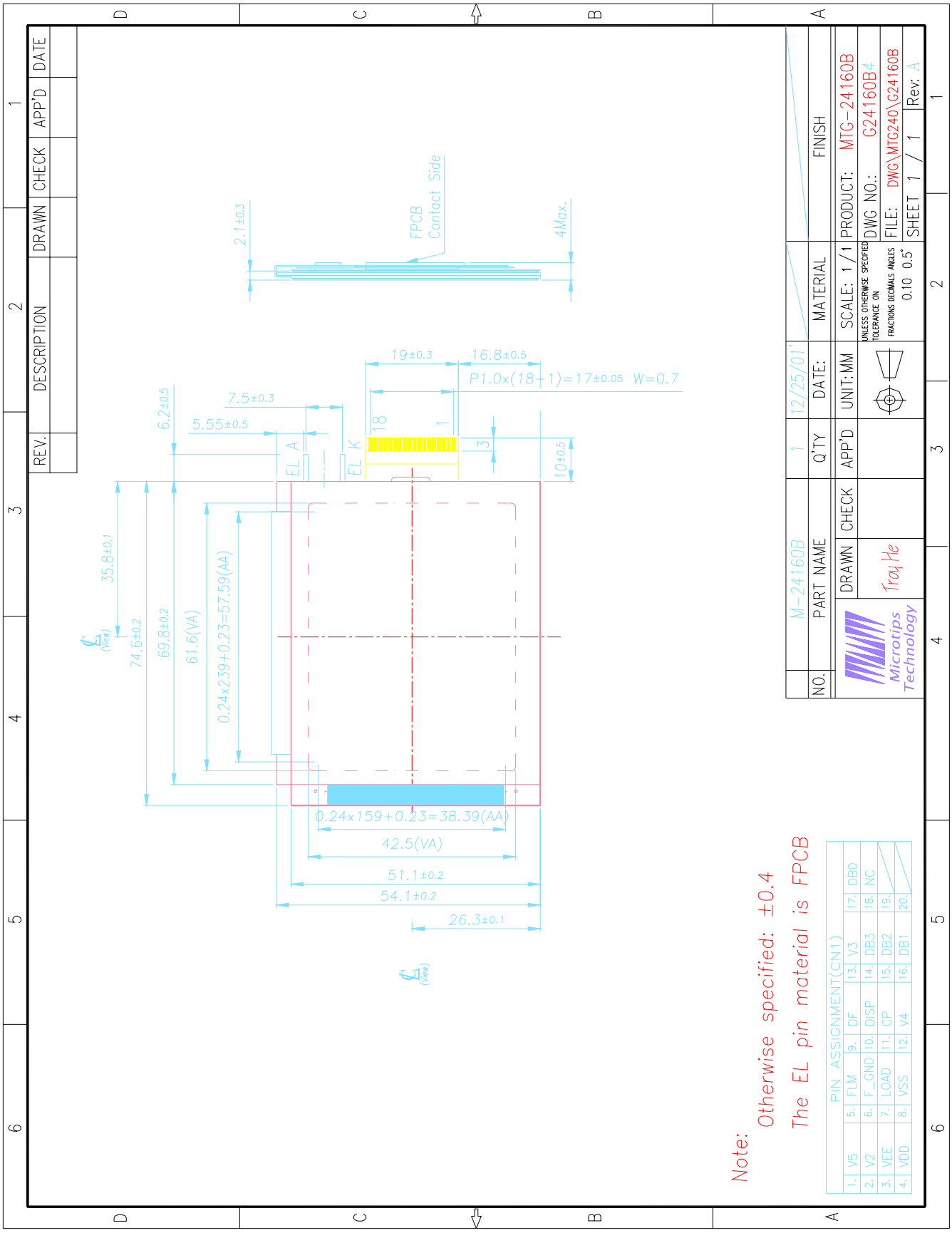
2 We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.

3 We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.

4 We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product. Microtips-origin longer than one year from Microtips production.

9. Dimensional Outlines

- See the next page.....


FPCB Contact Side
2.1±0.3
4Max.
19±0.3
16.8±0.5
P1.0x(18+1)=17±0.05 W=0.7
7.5±0.3
6.2±0.5
5.55±0.5
EL A
EL K
18
1
3
10±0.5
35.8±0.1
74.6±0.2
69.8±0.2
61.6(VA)
0.24x239+0.23=57.59(AA)
0.24x159+0.23=38.39(AA)
42.5(VA)
51.1±0.2
54.1±0.2
26.3±0.1
REV. DESCRIPTION DRAWN CHECK APP'D DATE
NO. PART NAME DRAWN CHECK APP'D Q'TY DATE: MATERIAL FINISH
M-24160B 1 12/25/01'
UNIT:MM SCALE: 1/1 PRODUCT: MTG-24160B
UNLESS OTHERWISE SPECIFIED TOLERANCE ON FRACTIONS DECIMALS ANGLES 0.10 0.5°
DWG NO.: G24160B4
FILE: DWG\MTG240\G24160B SHEET 1 / 1 Rev: A
Microtips Technology
Troy He
PIN ASSIGNMENT(CN1)
1. V5 2. V2 3. VEE 4. VDD 5. FLM 6. F_GND 7. LOAD 8. VSS 9. DF 10. DISP 11. CP 12. V4 13. V3 14. DB3 15. DB2 16. DB1 17. DB0 18. NC 19. 20.
Note:
Otherwise specified: ±0.4
The EL pin material is FPCB