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Part Number: [0458440003](#)
Status: **Active**
Overview: [edge_card_connectors](#)
Description: 2.50mm (.098") Pitch EXTreme PowerEdge™, Signal Connector, Double Sided, 4 Segments (8 Signal Contacts Per Segment), Through Hole Solder for 3.18mm (.125") or 2.36mm (.093") Thick PC Board

Documents:
[3D Model](#) [Product Specification PS-45719-001 \(PDF\)](#)
[Drawing \(PDF\)](#) [RoHS Certificate of Compliance \(PDF\)](#)

Agency Certification

CSA LR 19980
UL E29179

General

Product Family Edge Card Connectors
Series [45844](#)
Component Type Edgecard to PCB
Overview [edge_card_connectors](#)
Product Name EXTreme PowerEdge™

Physical

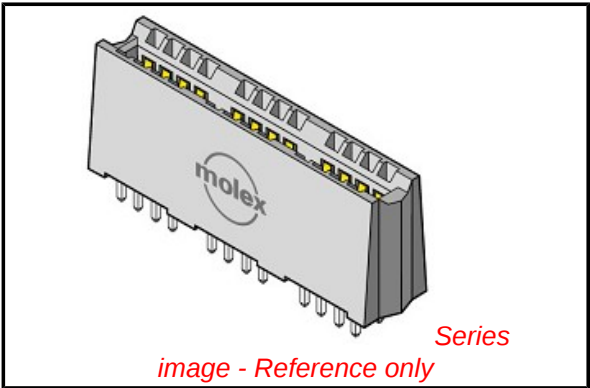
Circuits (Loaded) 32
Circuits (maximum) 32
Circuits Detail All Signal
Color - Resin Black
Durability (mating cycles max) 250
Entry Angle Vertical (Top Entry)
Flammability 94V-0
Keying to Mating Part N/A
Material - Metal Phosphor Bronze
Material - Plating Mating Gold
Material - Plating Termination Tin
Material - Resin High Temperature Thermoplastic
PC Tail Length (in) 0.182 In
PC Tail Length (mm) 4.63 mm
PCB Thickness Recommended (in) 0.093 In, 0.125 In
PCB Thickness Recommended (mm) 2.40 mm, 3.20 mm
Packaging Type Tray
Pitch - Mating Interface (in) 0.098 In
Pitch - Mating Interface (mm) 2.50 mm
Plating min: Mating (µin) 30
Plating min: Mating (µm) 0.75
Plating min: Termination (µin) 150
Plating min: Termination (µm) 3.75
Temperature Range - Operating -40°C to +105°C
Termination Interface: Style Through Hole

Electrical

Current - Maximum per Contact 3A
Voltage - Maximum 250V

Solder Process Data

Duration at Max. Process Temperature (seconds) 5
Lead-free Process Capability Wave Capable (TH only)
Max. Cycles at Max. Process Temperature 1



EU RoHS
ELV and RoHS
Compliant
REACH SVHC
Contains SVHC: No
Halogen-Free
Status
Halogen-Free

China RoHS



Need more information on product environmental compliance?

Email productcompliance@molex.com
For a multiple part number RoHS Certificate of Compliance, [click here](#)

Please visit the [Contact Us](#) section for any non-product compliance questions.

Search Parts in this Series
[45844Series](#)

Mates With
1.58mm (.062") double sided card edge

Process Temperature max. C250

Material Info

Reference - Drawing Numbers

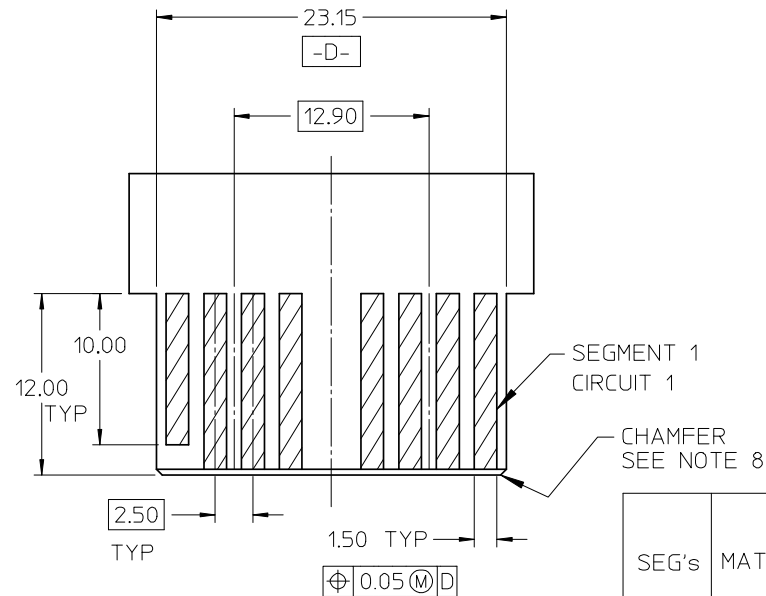
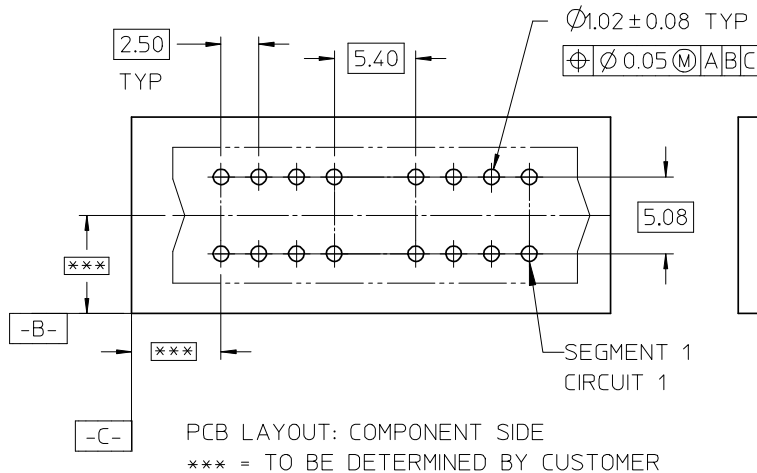
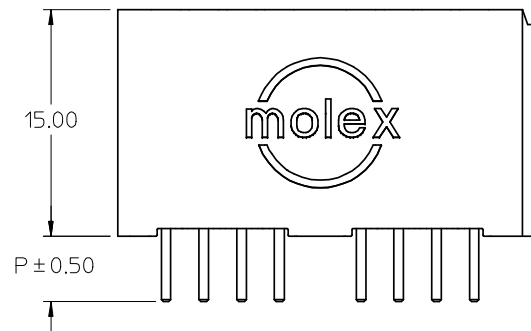
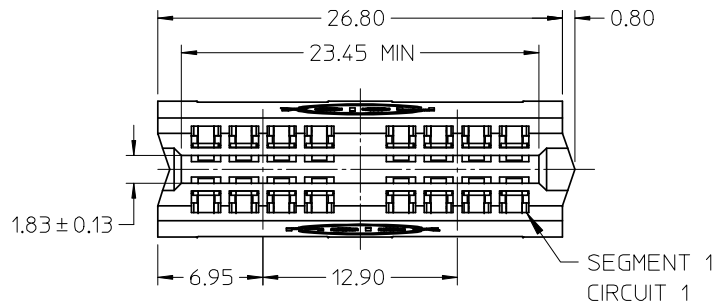
Packaging SpecificationPK-45714-001

Product SpecificationPS-45719-001

Sales DrawingSD-45844-001

This document was generated on 06/04/2010

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RECOMMENDED EDGE CARD LAYOUT
TOLERANCE: ± 0.05

NOTES:

1. HOUSING MATL: LCP, UL94V-0, BLACK
TERMINAL MATL: COPPER ALLOY
2. FINISH: SELECT GOLD: 30 M.I. MIN IN CONTACT AREA
SELECT TIN: 150 M.I. MIN IN SOLDER AREA
BOTH OVER NICKEL: 50 M.I. MIN
3. PRODUCT SPECIFICATION: PS-45719-001.
4. PACKAGING SPECIFICATION: PK-45714-001.
5. PCB THICKNESS: SEE CHART
6. EDGE CARD THICKNESS: 1.57 ± 0.15
7. DIMENSIONS GIVEN ACROSS CENTERLINES ARE SYMMETRICAL ABOUT THOSE CENTERLINES WITHIN HALF THE TOTAL TOLERANCE.
8. $0.50 \times 45^\circ$ CHAMFER ON LEADING EDGE, TYP 4 SIDES.

SEG's	MATERIAL NUMBER	DIM P	PCB THK
2	45844-0001	4.33	3.18/2.36
2	45844-0004	3.19	1.57

REVISE SLOT TOL. EC NO: UCP2008-2498 DRWN:JSCHAFER 2008/04/16 CHKD: 2008/04/22 APPR:APATEL 2008/04/25	DESCRIPTION REV	GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE MM ONLY		SCALE 2:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY DATE		TITLE POWER EDGE ASSEMBLY SIGNAL VERTICAL SOLDER TAIL				
		4 PLACES	± ---	± ---	MARGUL IS 2004/07/29						
		3 PLACES	± ---	± ---	CHECKED BY DATE						
		2 PLACES	± 0.25	± ---	MARGUL IS 2004/07/29						
		1 PLACE	± ---	± ---	APPROVED BY DATE		MOLEX MOLEX INCORPORATED				
		ANGULAR ±1/2°			MARGUL IS 2004/07/29						
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			SEE CHART		DOCUMENT NO. SD-45844-001				SHEET NO. 1 OF 3		
			SIZE A	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

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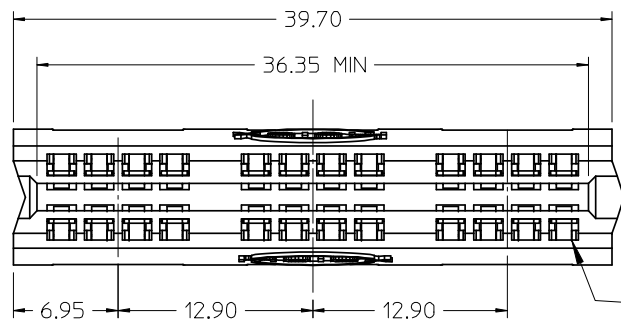
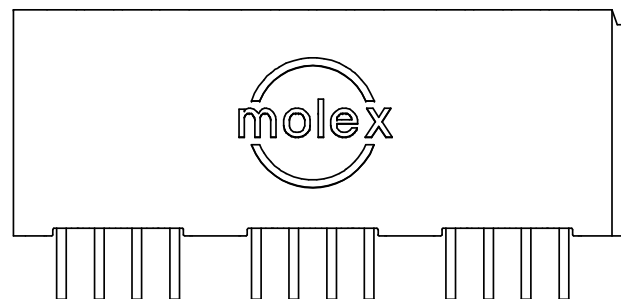
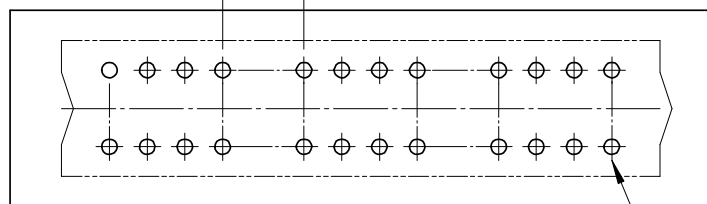
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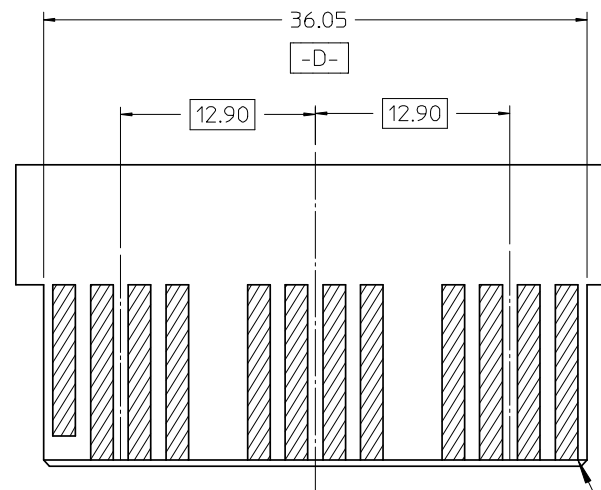
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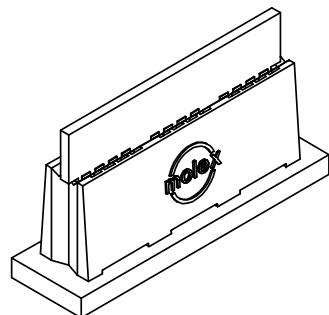
SEE SHEET ONE FOR NOTES AND DIMENSIONS NOT SHOWN

SEGMENT 1
CIRCUIT 15.40
TYP 2

PBC LAYOUT: COMPONENT SIDE

SEGMENT 1
CIRCUIT 1SEGMENT 1
CIRCUIT 1RECOMMENDED EDGE CARD LAYOUT:
TOLERANCE: ± 0.05

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3	45844-0005	3.19	1.57



EC NO: UCP2008-2498	2008/04/16	2008/04/22	2008/04/25
DRWN: JSCHAFER	CHKD:	APPR: APATEL	
D	REV	DESCRIPTION	

GENERAL TOLERANCES
(UNLESS SPECIFIED)

	mm	INCH
4 PLACES	$\pm$ ---	$\pm$ ---
3 PLACES	$\pm$ ---	$\pm$ ---
2 PLACES	± 0.25	$\pm$ ---
1 PLACE	$\pm$ ---	$\pm$ ---

ANGULAR $\pm 1/2^\circ$ DRAFT WHERE APPLICABLE
MUST REMAIN
WITHIN DIMENSIONSDIMENSION STYLE
MM ONLY

DRAWN BY	DATE
MARGULIS	2004/07/29
CHECKED BY	DATE
MARGULIS	2004/07/29
APPROVED BY	DATE
MARGULIS	2004/07/29

MATERIAL NUMBER
SEE CHARTSIZE
ASCALE
2:1DESIGN UNITS
METRICTHIRD ANGLE
PROJECTIONTITLE
POWER EDGE ASSEMBLY
SIGNAL VERTICAL
SOLDER TAIL

MOLEX INCORPORATED

DOCUMENT NO.
SD-45844-001SHEET NO.
2

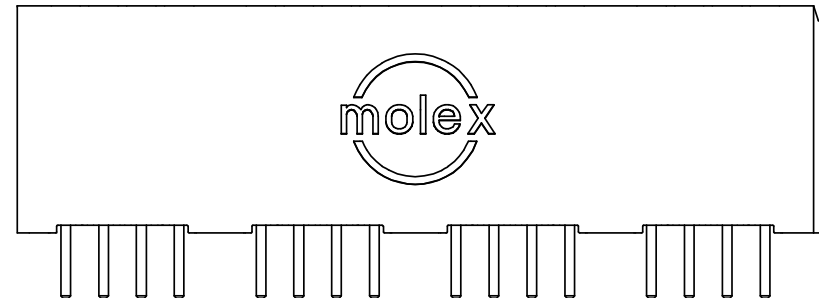
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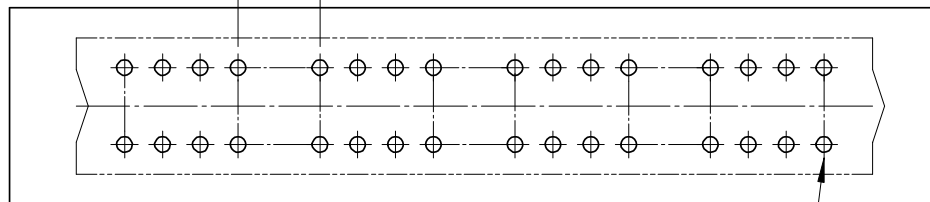
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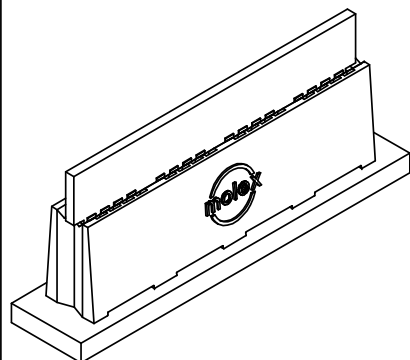


5.40
TYP 3



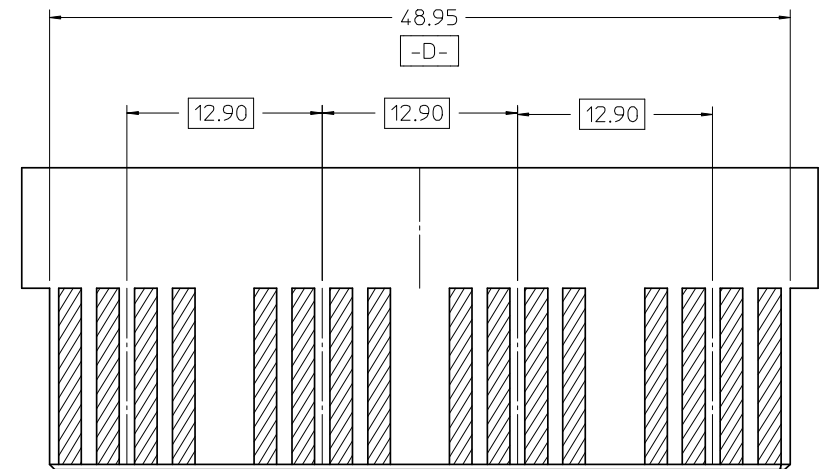
PCB LAYOUT: COMPONENT SIDE

SEGMENT 1
CIRCUIT 1



tb_frame_A_P_AM_T
Rev. D 2004/06/28

SEE SHEET ONE FOR NOTES AND DIMENSIONS NOT SHOWN



RECOMMENDED EDGE CARD LAYOUT
TOLERANCE:±0.05

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