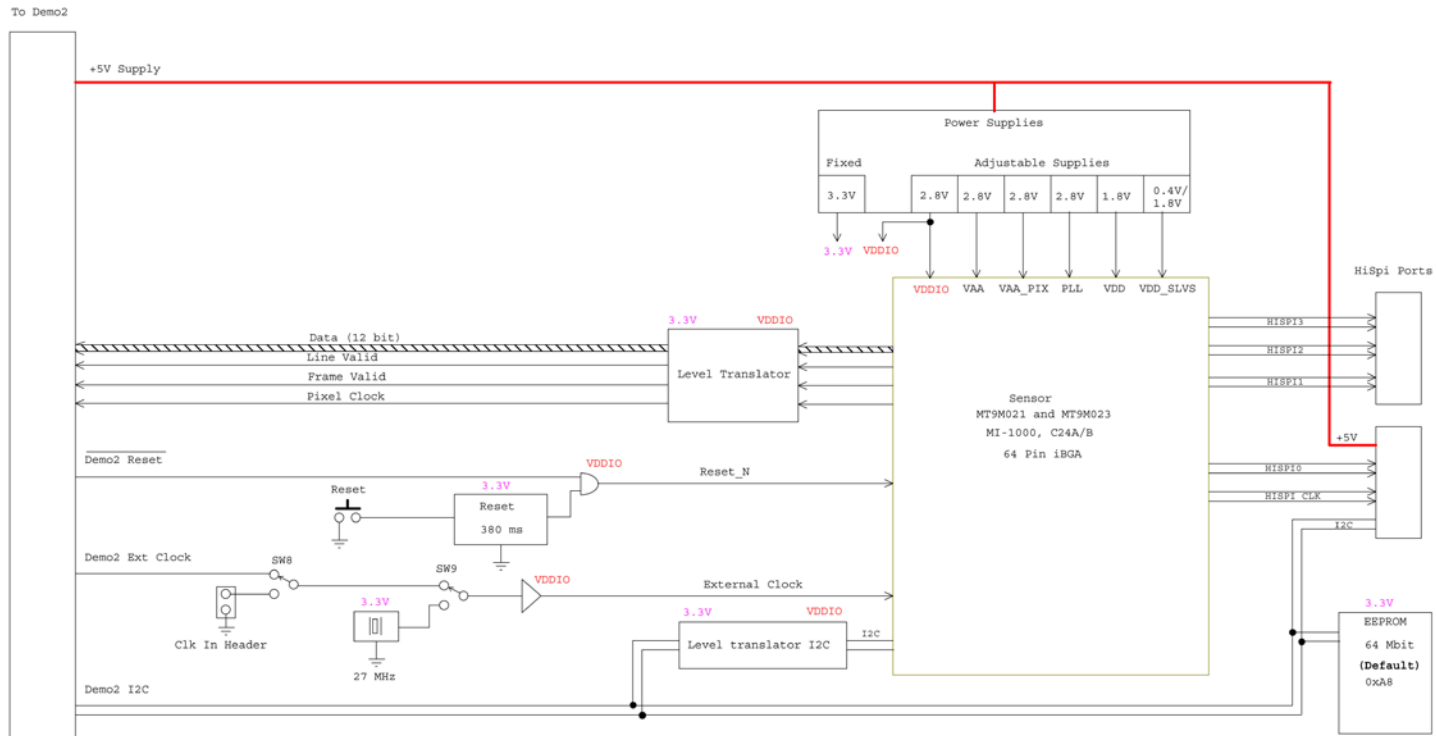
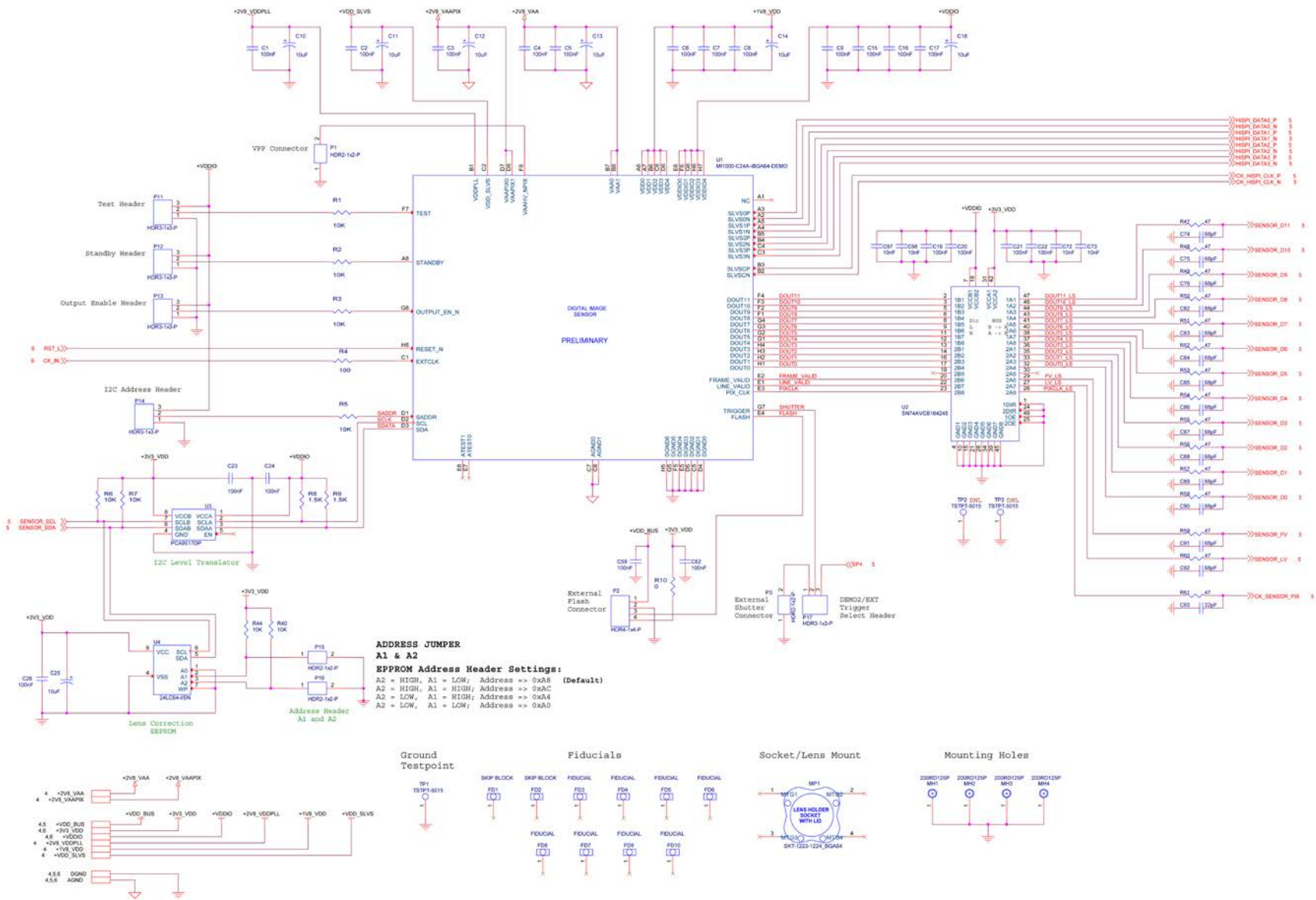


## Schematic for the AR0132AT6M00XPEAH3-GEVB Evaluation Board

Block Diagram - MT9M021 IBGA and MT9M023 IBGA



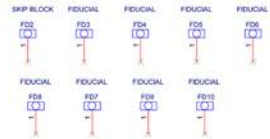


**ADDRESS JUMPER A1 & A2**  
**EPPROM Address Header Settings:**  
 A2 = HIGH, A1 = LOW; Address => 0xA8 (Default)  
 A2 = HIGH, A1 = HIGH; Address => 0xA0  
 A2 = LOW, A1 = HIGH; Address => 0xA4  
 A2 = LOW, A1 = LOW; Address => 0xA0

**Ground Testpoint**



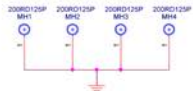
**Fiducials**



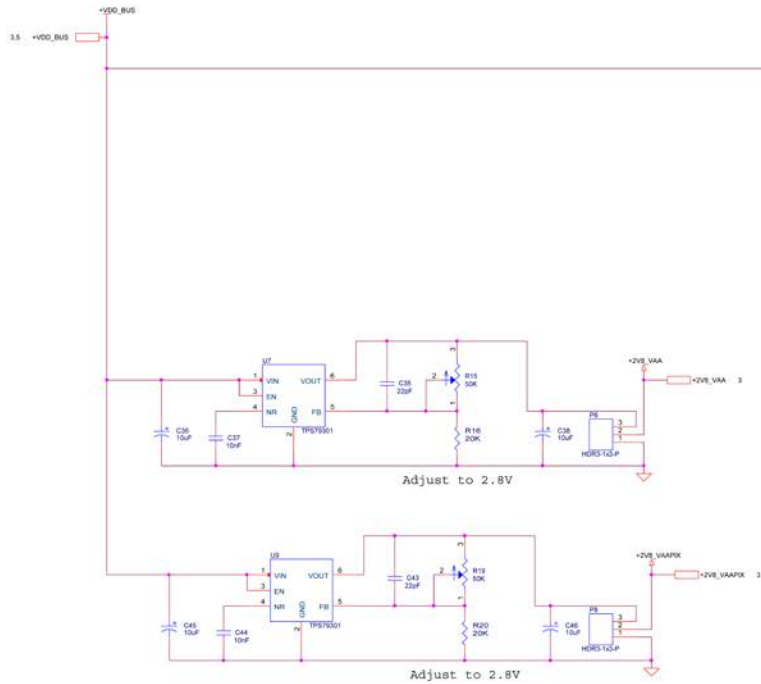
**Socket/Lens Mount**



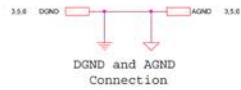
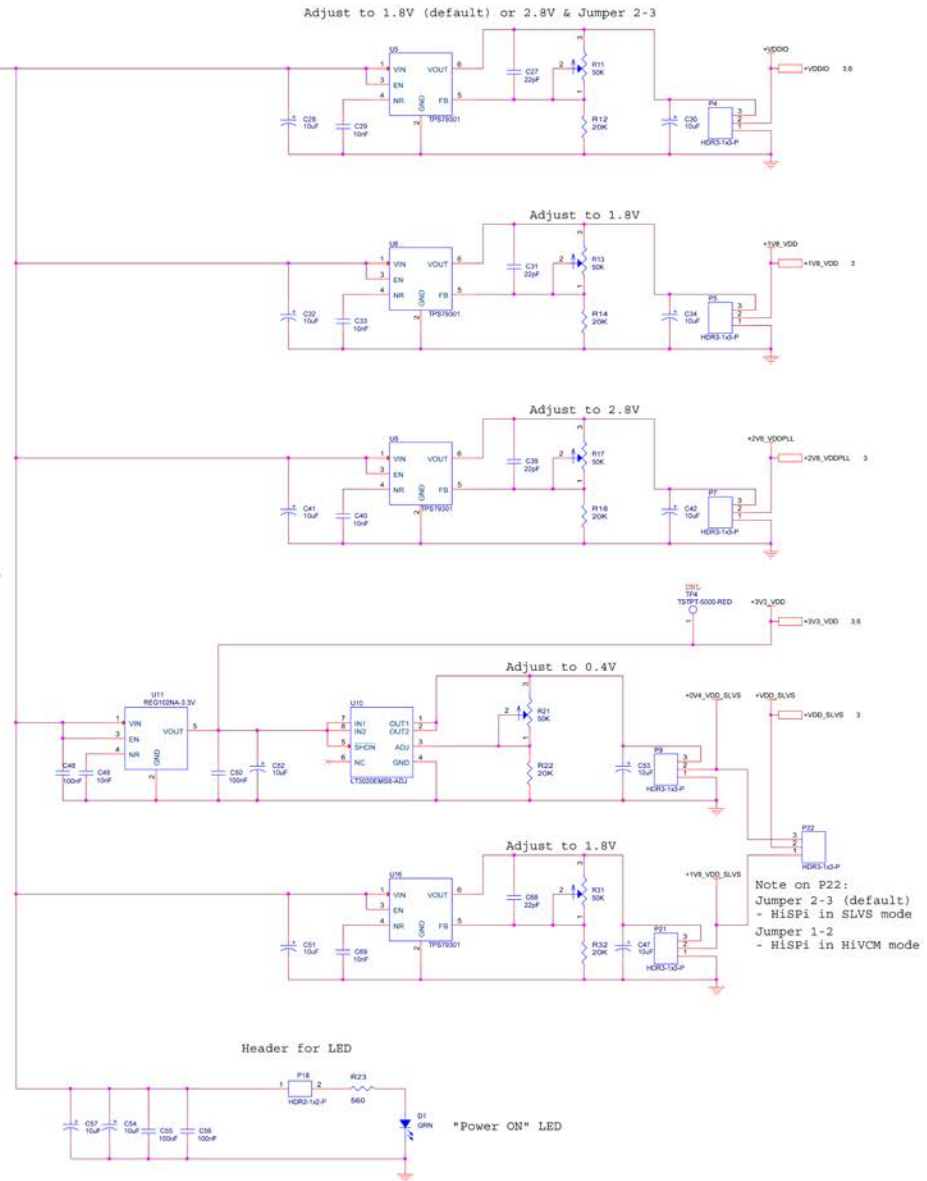
**Mounting Holes**



Analog



Digital





# Headboard Interface

