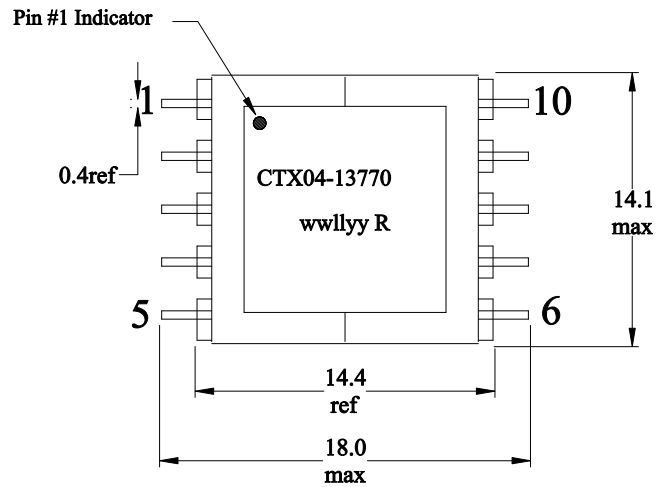
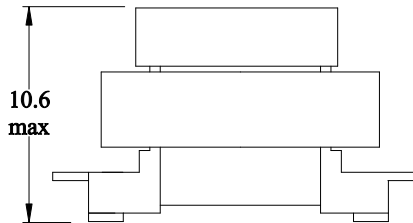


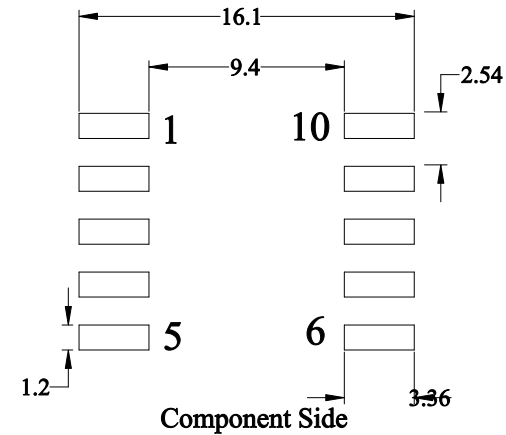
TOP VIEW



FRONT VIEW



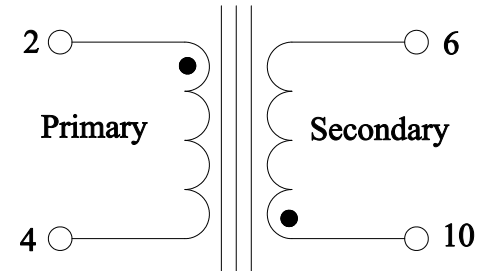
RECOMMENDED PCB LAYOUT



ELECTRICAL CHARACTERISTICS

OCL @ 10kHz, 0.25Vrms & 0.0Adc. Pins (2 - 4). 31.6 - 42.8uH,
 OCL @ 10kHz, 0.25Vrms & 2.50Adc. Pins (2 - 4). 26.8uH minimum.
 DCR @ 20 degrees C., pins (2 - 4). 0.300 Ohm Max.
 DCR @ 20 degrees C., pins (10 - 6): 105.0 Ohms Max.
 Turns ratio: Pins (2 - 4) : (10 - 6). 1:30
 HiPot @ 500Vdc for 1 second. Winding to winding and windings to core:

SCHEMATIC



Notes:

- 1). All dimensions are in mm unless otherwise specified.
 - 2). Tolerances are +/- 0.25 millimeters unless stated otherwise.
 - 3). All soldering surfaces must be coplanar within 0.1524 millimeters.
 - 4). Core wrapped with 2 layers of epoxy tape.
 - 5). wwllyy= Date Code, R= revision level
 - 6). PCB tolerances are +/-0.1 millimeters unless stated otherwise.
- This document subject to change without notice

TEMPLATE NAME: SG7.dot REV X5

Cooper Bussmann

1225 Broken Sound Parkway, Boca Raton, FL 33487

Transformer, continuous mode flyback
 4 - 6Vdc (input) : 330Vdc @ 30mAdc.

100kHz, 37uH 2.5@ Adc (peak).
 EE12.6, 10 Pin J-Lead, Horizontal, SMT

Size	Drawing Number:	CTX04-13770
A	Revision Level:	B