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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK4151

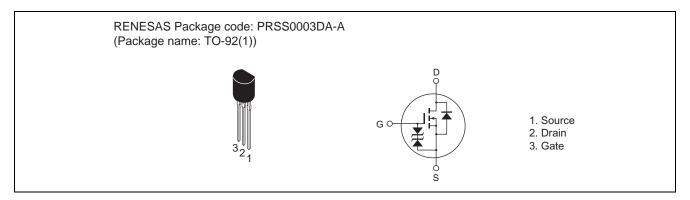
Silicon N Channel MOS FET High Speed Power Switching

REJ03G1901-0100 Rev.1.00 Mar 15, 2010

Features

- Capable of 2.5 V gate drive
- Low drive current
- Low on-resistance $R_{DS(on)}=1.5~\Omega~typ.~(at~I_D=0.5~A,~V_{GS}=4~V,~Ta=25^{\circ}C)$

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	150	V
Gate to source voltage	V _{GSS}	±10	V
Drain current	I _D Note1	1	Α
Drain peak current	I _{D (pulse)} Note2	4	Α
Body-drain diode reverse drain current	I _{DR} Note1	1	Α
Body-drain diode reverse drain peak current	I _{DR} (pulse)	4	Α
Channel dissipation	Pch Note2	0.75	W
Channel to ambient thermal impedance	θch-a	166.7	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 30%

2. $PW \le 10 \mu s$, duty cycle $\le 1\%$

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

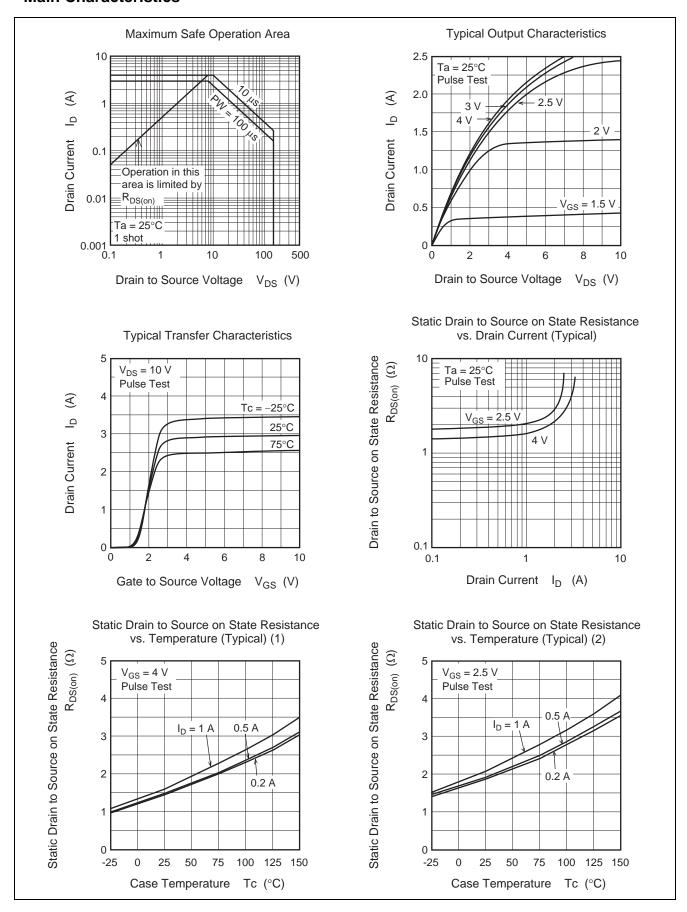
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	150	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	±10	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}			±10	μΑ	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}			1	μΑ	$V_{DS} = 150 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.5	_	1.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	_	1.5	1.95	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note3}}$
	R _{DS(on)}	_	1.9	2.5	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 2.5 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	98	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	31	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	14	_	pF	f = 1 MHz
Total gate charge	Qg	_	3.5	_	nC	V _{DD} = 100 V
Gate to source charge	Qgs	_	0.5	_	nC	$V_{GS} = 4 V$
Gate to drain charge	Qgd	_	1.8	_	nC	I _D = 1 A
Turn-on delay time	t _{d(on)}	_	8	_	ns	V _{GS} = 4 V
Rise time	t _r	_	12	_	ns	$I_D = 0.5 A$
Turn-off delay time	t _{d(off)}	_	34	_	ns	$R_L = 60 \Omega$
Fall time	t _f	_	19	_	ns	
Body-drain diode forward voltage	V_{DF}	_	1.0	1.5	V	$I_F = 1 \text{ A}, V_{GS} = 0^{\text{Note3}}$
Body-drain diode reverse recovery time	t _{rr}	_	60	_	ns	$I_F = 1 A, V_{GS} = 0$
						di _F / dt =100 A/μs

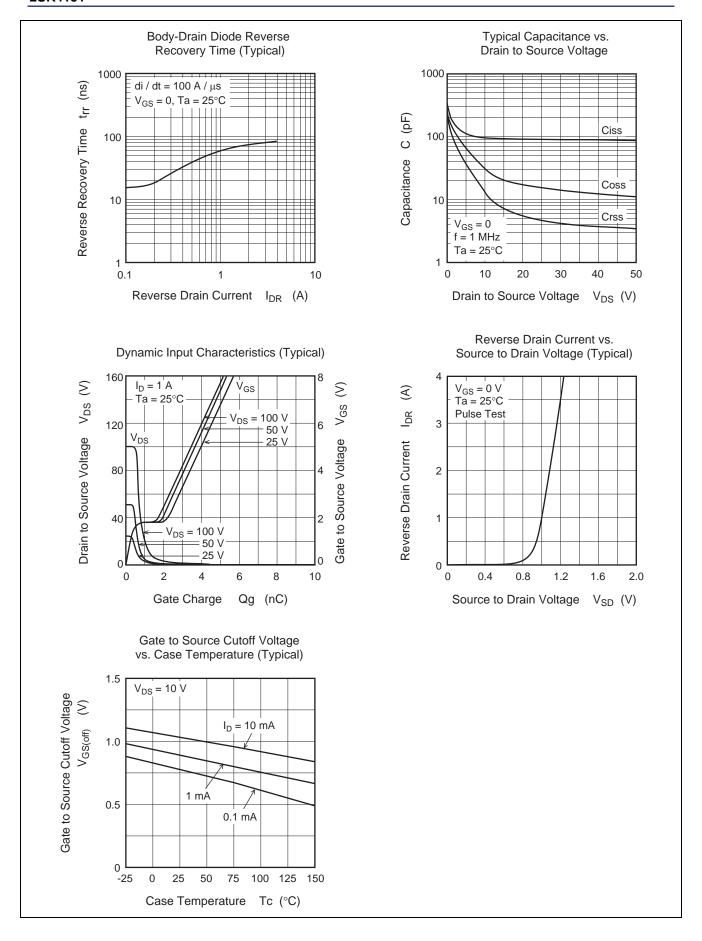
Notes: 3. Pulse test

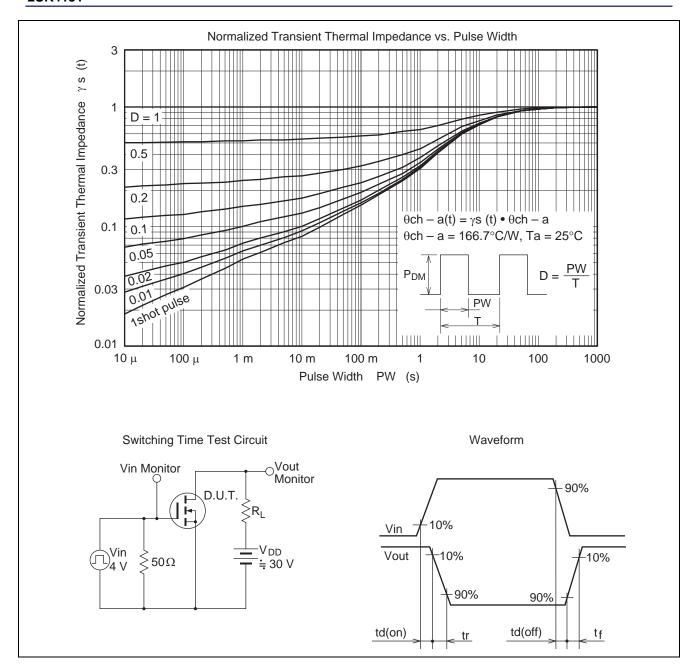
^{4.} This device is sensitive to electrostatic discharge.

It is recommended to adopt appropriate cautions when handling this product.

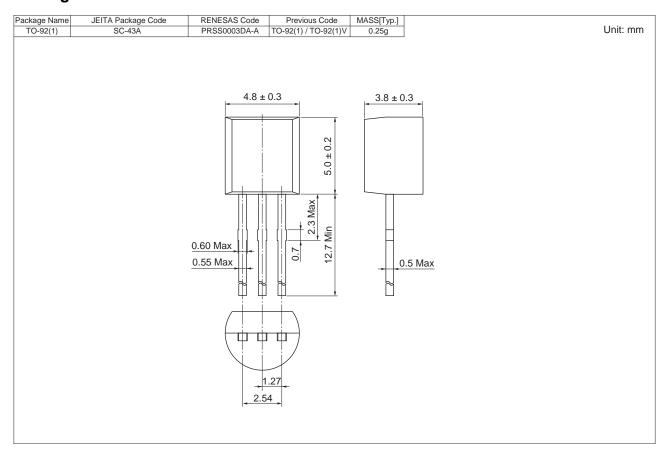
Main Characteristics







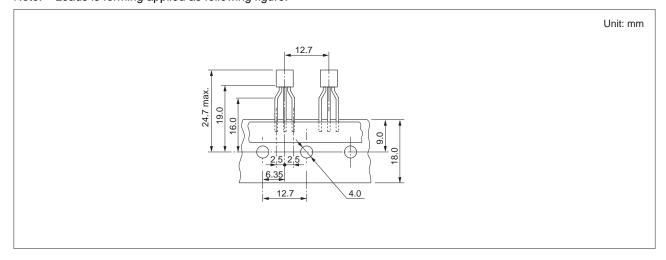
Package Dimension



Ordering Information

Part No.	Quantity	Shipping Container
2SK4151TZ-E	2500 pcs	Hold box, Radial taping

Note: Leads is forming applied as following figure.



Renesas Technology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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