-500mA / -40V Digital transistors (with built-in resistor) **DTB143TK**

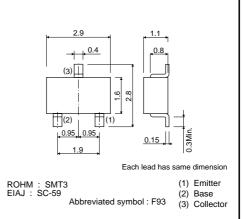
Applications

Inverter, Interface, Driver

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on / off conditions need to be set for operation, making the device design easy.

External dimensions (Unit : mm)



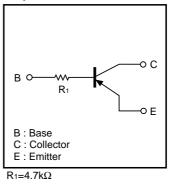
Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

	Package	SMT3			
	Packaging type	Taping			
	Code	T146			
Part No.	Basic ordering unit (pieces)	3000			
DTB143TK		0			

Equivalent circuit



Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	Vceo	-40	V
Emitter-base voltage	Vево	-5	V
Collector current	lc	-500	mA
Collector power dissipation	Pc	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	Ĵ



Transistors

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	-	-	V	Ic=-50μA
Collector-emitter breakdown voltage	BVCEO	-40	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	ВVево	-5	-	-	V	Iε= -50μA
Collector cutoff current	Ісво	-	-	-0.5	μA	Vcb=-50V
Emitter cutoff current	Іево	-	-	-0.5	μA	Veb=-4V
Collector-emitter saturation voltage	VCE(sat)	-	-	-0.3	V	Ic/IB=-50mA/-2.5mA
DC current transfer ratio	hfe	100	250	600	-	Vce= -5V, Ic= -50mA
Input resistance	R1	3.29	4.7	6.11	kΩ	_
Transition frequency	f⊤ *	_	200	_	MHz	Vce= -10V, Ie=50mA, f=100MHz

* Characteristics of built-in transistor

•Electrical characteristic curves

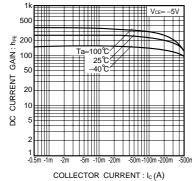


Fig.1 DC current gain vs. collectorcurrent

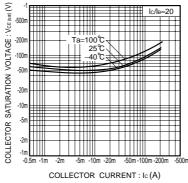


Fig.2Collector-emitter saturation voltage vs. collector current

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