EMD3FHA / UMD3NFHA / IMD3AFRA

NPN + PNP Complex Digital Transistors (Bias Resistor Built-in Transistors)

AEC-Q101 Qualified

Datasheet

<For DTr1(NPN)>

| Parameter | Value |
|----------------------|-------|
| V _{CC} | 50V |
| I _{C(MAX.)} | 100mA |
| R ₁ | 10kΩ |
| R_2 | 10kΩ |

<For DTr2(PNP)>

| Parameter | Value | | |
|----------------------|--------|--|--|
| V _{CC} | -50V | | |
| I _{C(MAX.)} | -100mA | | |
| R ₁ | 10kΩ | | |
| R_2 | 10kΩ | | |

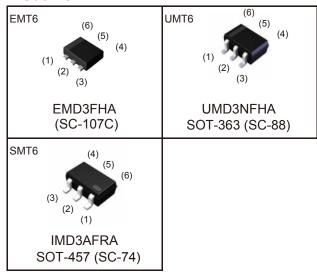
Features

- 1) Both the DTC114E chip and DTA114E chip in one package.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Lead Free/RoHS Compliant.

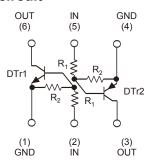
Application

Inverter circuit, Interface circuit, Driver circuit

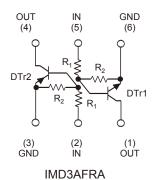
Outline



•Inner circuit



EMD3FHA / UMD3NFHA



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Packaging specifications

| Part No. | Package | Package size (mm) | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit (pcs) | Marking |
|----------|---------|-------------------------|----------------|-------------------|-----------------|---------------------------------|---------|
| EMD3FHA | EMT6 | 1616 | T2R | 180 | 8 | 8,000 | D3 |
| UMD3NFHA | UMT6 | 2021 | TR | 180 | 8 | 3,000 | D3 |
| IMD3AFRA | SMT6 | 2928 | T108 | 180 | 8 | 3,000 | D3 |

● Absolute maximum ratings (Ta = 25°C)

| Param | neter | Symbol | DTr1(NPN) | DTr2(PNP) | Unit |
|------------------------------|--------------------|-------------------------|---------------|------------|------|
| Supply voltage | | V_{CC} | 50 | -50 | V |
| Input voltage | | V_{IN} | -10 to +40 | -40 to +10 | V |
| Output current | | Io | 50 | -50 | mA |
| Collector current | | I _{C(MAX.)} *1 | 100 | -100 | mA |
| Power dissination | EMD3FHA / UMD3NFHA | P _D *2 | 150 (Total)*3 | | mW |
| Power dissipation IMD3AFRA | | Γ _D | 300 (Total)*4 | | mW |
| Junction temperature | | T _j | 150 | | °C |
| Range of storage temperature | | T _{stg} | −55 to +150 | | °C |

●Electrical characteristics(Ta = 25°C) <For DTr1(NPN)>

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
|----------------------|--------------------------------|--|------|------|------|------|
| Input voltage | $V_{I(off)}$ | $V_{CC} = 5V, I_{O} = 100 \mu A$ | 1 | - | 0.5 | V |
| | $V_{I(on)}$ | $V_0 = 0.3V, I_0 = 10mA$ | 3.0 | - | ı | V |
| Output voltage | $V_{O(on)}$ | $I_{O}/I_{I} = 10mA/0.5mA$ | - | 0.1 | 0.3 | V |
| Input current | I ₁ | V _I = 5V | - | - | 0.88 | mA |
| Output current | I _{O(off)} | $V_{CC} = 50V, V_I = 0V$ | - | - | 0.5 | μА |
| DC current gain | Gı | $V_O = 5V$, $I_O = 5mA$ | 30 | - | - | - |
| Input resistance | R ₁ | - | 7 | 10 | 13 | kΩ |
| Resistance ratio | R ₂ /R ₁ | - | 0.8 | 1 | 1.2 | - |
| Transition frequency | f _T *1 | $V_{CE} = 10V, I_{E} = -5mA$ f = 100MHz | | 250 | 1 | MHz |

●Electrical characteristics(Ta = 25°C) <For DTr2(PNP)>

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
|----------------------|--------------------------------|--|------|------|-------|------|
| Input voltage | $V_{I(off)}$ | $V_{CC} = -5V, I_{O} = -100 \mu A$ | 1 | - | -0.5 | V |
| | $V_{I(on)}$ | $V_O = -0.3V, I_O = -10mA$ | -3.0 | - | - | V |
| Output voltage | $V_{O(on)}$ | $I_{O}/I_{I} = -10\text{mA}/-0.5\text{mA}$ | - | -0.1 | -0.3 | V |
| Input current | I _I | V₁ = −5V | - | - | -0.88 | mA |
| Output current | I _{O(off)} | $V_{CC} = -50V, V_{I} = 0V$ | - | - | -0.5 | μΑ |
| DC current gain | G _I | $V_0 = -5V, I_0 = -5mA$ | 30 | - | - | - |
| Input resistance | R ₁ | - | 7 | 10 | 13 | kΩ |
| Resistance ratio | R ₂ /R ₁ | - | 0.8 | 1 | 1.2 | - |
| Transition frequency | f _T *1 | $V_{CE} = -10V, I_{E} = 5mA$ f = 100MHz | - | 250 | - | MHz |

^{*1} Characteristics of built-in transistor

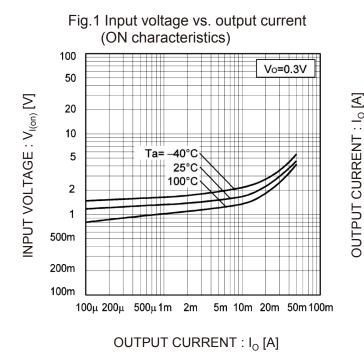


^{*2} Each terminal mounted on a reference footprint

^{*3 120}mW per element must not be exceeded.

^{*4 200}mW per element must not be exceeded.

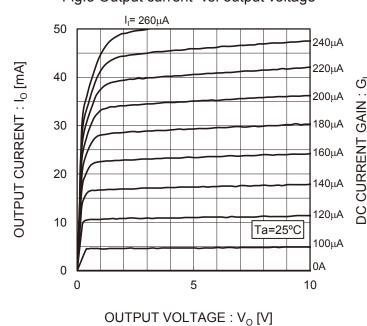
●Electrical characteristic curves (Ta = 25°C) <For DTr1(NPN)>



(OFF characteristics) 10m Vcc=5V 5m 2m Ta=100°C 1m 25°C 500μ -40°C 200μ 100μ 50μ 20μ 10μ 5μ 2μ 1μ 0 0.5 1.0 1.5 2.0 2.5 3.0 INPUT VOLTAGE : $V_{I(off)}[V]$

Fig.2 Output current vs. input voltage

Fig.3 Output current vs. output voltage



OUTPUT CURRENT : Io [A]

5m 10m 20m

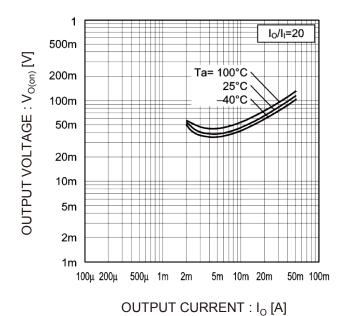
500μ 1m 2m

100μ 200μ

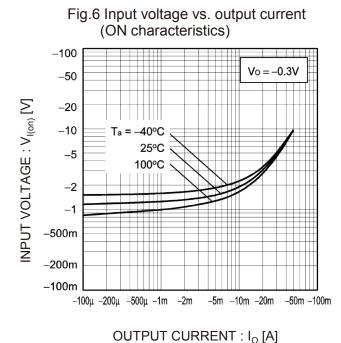
50m 100m

●Electrical characteristic curves (Ta = 25°C) <For DTr1(NPN)>

Fig.5 Output voltage vs. output current



●Electrical characteristic curves (Ta = 25°C) <For DTr2(PNP)>



(OFF characteristics) -10m -5m $V_{cc} = -5V$ -2m OUTPUT CURRENT : I_o [A] –1m –500μ –200μ 100°C 25°C -100μ -40°C -50μ -20μ -10μ -5μ -2μ -1μ -0.5-1.5-2.00 -1.0-3.0INPUT VOLTAGE: V_{I(off)}[V]

Fig.7 Output current vs. input voltage

●Electrical characteristic curves (Ta = 25°C) <For DTr2(PNP)>

Fig.8 Output current vs. output voltage

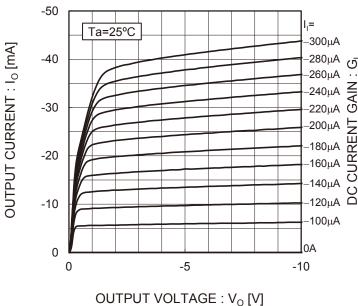
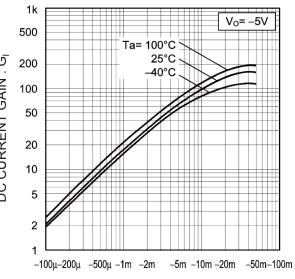
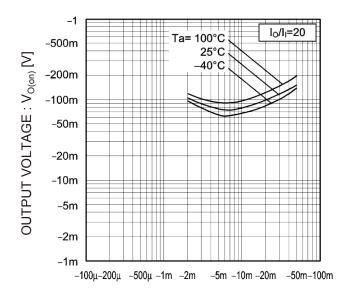


Fig.9 DC current gain vs. output current



OUTPUT CURRENT : Io [A]

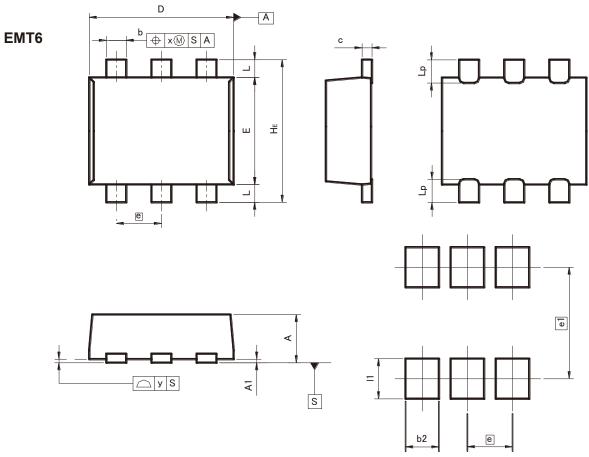
Fig.10 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

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●Dimensions (Unit : mm)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

| DIM MILIME | | ETERS | INC | HES |
|------------|------|-------|-------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.45 | 0.55 | 0.018 | 0.022 |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 |
| b | 0.17 | 0.27 | 0.007 | 0.011 |
| С | 0.08 | 0.18 | 0.003 | 0.007 |
| D | 1.50 | 1.70 | 0.059 | 0.067 |
| E | 1.10 | 1.30 | 0.043 | 0.051 |
| е | 0.9 | 50 | 0.020 | |
| HE | 1.50 | 1.70 | 0.059 | 0.067 |
| L | 0.10 | 0.30 | 0.004 | 0.012 |
| Lp | _ | 0.35 | 1 | 0.014 |
| х | _ | 0.10 | _ | 0.004 |
| У | _ | 0.10 | _ | 0.004 |

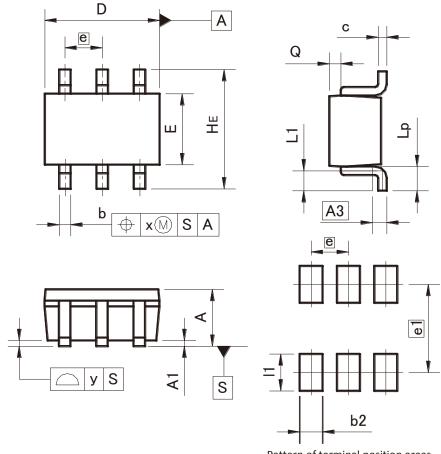
| DIM | MILIMETERS MILIMETERS | | INC | HES |
|-------|-----------------------|------|-----|-------|
| DIIVI | MIN | MAX | MIN | MAX |
| b2 | _ | 0.37 | _ | 0.015 |
| e1 | 1.25 | | 0.0 | 49 |
| l1 | _ | 0.45 | _ | 0.018 |

Dimension in mm / inches

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●Dimensions (Unit : mm)





Pattern of terminal position areas [Not a recommended pattern of soldering pads]

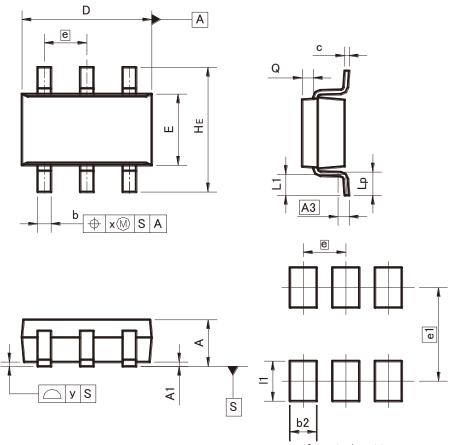
| DIM | MILIMETERS | | INC | HES |
|-----|------------|------|-------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.80 | 1.00 | 0.031 | 0.039 |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 |
| A3 | 0.3 | 25 | 0.0 | 10 |
| b | 0.15 | 0.30 | 0.006 | 0.012 |
| С | 0.10 | 0.20 | 0.004 | 0.008 |
| D | 1.90 | 2.10 | 0.075 | 0.083 |
| E | 1.15 | 1.35 | 0.045 | 0.053 |
| е | 0.0 | 65 | 0.026 | |
| HE | 2.00 | 2.20 | 0.079 | 0.087 |
| L1 | 0.20 | 0.50 | 0.008 | 0.020 |
| Lp | 0.25 | 0.55 | 0.010 | 0.022 |
| Q | 0.10 | 0.30 | 0.004 | 0.012 |
| Х | _ | 0.10 | _ | 0.004 |
| У | _ | 0.10 | _ | 0.004 |

| DIM MILIME | | ETERS | INCHES | |
|------------|------|-------|--------|-------|
| | | MAX | MIN | MAX |
| b2 | _ | 0.40 | _ | 0.016 |
| e1 | 1.55 | | 0.0 | 61 |
| l1 | _ | 0.65 | _ | 0.026 |

Dimension in mm / inches

●Dimensions (Unit : mm)





Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

| DIM | MILIMETERS | | INC | HES |
|-----|------------|------|-------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 1.00 | 1.30 | 0.039 | 0.051 |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 |
| A3 | 0.3 | 25 | 0.0 | 10 |
| b | 0.25 | 0.40 | 0.010 | 0.016 |
| С | 0.09 | 0.25 | 0.004 | 0.010 |
| D | 2.80 | 3.00 | 0.110 | 0.118 |
| E | 1.50 | 1.80 | 0.059 | 0.071 |
| е | 0.9 | 95 | 0.037 | |
| HE | 2.60 | 3.00 | 0.102 | 0.118 |
| L1 | 0.30 | 0.60 | 0.012 | 0.024 |
| Lp | 0.40 | 0.70 | 0.016 | 0.028 |
| Q | 0.20 | 0.30 | 0.008 | 0.012 |
| х | _ | 0.20 | _ | 0.008 |
| У | _ | 0.10 | _ | 0.004 |

| DIM | MILIMETERS | | INC | HES |
|-----|------------|------|-----|-------|
| DIM | MIN | MAX | MIN | MAX |
| b2 | | 0.60 | ı | 0.024 |
| e1 | 2.10 | | 0.0 | 83 |
| l1 | _ | 0.90 | _ | 0.035 |

Dimension in mm / inches

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| Ì | JÁPAN | USA | EU | CHINA |
|---|---------|---------|------------|----------|
| Γ | CLASSⅢ | CLASSII | CLASS II b | CLASSIII |
| Γ | CLASSIV | | CLASSⅢ | |

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 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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