RICOH

R1524N033B-EV

200 mA 36 V Input Ultra Low Supply Current VR Evaluation Board

No. EEV-332-N033B-181225

R1524N033B-EV is the evaluation board for R1524 which has the below features, benefits and specifications.

OUTLINE

The R1524N is an ultra-low supply current voltage regulator featuring 200 mA output current and 36 V input voltage. This device consists of an Output Short-circuit Protection Circuit, an Over-current Protection Circuit, and a Thermal Shutdown Circuit in addition to the basic regulator circuits. The operating temperature range is from −40°C to 105°C, and the maximum input voltage is 36 V. All these features allow the R1524N to become an ideal power source of electric home appliances.

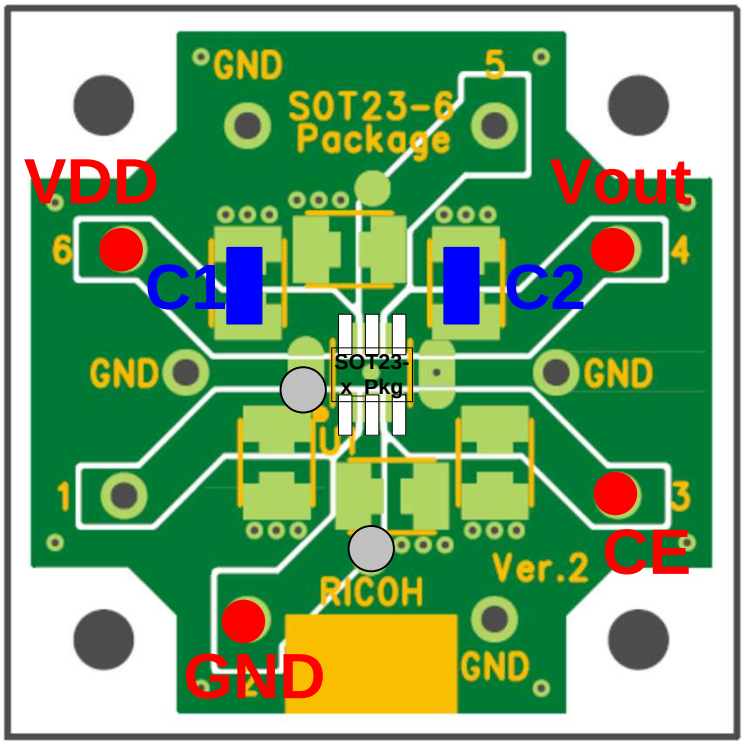
The output voltages are internally fixed at either of the following: 1.8 V, 2.5 V, 2.8 V, 3.0 V, 3.3 V, 3.4 V, 5.0 V, 5.5 V, 6.0 V, 6.4 V, 8.0 V, 8.5 V and 9.0 V. The output voltage accuracy is ±0.6%.

KEY SPECIFICATIONS

- Input Voltage Range (Maximum Rating) ····· 3.5 V to 36 V (50 V)
- Operating Temperature Range ····· –40°C to 105°C
- Supply Current ····· Typ. 2.2 μA
- Standby Current ····· Typ. 0.1 μA
- Dropout Voltage ····· Typ. 0.6 V (I_{OUT} = 200 mA, V_{OUT} = 5.0 V)
- Output Voltage Range ····· 1.8 V / 2.5 V / 2.8 V / 3.0 V / 3.3 V / 3.4V / 5.0 V / 5.5 V / 6.0 V / 6.4 V / 8.0 V / 8.5 V / 9.0 V
  *Contact Ricoh sales representatives for other voltages.
- Output Voltage Accuracy ····· ±0.6% (Ta = 25°C)
- Output Voltage Temperature-Drift Coefficient ····· Typ. ±60 ppm/°C
- Line Regulation ····· Typ. 0.01%/V (V_{SET} + 1 V ≤ V_{IN} ≤ 36 V)
- Built-in Output Short-circuit Protection Circuit ····· Typ. 80 mA
- Built-in Over-current Protection Circuit ····· Typ. 350 mA
- Built-in Thermal Shutdown Circuit ····· Thermal Shutdown Temperature: Typ. 160°C
- Ceramic capacitors are recommended to be used with this device ····· C_{OUT} = 0.1 μF or more
- Package ····· SOT-23-5
- For more details on R1524 IC, please refer to https://www.e-devices.ricoh.co.jp/en/products/power/vr_ldo/r1524/r1524-ea.pdf.

PCB LAYOUT

R1524N (Package: SOT-23-5) PCB Layout



:Jumper

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

Symbol	Item		Rating	Unit
V_{IN}	Input Voltage		−0.3 to 50	V
V_{IN}	Peak Input Voltage(1)		60	V
V_{CE}	Input Voltage (CE Pin)		−0.3 to 50	V
V_{OUT}	Output Voltage		−0.3 to V_{IN} + 0.3 ≤ 50	V
I_{OUT}	Output Current		300	mA
P_D	Power Dissipation(2) (JEDEC STD.51-7 Test Land Pattern)	SOT-23-5	660	mW
Tj	Junction Temperature Range		−40 to 125	°C
Tstg	Storage Temperature Range		−55 to 125	°C

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

RECOMMENDED OPERATING CONDITIONS

Recommended Operating Conditions

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	3.5 to 36	V
Ta	Operating Temperature Range	−40 to 105	°C

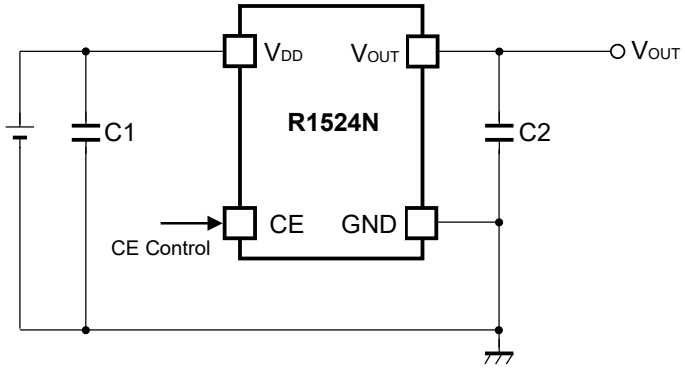
RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

(1) Duration time: 200 ms

(2) Refer to *POWER DISSIPATION* for detailed information.

TYPICAL APPLICATION



R1524N Typical Application

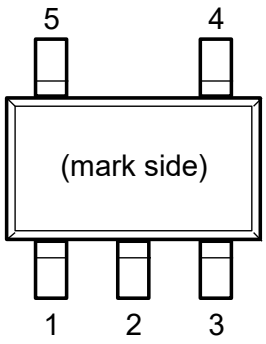
※Although C2 operates even at 0.1 µF, C2 = 10 µF is recommended to improve transient characteristics.

Recommended External Components[*1]

Symbol	Value
C1	0.1 µF
C2	10 µF

[*1] The bill of materials will be attached on the shipment of each purchased evaluation board.

PIN DESCRIPTION



SOT-23-5 Pin Configuration

SOT-23-5 Pin Description

Pin No.	Symbol	Description
1	GND(1)	Ground Pin
2	GND(1)	Ground Pin
3	CE	Chip Enable Pin (Active-high)
4	V_{OUT}	Output Pin
5	V_{DD}	Input Pin

(1) The GND pin must be wired together when it is mounted on board.

TECHNICAL NOTES

Phase Compensation

In the R1524N, phase compensation is provided to secure stable operation even when the load current is varied. For this purpose, make sure to use 0.1 µF or more of a capacitor (C2).

In case of using a tantalum type capacitor and the ESR (Equivalent Series Resistance) value of the capacitor is large, the output might be unstable. Evaluate the circuit including consideration of frequency characteristics.

Connect 0.1 µF or more of a capacitor (C1) between V_{DD} and GND, and as close as possible to the pins.

PCB Layout

For SOT-23-5 package type, wire the following GND pins together: No. 1 and No. 2.

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RoHS Compliant
Halogen Free

Ricoh is committed to reducing the environmental loading materials in electrical devices with a view to contributing to the protection of human health and the environment.
Ricoh has been providing RoHS compliant products since April 1, 2006 and Halogen-free products since April 1, 2012.

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