

### Vishay High Power Products

### Schottky Rectifier, 120 A





Lug terminal anode

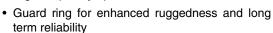
HALF-PAK (D-67)

Base cathode

PRODUCT SUMMARY				
I <sub>F(AV)</sub>	120 A			
$V_{R}$	15 V			

#### **FEATURES**

- 125 °C T<sub>J</sub> operation (V<sub>R</sub> < 5 V)
- · Low forward voltage drop
- High frequency operation



- Lead (Pb)-free
- · Designed and qualified for industrial level

#### **DESCRIPTION**

The 125NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	120	A		
V <sub>RRM</sub>		15	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	10 800	А		
V <sub>F</sub>	120 Apk, T <sub>J</sub> = 125 °C	0.37	V		
T <sub>J</sub>	Range	- 55 to 125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	125NQ015PbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	15	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	25	] v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 74 °C, rectangular waveform		120	
Maximum peak one cycle non-repetitive surge current	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	10 800	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	1700	
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25$ °C, $I_{AS} = 5$ A, $L = 1$ mH		12	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	Α

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# 125NQ015PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	120 A	T <sub>J</sub> = 25 °C	0.43	V
		240 A		0.58	
		120 A	T <sub>J</sub> = 75 °C	0.37	
		240 A		0.52	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	40	mA
See fig. 2		T <sub>J</sub> = 100 °C		2000	IIIA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		7700	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range  Maximum storage temperature range		TJ		- 55 to 125	°C	
		T <sub>Stg</sub>		- 55 to 150		
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	0.38	0.38 0.05	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
				1.06	OZ.	
Manual mini				3 (26.5)		
Mounting torque n	maximum		Non-lubricated threads	4 (35.4)	$\textbf{N}\cdot\textbf{m}$	
Terminal torque —	minimum			3.4 (30)	(lbf $\cdot$ in)	
	maximum			5 (44.2)		
Case style				HALF-PAK module		



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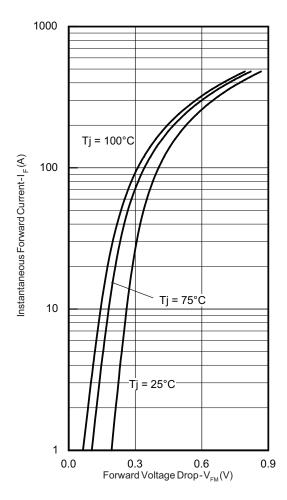


Fig. 1 - Maximum Forward Voltage Drop Characteristics

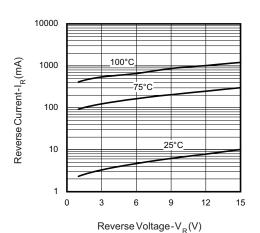


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

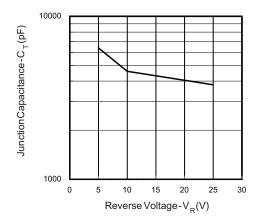


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

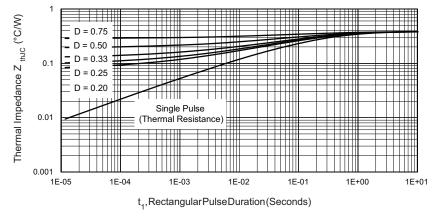


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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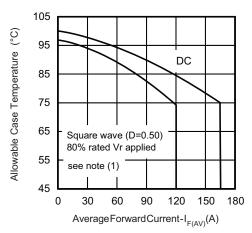


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

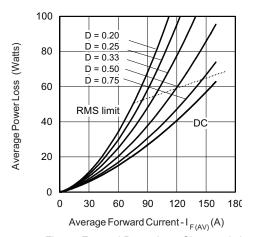


Fig. 6 - Forward Power Loss Characteristics

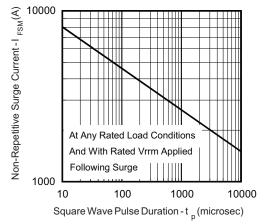


Fig. 7 - Maximum Non-Repetitive Surge Current

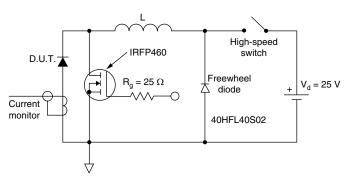


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

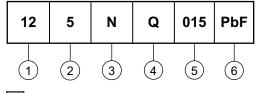
 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_{R} \ (1 - D); \ I_{R} \ \text{at } V_{R1} = \text{Rated } V_{R} \\ \end{array}$ 



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#### **ORDERING INFORMATION TABLE**

Device code



1 - Average current rating (x 10)

2 - Product silicon identification

3 - N = Not isolated

4 - Q = Schottky rectifier diode

5 - Voltage rating (015 = 15 V)

6 - Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95020			

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