



A Product Line of Diodes Incorporated



#### ZXMN6A07F

#### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Product Summary**

V <sub>(BR)DSS</sub>	Max R <sub>DS(on)</sub>	Max I <sub>D</sub> T <sub>A</sub> = 25°C (Note 7)
60V	250mΩ @ V <sub>GS</sub> = 10V	1.4A
	350mΩ @ V <sub>GS</sub> = 4.5V	1.2A

### Description

This MOSFET utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed, making it ideal for high-efficiency power management applications.

# Applications

- DC DC converters
- Power management functions
- Relay and solenoid driving
- Motor control

Features

- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate charge
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP capable (Note 4)

# Mechanical Data

Case: SOT23

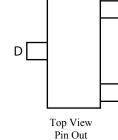
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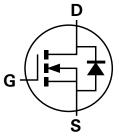
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- Case Material: Molded Plastic, "Green" Molding Compound,
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Weight: 0.008 grams (approximate)



SOT23





Equivalent Circuit

#### Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN6A07FTA	AEC-Q101	7N6	7	8	3000
ZXMN6A07FQTA	Automotive	7N6	7	8	3000

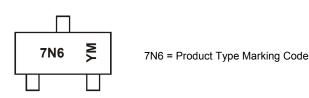
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
 Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</li>

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.

5. For packaging details, go to our website at http://www.diodes.com.

# **Marking Information**







## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	60	V
Gate-Source Voltage			V <sub>GS</sub>	±20	V
Continuous Drain Current	V <sub>GS</sub> = 10V	$T_A = +25^{\circ}C$ (Note 7) $T_A = +70^{\circ}C$ (Note 7) $T_A = +25^{\circ}C$ (Note 6)	ID	1.4 1.1 1.2	A
Pulsed Drain Current (Note 8)			I <sub>DM</sub>	6.9	A
Continuous Source Current (Body Diode) (Note 7)			Is	1	A
Pulsed Source Current (Body Diode) (Note 8)			I <sub>SM</sub>	6.9	A

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation (Note 6) Linear Derating Factor		P <sub>D</sub>	625 5	mW mW/°C
Power Dissipation (Note 7) Linear Derating Factor		P <sub>D</sub>	806 6.4	mW mW/°C
Thermal Resistance, Junction to Ambient	(Note 6)	Р	200	
(No		) R <sub>θJA</sub>	155	°C/W
Thermal Resistance, Junction to Ambient (Note 9)		R <sub>θJL</sub>	194	
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

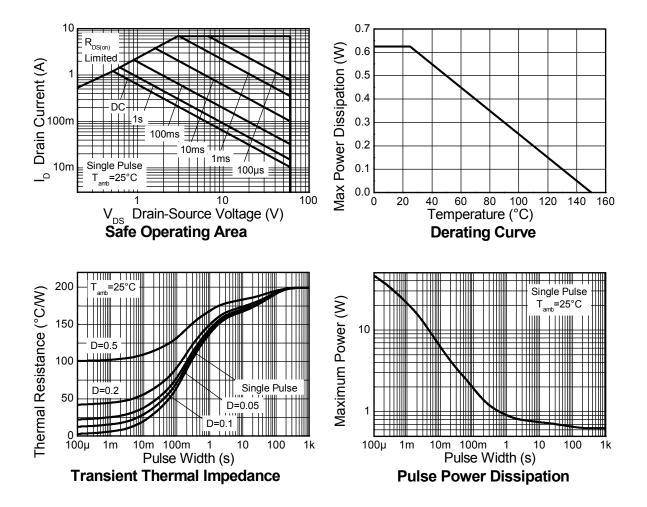
6. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
7. For a device surface mounted on FR4 PCB measured at t ≤5 secs.
8. Repetitive rating 25mm x 25mm FR4 PCB, D=0.02 pulse width=300µs - pulse current limited by maximum junction temperate Notes:

9. Thermal resistance from junction to solder-point (at the end of the drain lead).





## **Thermal Characteristics**





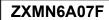
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Electrical Characteristics (@T <sub>A</sub> = +25°C, unless otherwise specified.)							
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS				•			
Drain-Source Breakdown Voltage	<b>BV</b> <sub>DSS</sub>	60	_	_	V	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	_	3.0	V	I <sub>D</sub> = 250μA, V <sub>DS</sub> = V <sub>GS</sub>	
Statia Drain Source On Desistence (Note 10)				0.250		V <sub>GS</sub> = 10V, I <sub>D</sub> = -1.8A	
Static Drain-Source On-Resistance (Note 10)	R <sub>DS (ON)</sub>			0.350	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = -1.3A	
Forward Transconductance (Notes 10 and 12)	<b>g</b> fs		2.3	_	S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 1.8A	
Diode Forward Voltage (Note 10)	V <sub>SD</sub>		0.8	0.95	V	$T_J = +25^{\circ}C, I_S = 0.45A, V_{GS} = 0V$	
Reverse Recovery Time (Note 12)	trr	_	20.5	_	ns	T <sub>J</sub> = +25°C, I <sub>F</sub> = 1.8A, di/dt = 100A/μs	
Reverse Recovery Charge (Note 12)	Qrr		21.3		nC		
DYNAMIC CHARACTERISTICS (Note 12)						-	
Input Capacitance	C <sub>iss</sub>	_	166	_		V <sub>DD</sub> = 40V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance	C <sub>oss</sub>	_	19.5	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	8.7	_			
Turn-On Delay Time (Note 11)	t <sub>D(on)</sub>	_	1.8	_			
Turn-On Rise Time (Note 11)	tr	_	1.4	_	V <sub>DD</sub> = 30V, I <sub>D</sub> = 1.8A,		
Turn-Off Delay Time (Note 11)	t <sub>D(off)</sub>	_	4.9	_	ns	$R_G \cong 6.0 \Omega, \ V_{GS} = 10 V$	
Turn-Off Fall Time (Note 11)	t <sub>f</sub>		2.0				
Total Gate Charge (Note 11)	Qg		1.65		nC	$V_{DS} = 30V, V_{GS} = 5V,$ $I_{D} = 1.8A$	
Total Gate Charge (Note 11)	Qq	_	3.2	_			
Gate-Source Charge (Note 11)	Q <sub>gs</sub>	_	0.67	_	nC	$V_{DS} = 30V, V_{GS} = 10V,$	
Gate-Drain Charge (Note 11)	Q <sub>gd</sub>	_	0.82	_	$I_D = 1.8A$		

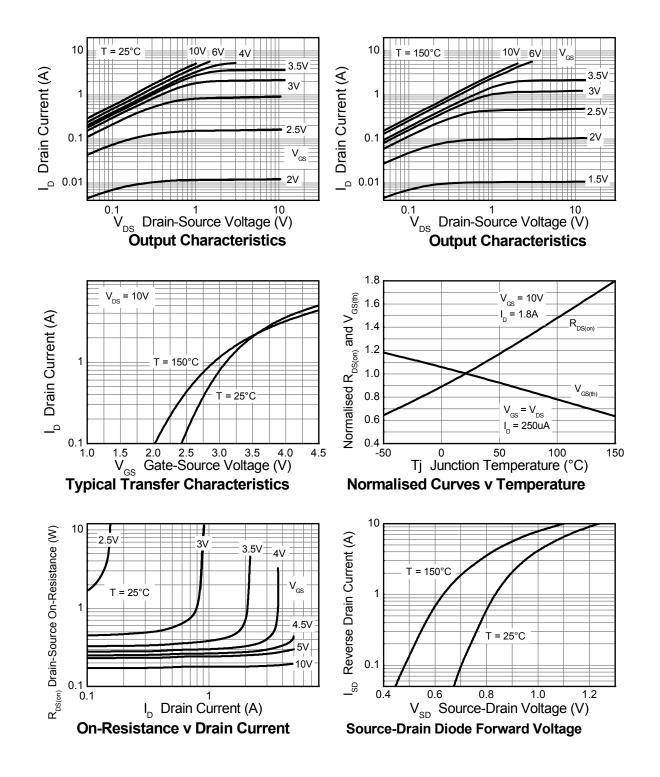
Notes:

Measured under pulsed conditions. Pulse width = 300µs. Duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.
 For design aid only, not subject to production testing.





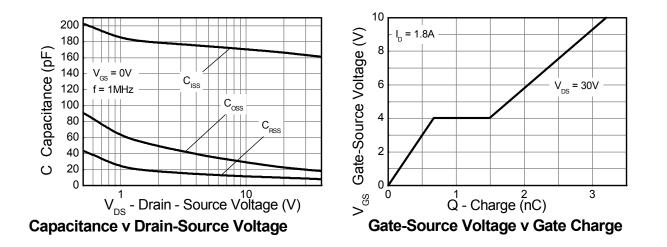
# **Typical Characteristics**



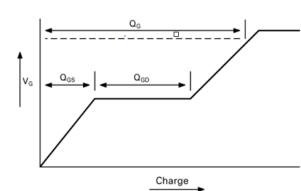




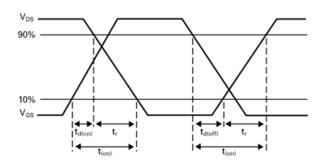
## **Typical Characteristics - continued**



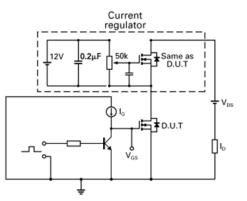
**Test Circuits** 



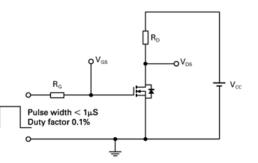
Basic gate charge waveform



Switching time waveforms



Gate charge test circuit



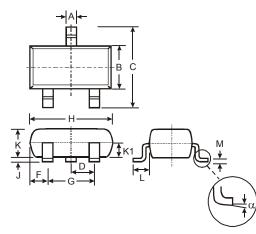
#### Switching time test circuit



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# Package Outline Dimensions

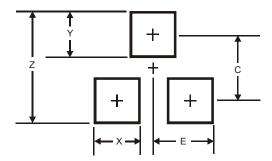
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23					
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
C	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
κ	0.903	1.10	1.00			
K1	-	-	0.400			
L	0.45	0.61	0.55			
М	0.085	0.18	0.11			
α	0°	8°	-			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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