





20mΩ P-Channel SmartSwitch for UMPC Battery Charging Applications

General Description

The AAT4681 SmartSwitch enables separate stand-alone AC adapter and PMU USB chargers to independently control a single low $R_{DS(ON)}$ power MOSFET between battery and system power output. A 20V version is available for multi-cell Li-ion applications and a 6V version is available for single-cell Li-ion applications.

The two P-channel power MOSFETs required in UMPC applications for controlling independent charger ICs can be consolidated to a single device, saving space and reducing cost. The single 20m Ω P-channel device in the AAT4681/-1 has four times lower $R_{\text{DS(ON)}}$ than the equivalent path resistance formed by two series devices.

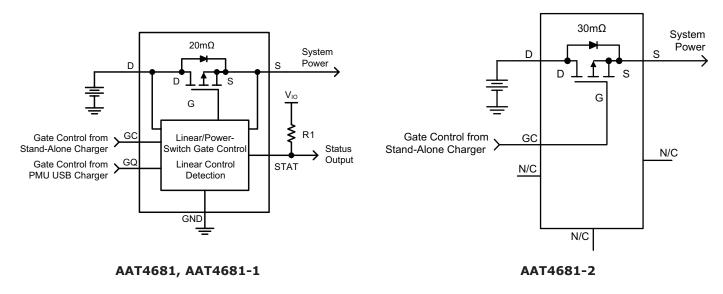
Ordering options are available for multi-cell and singlecell Li-ion versions. For the single-cell application, a 6V device with dual independent gate control is available. For 2-cell and 3-cell applications a 20V ordinary P-channel device is available in the same package and pin configuration. Both devices are available in the TDFN-10L 3mm x 3mm package.

Features

- Multi-Cell 20V Device and Single-Cell 6V Device
- Dual Independent Gate Controls
 - Independent Linear Regulator and SMPS Power Switch States are Maintained
- 3mm x 3mm TDFN-10L package
- Temperature Range: -40°C to 85°C

Applications

- Smart Phones
- Sub Notebooks
- Smartbooks
- Netbooks
- Ultra-Mobile PCs
- Wireless Media Devices



Typical Application



$20m\Omega$ P-Channel SmartSwitch for UMPC Battery Charging Applications

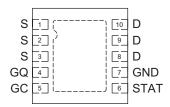
Pin Descriptions

	Pin N	lame		
Pin #	AAT4681/-1	AAT4681-2	Function	
1, 2, 3	S	S	Source connection.	
4	GQ	N/C	Gate control from PMU charger.	
5	GC	GC	Gate control from stand-alone charger.	
6	STAT	N/C	Open drain status output. "STAT" signal "high" means QC is "on" and "STAT" signal low means GQ is "on"	
7	GND	N/C	Ground connection	
8, 9, 10	D	D	Drain connection.	

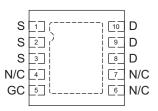
Pin Configuration

TDFN33-10L (Top View)

AAT4681/-1



AAT4681-2





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Absolute Maximum Ratings¹

Symbol	Description	Value	Units	
AAT4681, AAT468	1-1		I	
V_{D} , V_{S}	Drain or Source Voltage to GND		6.0	V
V _{STAT}	STAT to GND		-0.3 to 6.0	V
I _{STAT}	STAT Current	10	mA	
V _{GC} , V _{GQ}	Gate Voltage Levels to GND	-0.3 to 6.0	V	
-	Continuous Drain Current @ T _A = 85°C	AAT4681	±7	A
I_{D}		AAT4681-1	±5	
I _{DM}	Pulsed Drain Current ²	±10	A	
Is	Continuous Source Current (Source-Drain Diode	-1.5	A	
AAT4681-2			·	
V _{DS}	Drain-Source Voltage	-20	V	
V _{GS}	Gate-Source Voltage	±12	V	
Ŧ	Continuous Drain Current	$T_A = 25^{\circ}C$	±4.0	A
I _D		T _A = 70°C	±3.2	А
I _{DM}	Pulsed Drain Current		±24	А
Is	Continuous Source Current (Source-Drain Diode	-1.5	A	

Thermal Characteristics³

Symbol	Description	Value	Units			
T ₁	Operating Junction Temperature Range	-40 to +125	°C			
T _{LEAD}	Maximum Soldering Temperature (at leads, 10 sec.)	300	°C			
TDFN33-10L Thermal Impedance						
θ _{JA}	Maximum Junction-to-Ambient Thermal Resistance 50		°C/W			
P _D	Maximum Power Dissipation ⁴	2	W			

¹ Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

^{2.} Pulse width $<300\mu$ s, duty cycle <1%.

^{3.} T₁ is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: T₁ = T_A + P_D \cdot θ_{JA} .

^{4.} Thermal Resistance is specified with approximately 1 square inch of 1 oz. copper.

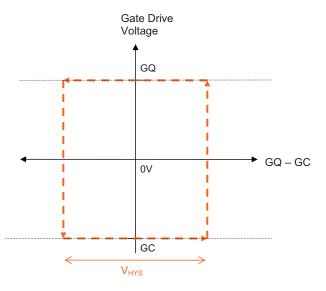


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Electrical Characteristics

 T_A = -40°C to +85°C, unless otherwise noted. Typical values are at T_A = +25°C.

Symbol	Description	Conditions		Min	Тур	Max	Units		
AAT4681	AAT4681/-1								
V _{SYS}	Input Voltage Range ¹			1.8		5.5	V		
V _{UVLO}	Under-Voltage Lockout	For $V_{SYS} < V_{UVLO}$, GC active			1.4		V		
I_Q	Quiescent Current	$V_{\rm D} = 4.2V, T_{\rm J} = 55^{\circ}{\rm C}$			3.6	15	μA		
I _{DSS}	Drain-Source Leakage Current	$V_{GS} = 0V, V_{DS} = -5.5V, T_1 = 55^{\circ}C$				-5	μA		
D	P-Channel On Resistance ²	$V_{D} = V_{GC} = 4.2V, V_{GQ} = GND, I_{D} = 5A,$	AAT4681		18	25			
R _{DS(on)}		$T_A = 25^{\circ}C$	AAT4681-1		23	28	mΩ		
V _{HYS}	GQ-GC Transition Hysteresis					300	mV		
t _{gsw}	GQ-GC Transition Delay	2-GC Transition Delay Slew rate of QG @ 1ms			10		μs		
V _{STATLOW}	STAT Logic Output Low	TAT Logic Output Low I _{STAT(SINK)} = 1mA			0.025	0.4	V		
I _{STAT(SINK)}	STAT Logic High Leakage Current $V_{STAT} = 5.5V, V_{GC} = 5.5V, V_{GQ} = GND$			0.005	1	μA			
AAT4681-2									
BV _{DSS}	Drain-Source Breakdown Voltage $V_{GS} = 0V$, $I_D = -250\mu A$		-20			V			
R _{DS(ON)}	Drain-Source On-Resistance ² $V_{GS} = -4.5V, I_D = -4.0A$			27	40	mΩ			
I _{D(ON)}	On-State Drain Current	$V_{GS} = -4.5V, V_{DS} = -5V (pulse)^2$		-24			Α		
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = -250 \mu A$			-0.8		V		



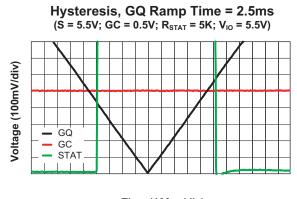
1. Where V_{SYS} is the greater of V_D or V_S .

2. Pulse width < 300µs, duty cycle < 1%.

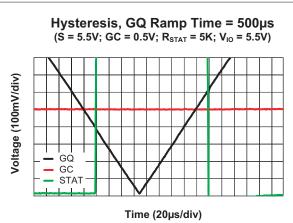


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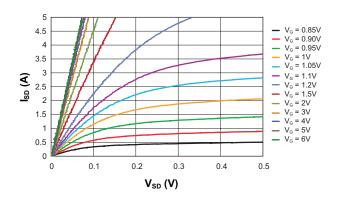
AAT4681/-1 Typical Electrical Characteristics



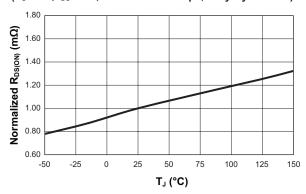
Time (100µs/div)

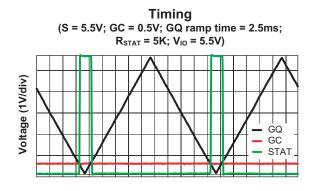


Shutdown Current vs. Shutdown Voltage

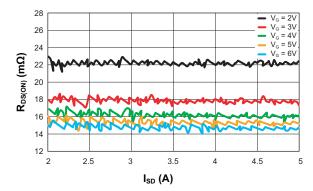


On-Resistance vs. Junction Temperature (Vs = 6V; Ips = 5A; Pulse width <300µs; Duty Cycle < 1%)





Time (1ms/div)



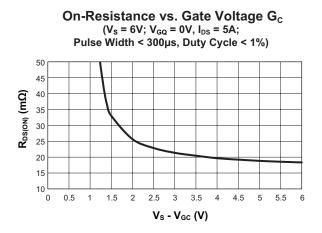
 $R_{DS(ON)}$ vs. I_{SD}

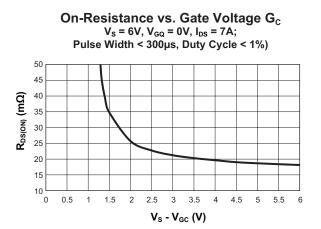
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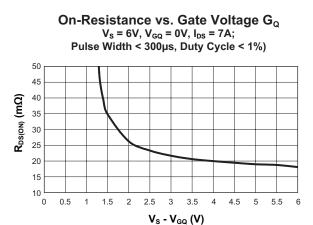
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AAT4681/-1 Typical Electrical Characteristics





On-Resistance vs. Gate Voltage G_c $V_{s} = 6V, V_{GC} = 0V, I_{DS} = 5A;$ Pulse Width < 300µs, Duty Cycle < 1%) 50 45 40 $R_{DS(ON)}$ (m Ω) 35 30 25 20 15 10 0 0.5 1 1.5 2 2.5 3 3.5 4 4.5 5 5.5 6 $V_{s} - V_{GQ} (V)$



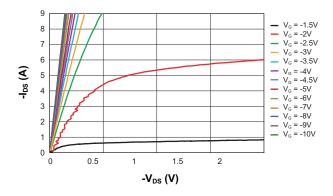
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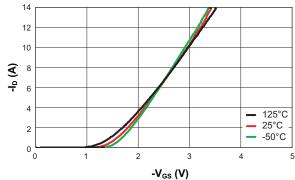
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AAT4681-2 Typical Electrical Characteristics

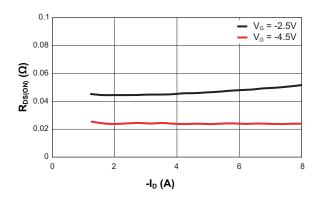
Output Characteristics

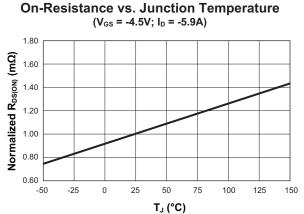


Transfer Characteristics

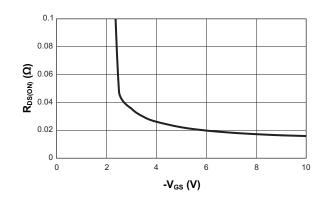


On-Resistance vs. Drain Current

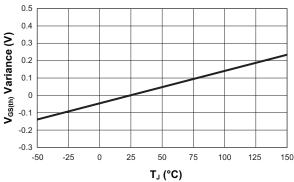




On-Resistance vs. Gate-Source Voltage



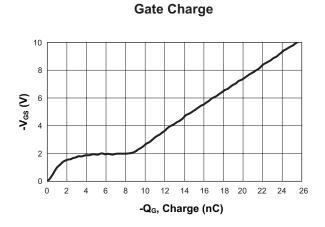
Threshold Voltage vs. Junction Temperature



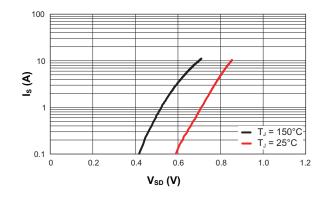


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AAT4681-2 Typical Electrical Characteristics



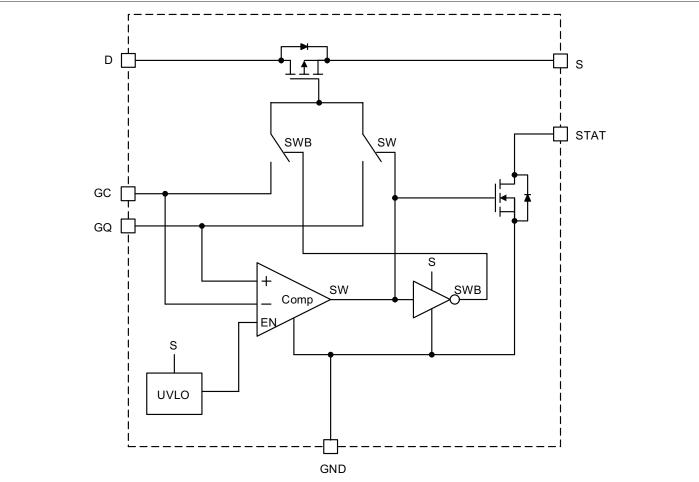
Source-Drain Diode Forward Voltage





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Functional Block Diagram



GC (Gate Control from Stand-Alone Charger)	GQ (Gate Control from PMU USB Charger)	P-Ch Gate Voltage Control Source
Vin	Vin	GC
Linear	0V	GC
0V*	Linear	GQ
0V	0V	GC
float	float	GC

*Switch to GQ when GQ > GC even if QC is not equal to zero.



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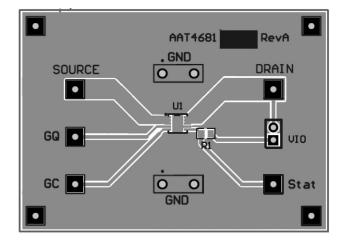


Figure 1: AAT4681IDE Evaluation Board Top Side Layout.

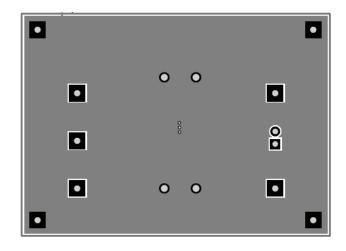


Figure 2: AAT4681IDE Evaluation Board Bottom Side Layout

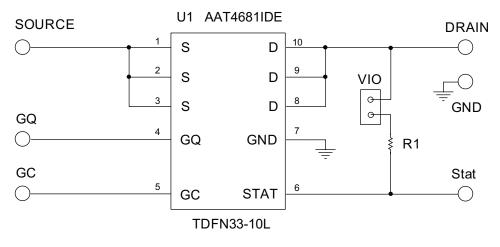


Figure 3: AAT4681IDE Evaluation Board Schematic.



20m P-Channel SmartSwitch for UMPC Battery Charging Applications

Ordering Information

Package	Marking ¹	Continuous Drain Current (A)	Part Number (Tape and Reel) ²
TDFN33-10L	J8XYY	±7.0 ³	AAT4681IDE-T1
TDFN33-10L	F5XYY	±5.0 ³	AAT4681IDE-1-T1
TDFN33-10L	Y4XYY	±3.24	AAT4681IDE-2-T1

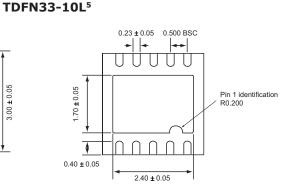


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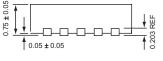
Package Information

Pin 1 dot by marking

Top View



Bottom View





All dimensions in millimeters.

1. XYY = assembly and date code.

5. The leadless package family, which includes QFN, TQFN, DFN, TDFN and STDFN, has exposed copper (unplated) at the end of the lead terminals due to the manufacturing process. A solder fillet at the exposed copper edge cannot be guaranteed and is not required to ensure a proper bottom solder connection.

^{2.} Sample stock is generally held on part numbers listed in **BOLD**.

^{3.} $T_A = 85^{\circ}C.$

^{4.} T_A = 70°C.



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