

Micro Commercial Components



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

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DTA143ZCA

Features

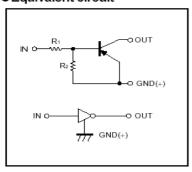
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisure Sensitivity Level 1
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy
- Halogen free available upon request by adding suffix "-HF"
 Absolute maximum ratings @ 25 ℃

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Symbol	Parameter	Min	Тур	Max	Unit
V _{cc}	Supply voltage		-50		V
V_{IN}	Input voltage	-30		5.0	V
I _O I _{C(MAX)}	Output current		-100 -100		mA
P_d	Power dissipation		200		mW
Tj	Junction temperature		150		$^{\circ}$
T _{stg}	Storage temperature	-55		150	$^{\circ}$

Electrical Characteristics @ 25° C

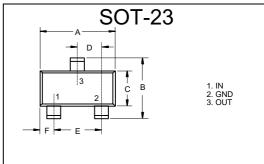
Symbol	Parameter		Тур	Max	Unit
$V_{I(off)}$	Input voltage (V _{CC} =-5V, I _O =-100 μ A)	-	-	-0.5	V
$V_{I(on)}$	$(V_0=-0.3V, I_0=-5mA)$	-1.3			V
$V_{O(on)}$	Output voltage (I _O /I _I =-5mA/-0.25mA			-0.3	V
I _I	Input current (V _I =-5V)			-1.8	mA
I _{O(off)}	Output current (V _{CC} =-50V, V _I =0)			-0.5	μА
G	DC current gain (V _O =-5V, I _O =-10mA)	80			
R ₁	Input resistance	3.29	4.7	6.11	$\mathbf{K} \Omega$
R ₂ /R ₁	Resistance ratio	8.0	10	12	
f⊤	Transition frequency (V _{CE} =-10V, I _E =5mA, f=100MHz)		250		MHz

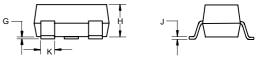
Equivalent circuit



*Marking: E13

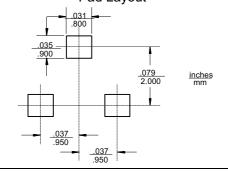
PNP Digital Transistors





DIMENSIONS						
	INCHES		N			
DIM	MIN	MAX	MIN	MAX	NOTE	
Α	.110	.120	2.80	3.04		
В	.083	.104	2.10	2.64		
С	.047	.055	1.20	1.40		
D	.035	.041	.89	1.03		
Е	.070	.081	1.78	2.05		
F	.018	.024	.45	.60		
G	.0005	.0039	.013	.100		
Н	.035	.044	.89	1.12		
J	.003	.007	.085	.180		
K	015	020	37	51		

Suggested Solder Pad Layout



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Electrical characteristic curves

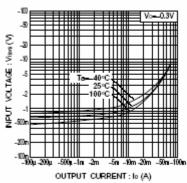


Fig.1 Input voltage vs. output current (ON characteristics)

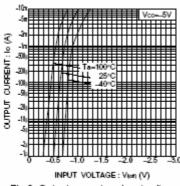


Fig.2 Output current vs. input voltage (OFF characteristics)

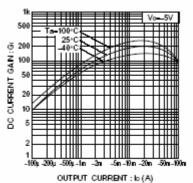


Fig.3 DC current gain vs. output current

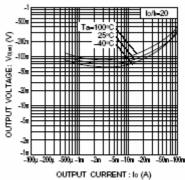


Fig.4 Output voltage vs. output current

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Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel 3Kpcs/Reel

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