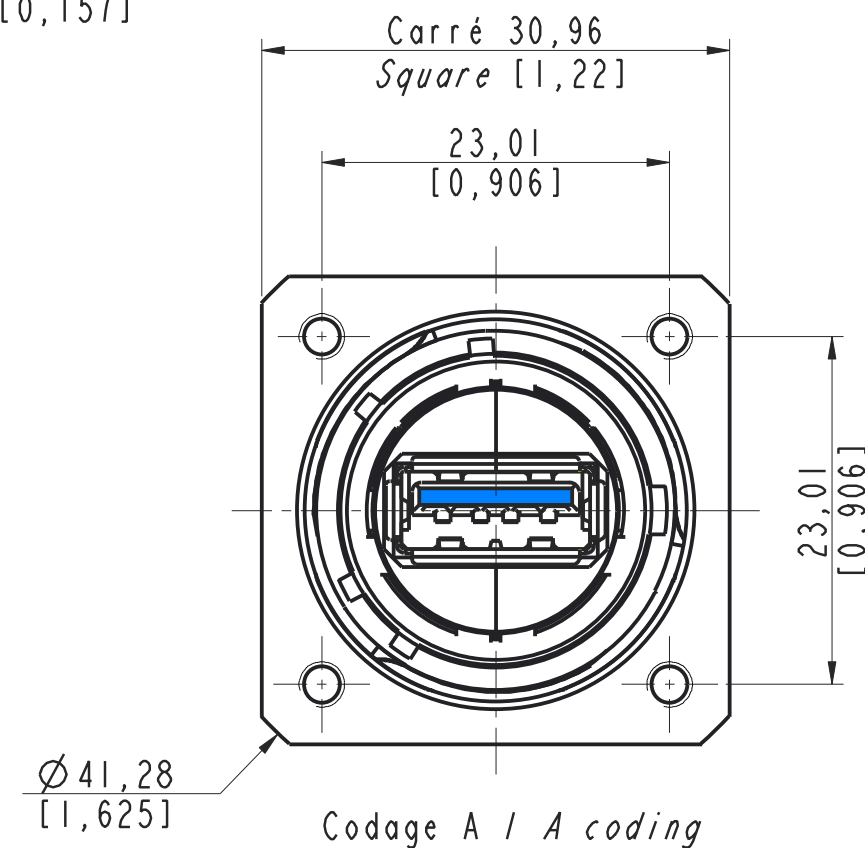
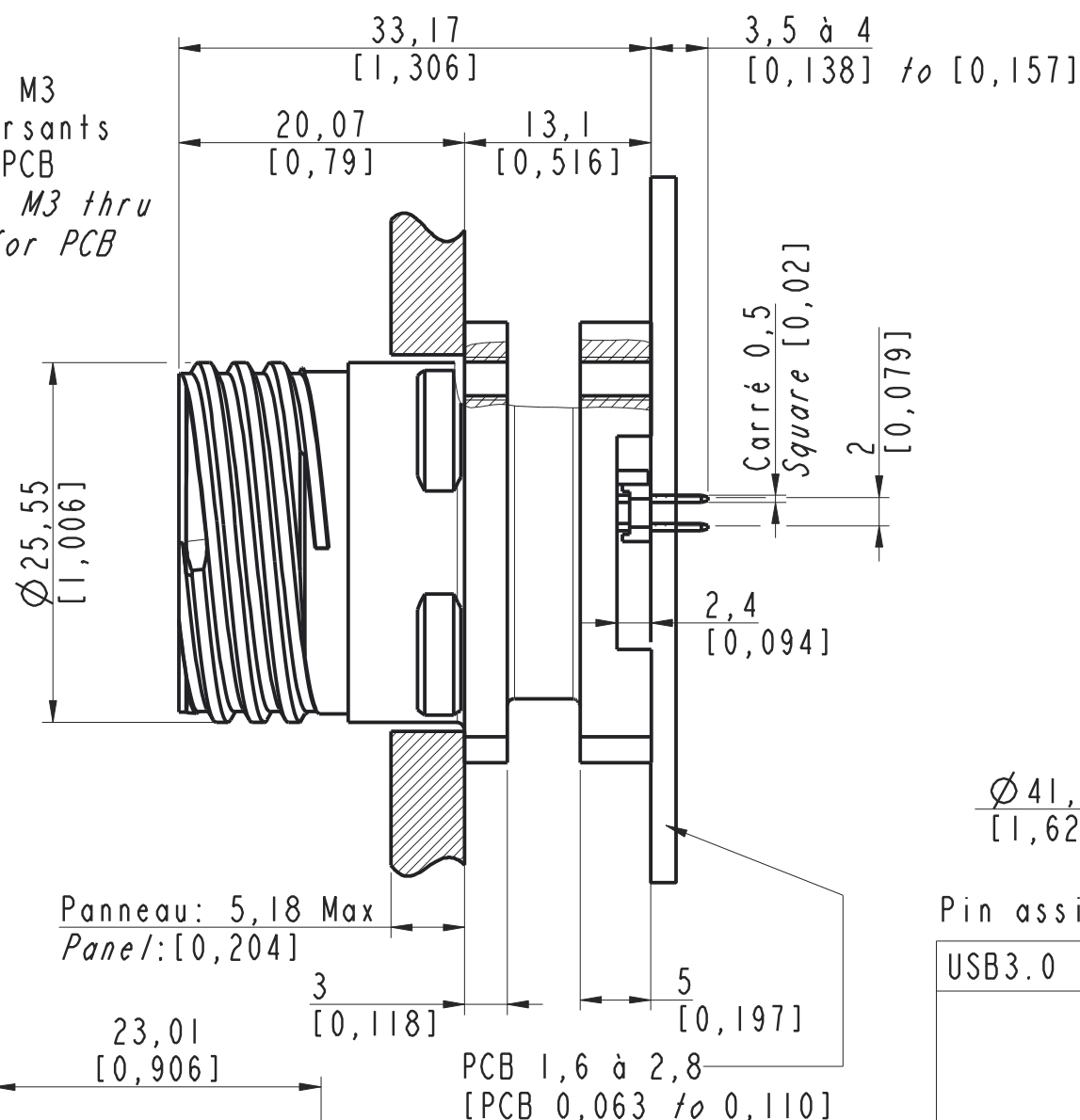
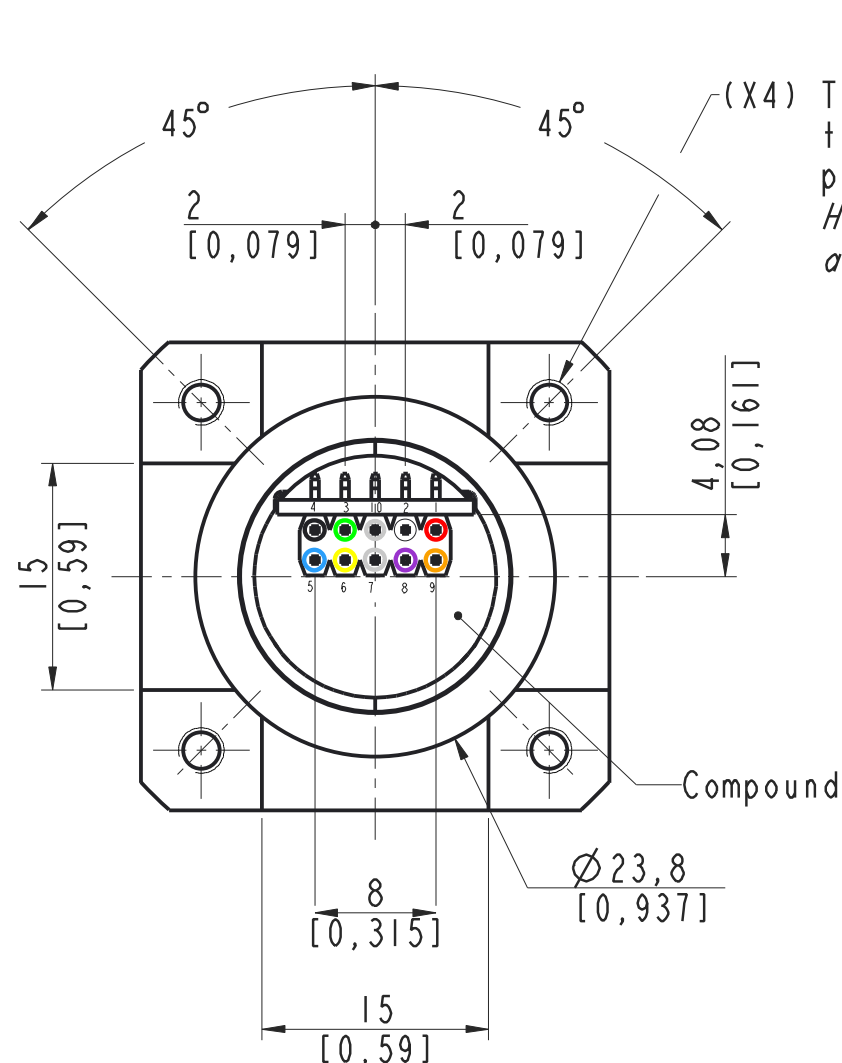
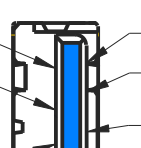


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	Sequence	Pin number	Signal name
 Diagram illustrating the cross-section of a multi-layer PCB with 9 layers, numbered 1 to 9 from top to bottom. The layers are: 1 (Top Copper), 2 (Prepreg), 3 (Core), 4 (Prepreg), 5 (Core), 6 (Prepreg), 7 (Core), 8 (Prepreg), and 9 (Bottom Copper). A central blue vertical bar represents the core. An arrow points to the bottom copper layer with the label 'Shell'.	Second	1	VBUS
	Third	2	D-
		3	D+
	Second	4	GND
	Last	5	StdA_SSRX-
		6	StdA_SSRX+
		7	GND_DRAIN
		8	StdA_SSTX-
		9	StdA_SSTX+
	First	Shell	Shield

G:	Olive drab cadmium plating
N:	Nickel plating

MODIFICATIONS	A	Plan original	ECHELLE
			2
			