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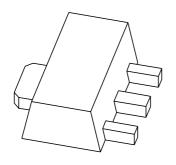
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

# **DISCRETE SEMICONDUCTORS**

# DATA SHEET



PBSS4250X 50 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

Product data sheet Supersedes data of 2003 Jun 17 2004 Nov 08



# 50 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

# **PBSS4250X**

#### **FEATURES**

- SOT89 (SC-62) package
- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability: I<sub>C</sub> and I<sub>CM</sub>
- · Higher efficiency leading to less heat generation
- Reduced printed-circuit board requirements.

### **APPLICATIONS**

- · Power management
  - DC/DC converters
  - Supply line switching
  - Battery charger
  - LCD backlighting.
- · Peripheral drivers
  - Driver in low supply voltage applications (e.g. lamps and LEDs).
  - Inductive load driver (e.g. relays, buzzers and motors).

## **DESCRIPTION**

NPN low  $V_{\text{CEsat}}$  transistor in a SOT89 plastic package. PNP complement: PBSS5250X.

# **MARKING**

TYPE NUMBER	MARKING CODE(1)
PBSS5250X	*1M

# Note

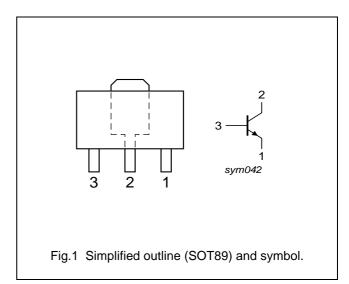
- 1. \* = p: Made in Hong Kong
  - \* = t: Made in Malaysia
  - \* = W: Made in China.

#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
V <sub>CEO</sub>	collector-emitter voltage	50	V
I <sub>C</sub>	collector current (DC)	2	Α
I <sub>CM</sub>	peak collector current	5	Α
R <sub>CEsat</sub>	equivalent on-resistance	160	mΩ

## **PINNING**

PIN	DESCRIPTION
1	emitter
2	collector
3	base



# **ORDERING INFORMATION**

TYPE NUMBER		PACKAGE			
NAME NAME		DESCRIPTION	VERSION		
PBSS4250X	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89		

# 50 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

PBSS4250X

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	50	V
$V_{CEO}$	collector-emitter voltage	open base	-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		-	2	Α
I <sub>CM</sub>	peak collector current	limited by T <sub>j(max)</sub>	_	5	Α
I <sub>B</sub>	base current (DC)		_	0.5	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C			
		note 1	_	550	mW
		note 2	_	1	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

#### **Notes**

- 1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

### THERMAL CHARACTERISTICS

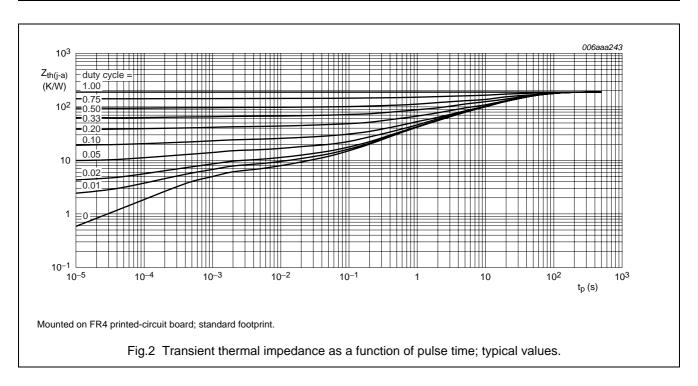
SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air		
		note 1	225	K/W
		note 2	125	K/W
		note 3	90	K/W
		note 4	80	K/W
R <sub>th(j-s)</sub>	thermal resistance from junction to soldering point		16	K/W

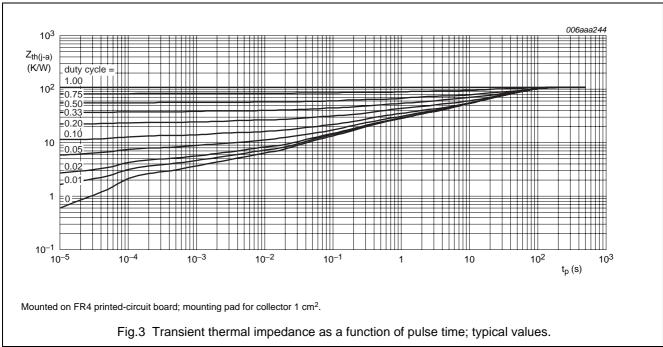
# Notes

- 1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.
- 3. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.
- 4. Device mounted on a ceramic printed-circuit board 7 cm<sup>2</sup>, single-sided copper, tin-plated.

50 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

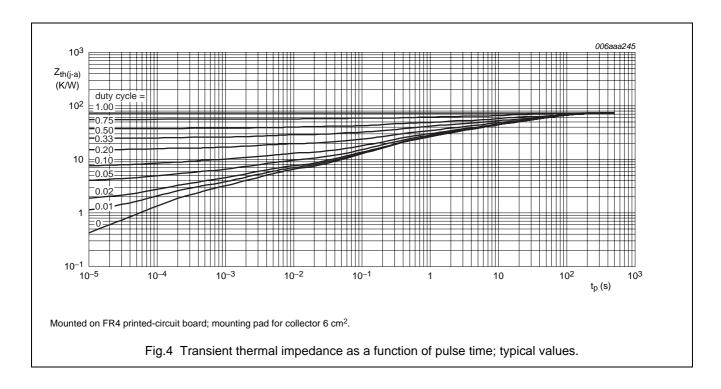
**PBSS4250X** 





# 50 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

**PBSS4250X** 



# 50 V, 2 A NPN low $V_{CEsat}$ (BISS) transistor

PBSS4250X

## **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A	_	100	nA
		$V_{CB} = 50 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	-	50	μΑ
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = 50 V; V <sub>BE</sub> = 0 V	_	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}$	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V			
		I <sub>C</sub> = 0.1 A	300	_	
		I <sub>C</sub> = 0.5 A	300	_	
		I <sub>C</sub> = 1 A; note 1	300	_	
		I <sub>C</sub> = 2 A; note 1	150	_	
V <sub>CEsat</sub>	collector-emitter saturation	I <sub>C</sub> = 0.5 A; I <sub>B</sub> = 50 mA	-	90	mV
	voltage	I <sub>C</sub> = 1 A; I <sub>B</sub> = 50 mA	_	250	mV
		I <sub>C</sub> = 2 A; I <sub>B</sub> = 100 mA	-	380	mV
		I <sub>C</sub> = 2 A; I <sub>B</sub> = 200 mA; note 1	-	320	mV
R <sub>CEsat</sub>	equivalent on-resistance	I <sub>C</sub> = 2 A; I <sub>B</sub> = 200 mA; note 1	_	160	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 2 A; I <sub>B</sub> = 100 mA	_	1.1	V
V <sub>BEon</sub>	base-emitter turn-on voltage	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 1 A	1.1	_	V
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 100 mA; V <sub>CE</sub> = 5 V; f = 100 MHz	100	_	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	_	25	pF

## Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

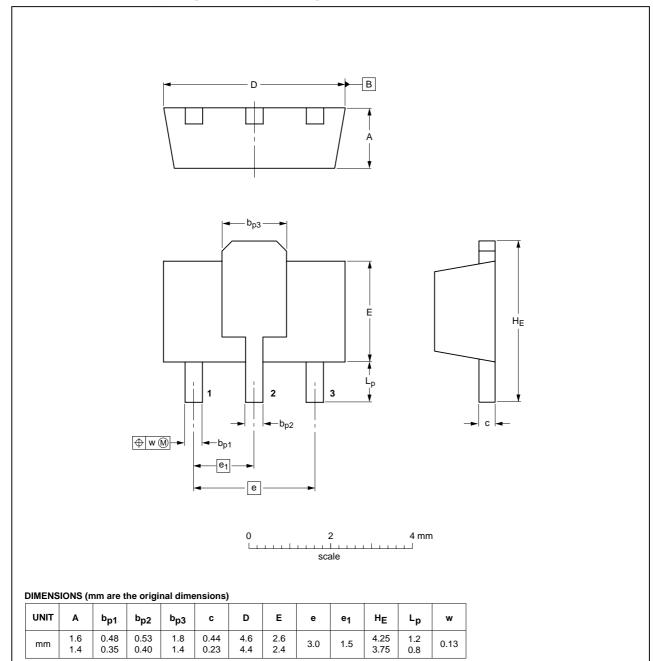
# 50 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

**PBSS4250X** 

## **PACKAGE OUTLINE**

# Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE	OUTLINE REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT89		TO-243	SC-62			<del>04-08-03</del> 06-03-16

# 50 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

PBSS4250X

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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# **NXP Semiconductors**

## **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

### **Contact information**

For additional information please visit: http://www.nxp.com
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