Product data sheet

1. General description

PNP switching transistor in an ultra small DFN1006-3 (SOT883) leadless Surface-Mounted Device (SMD) plastic package.

NPN complement: PMBT2222AM

2. Features and benefits

- High current (max. 600 mA)
- Low voltage (max. 60 V)
- · Leadless ultra small SMD plastic package
- Low package height of 0.50 mm
- Power dissipation comparable to SOT23
- AEC-Q101 qualified

3. Applications

- Switching and linear applications
- · Mobile applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-60	V
I _C	collector current			-	-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-800	mA
h _{FE}	DC current gain	$V_{CE} = -10 \text{ V}; I_{C} = -150 \text{ mA}$	[1]	100	-	300	
		$V_{CE} = -10 \text{ V}; I_{C} = -500 \text{ mA}$	[1]	50	-	-	

[1] Pulsed test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$



60 V, 600 mA PNP switching transistor

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	1 🔲	C
2	Е	emitter	2 3	В—
3	С	collector	Transparent top view	E sym132
			DFN1006-3 (SOT883)	39111132

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMBT2907AM	DFN1006-3	DFN1006-3: leadless ultra small plastic package; 3 solder lands	SOT883			

7. Marking

Table 4. Marking codes

Type number	Marking code
PMBT2907AM	M4

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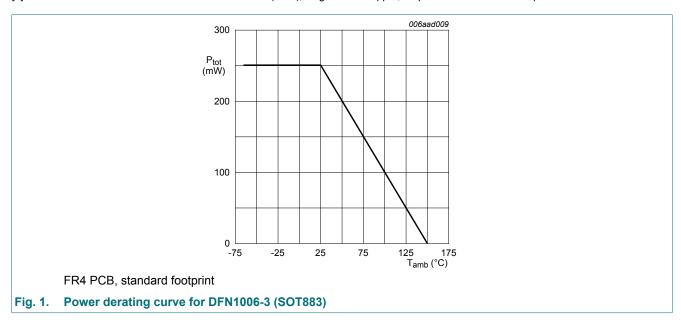
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-60	V
V_{CEO}	collector-emitter voltage	open base		-	-60	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



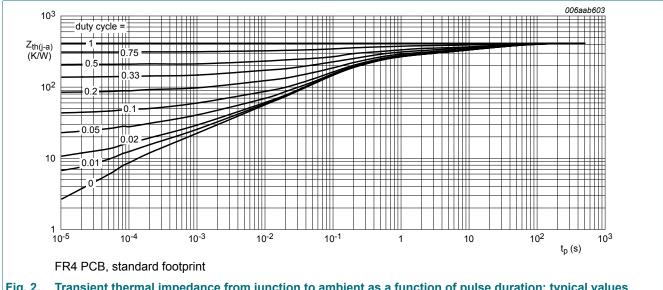
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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

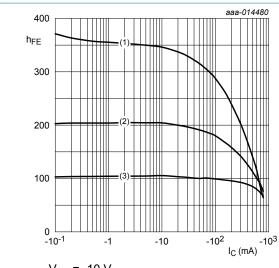
Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-60	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = -2 \text{ mA}; I_B = 0 \text{ A}$		-60	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I _C = 0 A; I _E = -100 μA		-5	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = -50 V; I _E = 0 A		-	-	-10	nA
	current	V _{CB} = -50 V; I _E = 0 A; T _j = 125 °C		-	-	-10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A		-	-	-50	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -100 μA		75	-	-	
		V _{CE} = -10 V; I _C = -1 mA		100	-	-	
		V _{CE} = -10 V; I _C = -10 mA		100	-	-	
		V _{CE} = -10 V; I _C = -150 mA	[1]	100	-	300	
		V _{CE} = -10 V; I _C = -500 mA	[1]	50	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -150 mA; I _B = -15 mA	[1]	-	-	-400	mV
		I _C = -500 mA; I _B = -50 mA	[1]	-	-	-1.6	V
V _{BEsat}	base-emitter saturation voltage	I _C = -150 mA; I _B = -15 mA	[1]	-	-	-1.3	V
		I _C = -500 mA; I _B = -50 mA	[1]	-	-	-2.6	V
t _d	delay time	I _C = -150 mA; I _{Bon} = -15 mA;		-	-	15	ns
t _r	rise time	I _{Boff} = 15 mA		-	-	30	ns
t _{on}	turn-on time			-	-	45	ns
t _s	storage time			-	-	300	ns
t _f	fall time			-	-	65	ns
t _{off}	turn-off time			-	-	365	ns
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ f = 1 MHz		-	-	8	pF
C _e	emitter capacitance	$V_{EB} = -2 \text{ V}; I_C = 0 \text{ A}; I_c = 0 \text{ A}; f = 1 \text{ MHz}$		-	-	30	pF
f _T	transition frequency	$V_{CE} = -20 \text{ V}; I_{C} = -50 \text{ mA}; f = 100 \text{ MHz}$	[1]	-	210	-	MHz

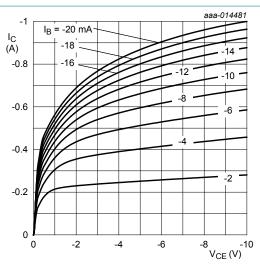
^[1] Pulsed test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$

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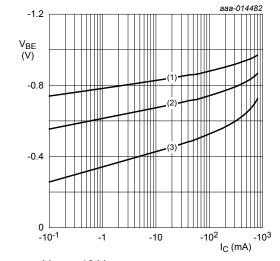
$$(1) T_{amb} = 150 ° ($$

Fig. 3. DC current gain as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 4. Collector current as a function of collectoremitter voltage; typical values



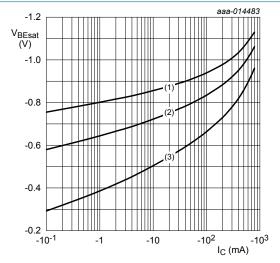
$$V_{CE}$$
 = -10 V

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 5. Base-emitter voltage as a function of collector current; typical values



 $I_{\rm C}/I_{\rm B}=10$

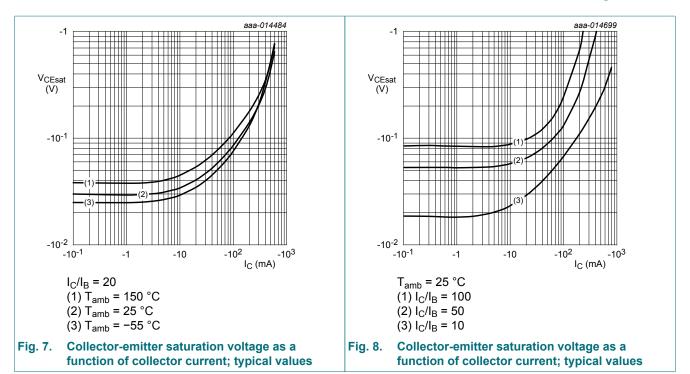
(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25 \, ^{\circ}C$

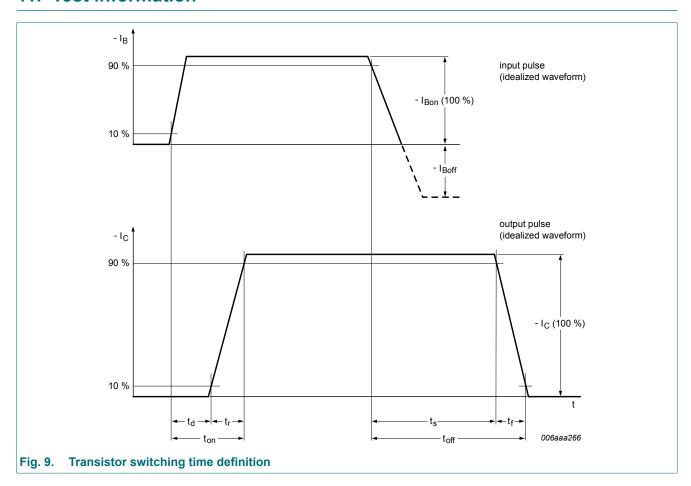
(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

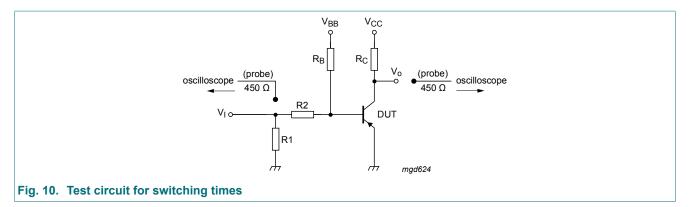
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11. Test information



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Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline

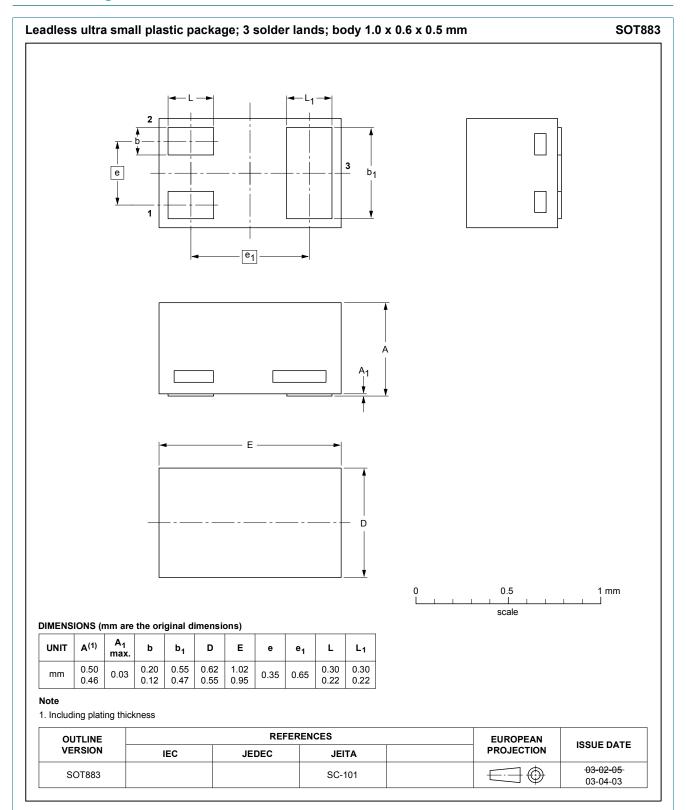
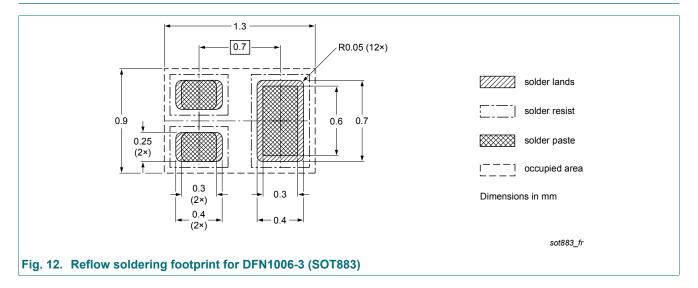


Fig. 11. Package outline DFN1006-3 (SOT883)

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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT2907AM v.1	20180921	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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	Features and benefits

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