

# Schottky Rectifier, 2 A



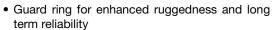


DO-214AC (SMA)

PRODUCT SUMMARY			
Package	DO-214AC (SMA)		
I <sub>F(AV)</sub>	2 A		
$V_{R}$	40 V		
V <sub>F</sub> at I <sub>F</sub>	0.63 V		
I <sub>RM</sub>	26 mA at 125 °C		
T <sub>J</sub> max.	150 °C		
Diode variation	Single die		
E <sub>AS</sub>	3.0 mJ		

#### **FEATURES**

Low forward voltage drop





**FREE** 

- Halogen-free according to IEC 61249-2-21
- definition
- · Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of
- Compliant to RoHS Directive 2002/95/EC

#### **DESCRIPTION**

The VS-20MQ040-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	2	А		
$V_{RRM}$		40	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	120	Α		
V <sub>F</sub>	2 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.63	V		
T <sub>J</sub>	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-20MQ040-M3	UNITS	
Maximum DC reverse voltage	$V_{R}$	40	V	
Maximum working peak reverse voltage	$V_{RWM}$	40	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4		50 % duty cycle at T <sub>C</sub> = 110 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		2.1	А
		50 % duty cycle at $T_C$ = 112 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		2	A
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated	120	Α
See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	30	A
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_{J} = 25  ^{\circ}\text{C},  I_{AS} = 1  \text{A},  L = 6  \text{mH}$		3	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		А	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		2 A		0.69	V
		1.5 A	T <sub>J</sub> = 25 °C	0.62	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1 A		0.54	
See fig. 1	V FM (1)	2 A		0.63	
		1.5 A	T <sub>J</sub> = 125 °C	0.56	
		1 A		0.49	
Maximum reverse leakage current	,	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.5	- mA
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C		26	
Threshold voltage	V <sub>F(TO)</sub>	T <sub>.I</sub> = T <sub>.I</sub> maximum		0.36	V
Forward slope resistance	r <sub>t</sub>			104	mΩ
Typical junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 10 V <sub>DC</sub> , T <sub>J</sub> = 25 °C, test signal = 1 MHz		38	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/ <sub>I</sub>		V/µs	

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width = 300  $\mu s,$  duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (similar D-64)	2	F

### Note



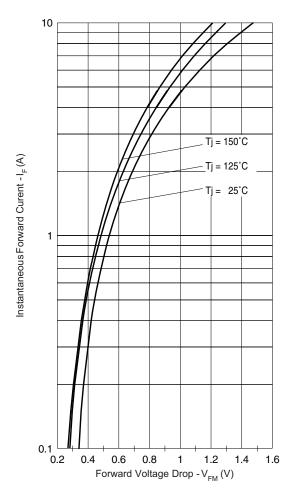


Fig. 1 - Maximum Forward Voltage Drop Characteristics

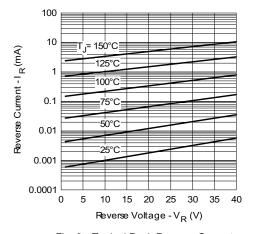


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

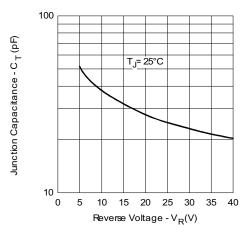


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

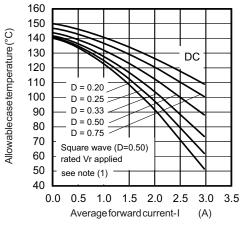


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

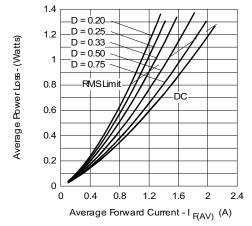


Fig. 5 - Maximum Average Forward Dissipation vs.
Average Forward Current

#### Note

(1) Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

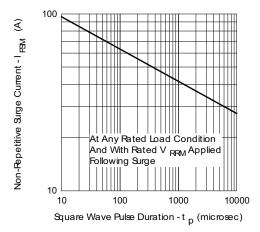
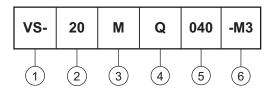


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product suffix

2 - Current rating

3 - M = SMA

4 - Q = Schottky "Q" series

5 - Voltage rating (040 = 40 V)

6 - Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE   MINIMUM ORDER QUANTITY   PACKAGING DESCRIPTION					
VS-20MQ040-M3/5AT	5AT	7500	13" diameter plastic tape and reel			

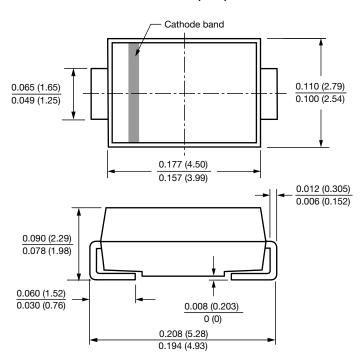
LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95400</u>			
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



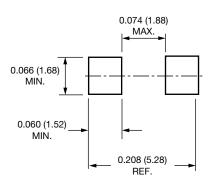
## **SMA**

### **DIMENSIONS** in inches (millimeters)

### **DO-214AC (SMA)**



### Mounting Pad Layout





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Vishay

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